### **CYStech Electronics Corp.**

Spec. No.: C775LG Issued Date: 2010.03.26 Revised Date:

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# General Purpose Plastic Rectifiers Reverse Voltage 50V to 1000V Forward Current 2.0 Amperes

## **RL201 thru RL207**



### **Features**

- Low cost
- Low leakage
- Low forward voltage drop
- High current capability
- The plastic material carries UL recognition 94V-0
- Tj is 150°C (max) and TsTG is 175°C (max) with PI glue

#### **Mechanical Characteristics:**

•Case: JEDEC DO-204AC(DO-15) molded plastic

•Terminals: Tin plated axial leads, solderable per MIL-STD-202E, method 208C

•Polarity: Color band denotes cathode

Mounting position: AnyWeight: 0.014oz., 0.39grams

### **Maximum Ratings and Electrical Characteristics**

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

Parameter		Symbol	Туре							TT
			RL201	RL202	RL203	RL204	RL205	RL206	RL207	Units
Maximum Repetitive peak reverse voltage		Vrrm	50	100	200	400	600	800	1000	V
Maximum RMS voltage		$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC blocking voltage		$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum instantaneous forward voltage IF=2A		$V_{\rm F}$	1.1							V
Maximum average forward rectified current, 0.375"(9.5mm) lead length at T <sub>A</sub> =50°C		I <sub>F</sub> (AV)	2							A
Peak forward surge current @8.3ms single half sine wave superimposed on rated load (JEDEC method)		Ifsm	70							A
Maximum DC reverse current	Γ <sub>A</sub> =25°C	T <sub>n</sub>	5							μΑ
at rated DC blocking voltage	Γ <sub>A</sub> =100°C	Ir	50							
Maximum full load reverse current full cycle Average @ .375"(9.5mm) lead length at T <sub>L</sub> =75 °C		Ir(AV)	30							
Typical junction capacitance (Note 1)		CJ	20							pF
Typical Thermal Resistance		$R_{\theta JA}$	40						•	°C/W
Storage temperature range		Tstg	<b>-</b> 50 ∼ +125							$^{\circ}\!\mathbb{C}$
Operating junction temperature range		TJ	<b>-</b> 50 ∼ +150							$^{\circ}\mathbb{C}$

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0VDC

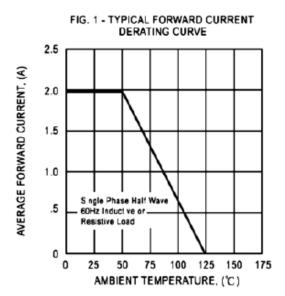


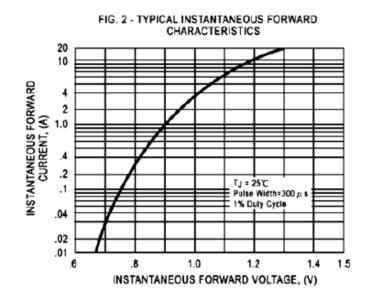
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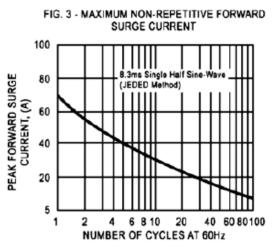
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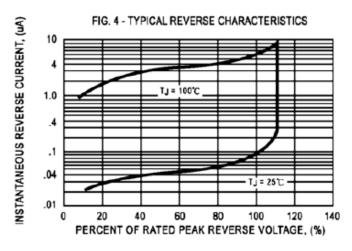
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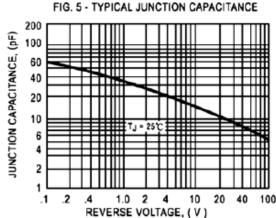
### **Ratings and Characteristic Curves**











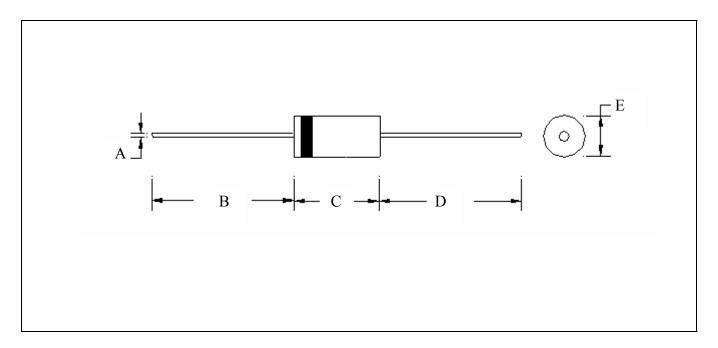


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### DO-204AC(DO-15) Dimension



### \*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.	DIIVI	Min.	Max.	Min.	Max.
Α	φ0.028	φ0.034	φ0.70	φ0.90	D	1.000	-	25.40	-
В	1.000	-	25.40	-	Е	φ0.104	φ0.140	φ2.60	φ3.60
С	0.2300	0.3000	5.80	7.60					

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

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