

P-Channel Enhancement Mode MOSFET

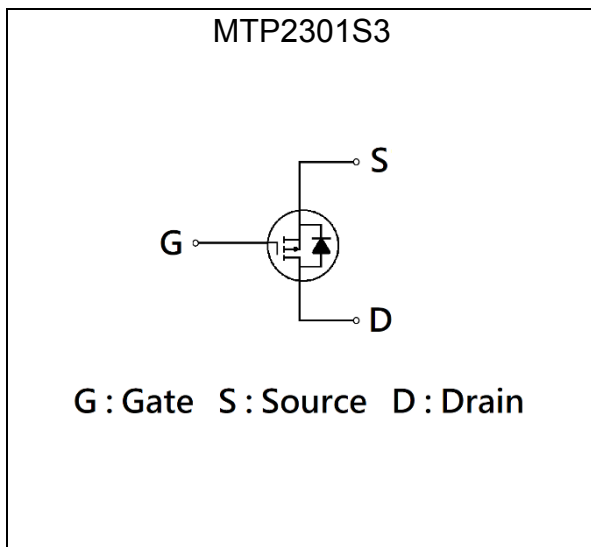
MTP2301S3

Features

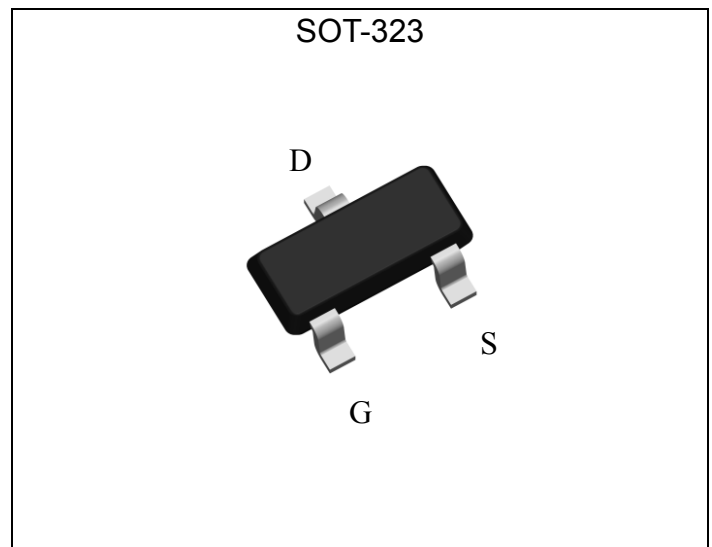
- Advanced trench process technology
- High density cell design for ultra low on resistance
- Excellent thermal and electrical capabilities
- Compact and low profile SOT-323 package

BV_{DSS}	-20V
$I_D@V_{GS}=10V, T_A=25^\circ C$	-1.6A
$R_{DS(ON)}@V_{GS}=-4.5V, I_D=-1.6A$	85mΩ
$R_{DS(ON)}@V_{GS}=-2.5V, I_D=-1A$	105mΩ

Equivalent Circuit

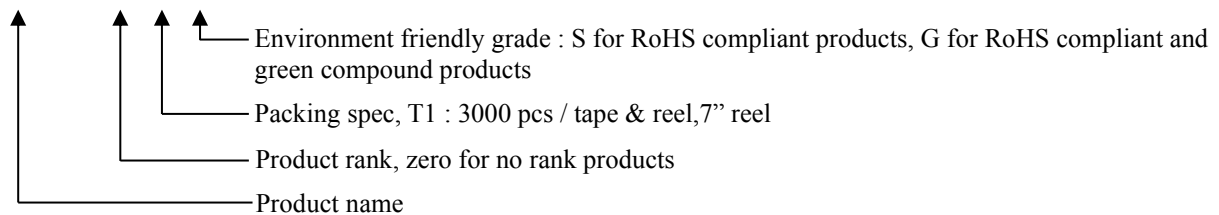


Outline



Ordering Information

Device	Package	Shipping
MTP2301S3-0-T1-G	SOT-323 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



**Absolute Maximum Ratings (T_A=25°C)**

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	-20	V	
Gate-Source Voltage	V _{GS}	±8		
Continuous Drain Current @ V _{GS} =10V, T _A =25°C	I _D	-1.6	A	
Continuous Drain Current @ V _{GS} =10V, T _A =70°C		-1.3		
Pulsed Drain Current	I _{DM}	-10		
Continuous Body Diode Forward Current @ T _A =25°C	I _S	-1.6		
Avalanche Current @ L=0.1mH	I _{AS}	-9		
Avalanche Energy @ L=0.5mH	E _{AS}	6.3	mJ	
Total Power Dissipation	P _D	T _A =25°C	340	mW
		T _A =70°C	218	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C	

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	R _{θJA}	367	°C/W

Note:

*a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

*b. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.



Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-20	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-0.45	-	-1		V _{DS} =V _{GS} , I _D =-250μA
G _{FS}	-	5.5	-	S	V _{DS} =-5V, I _D =-1.6A
I _{GSS}	-	-	±100	nA	V _{GS} =±8V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-16V, V _{GS} =0V
R _{DS(ON)}	-	85	110	mΩ	V _{GS} =-4.5V, I _D =-1.6A
	-	105	150		V _{GS} =-2.5V, I _D =-1A
Dynamic					
C _{iss}	-	600	-	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz
C _{oss}	-	60	-		
C _{rss}	-	52	-		
R _g	-	14	-	Ω	f=1MHz
Q _g *1, 2	-	8	-	nC	V _{DS} =-10V, I _D =-1.6A, V _{GS} =-4.5V
Q _{gs} *1, 2	-	1	-		
Q _{gd} *1, 2	-	1.7	-		
t _{d(ON)} *1, 2	-	4	-	ns	V _{DS} =-10V, I _D =-1A, V _{GS} =-4.5V, R _{GS} =3.3Ω
t _r *1, 2	-	20	-		
t _{d(OFF)} *1, 2	-	50	-		
t _f *1, 2	-	5.2	-		
Source-Drain Diode					
V _{SD} *1	-	-0.85	-1.2	V	I _S =-1.6A, V _{GS} =0V
t _{rr}	-	6	-	ns	I _F =-1.6A, dI _F /dt=100A/μs
Q _{rr}	-	1.7	-	nC	

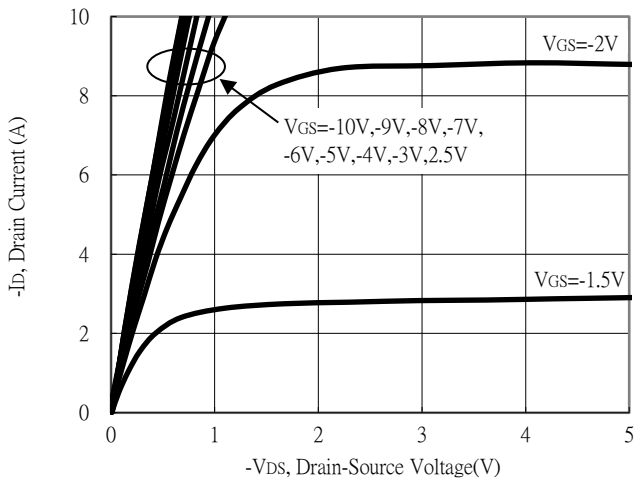
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

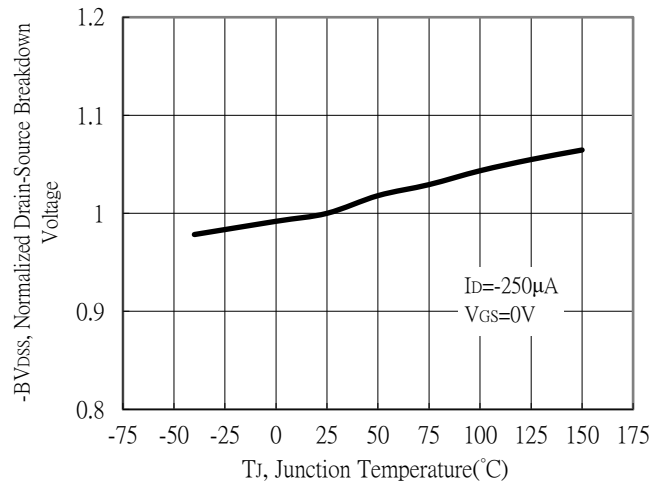
*2. Independent of operating temperature

Typical Characteristics

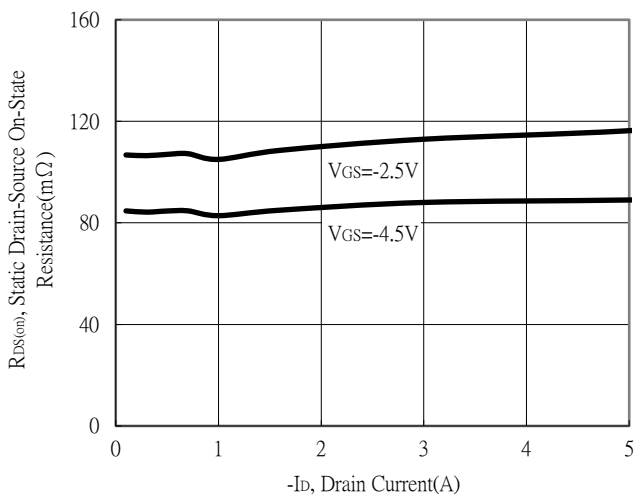
Typical Output Characteristics



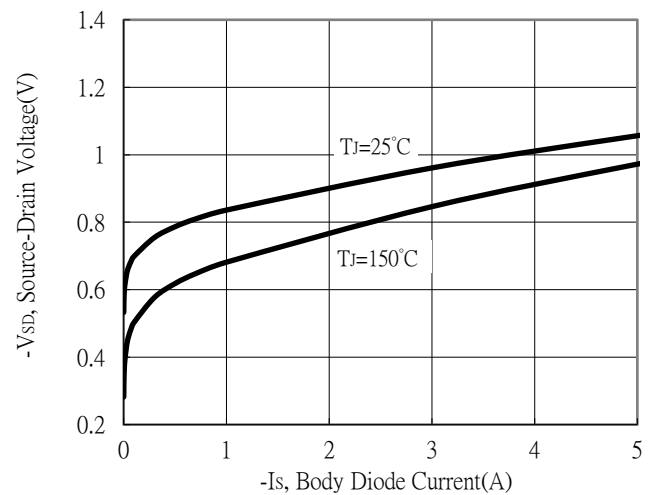
Breakdown Voltage vs Ambient Temperature



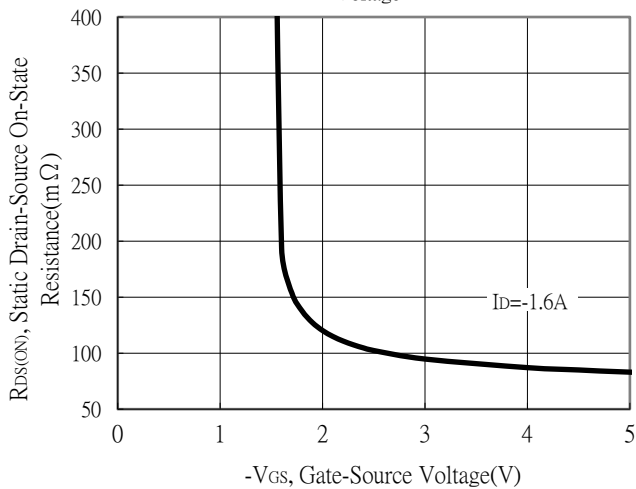
Static Drain-Source On-State resistance vs Drain Current



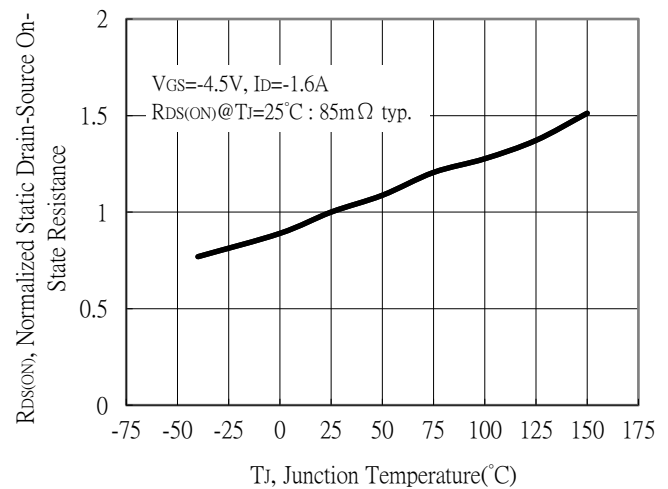
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

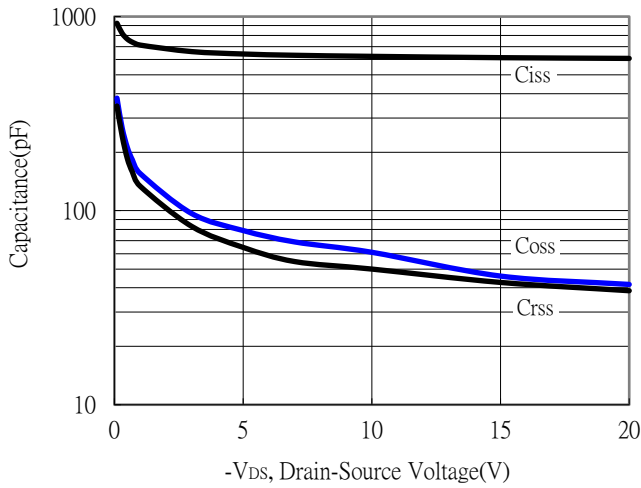


Drain-Source On-State Resistance vs Junction Temperature

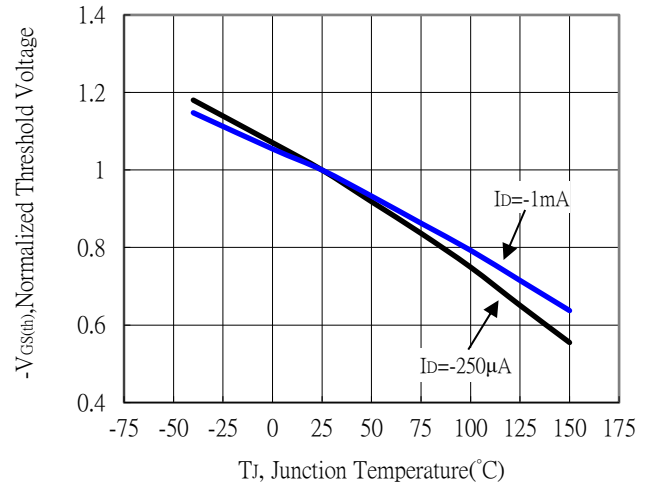


Typical Characteristics (Cont.)

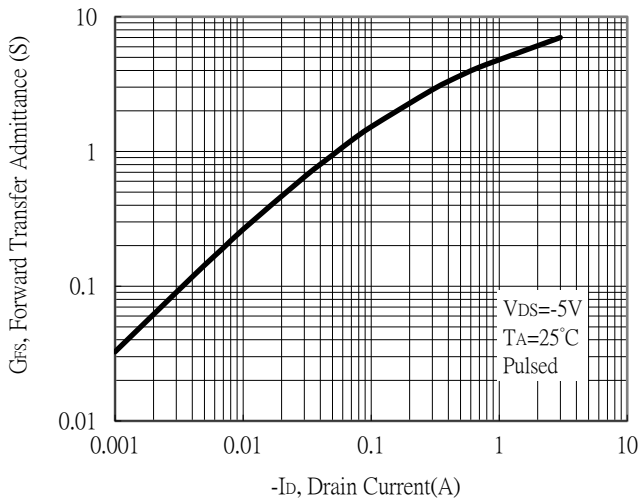
Capacitance vs Drain-to-Source Voltage



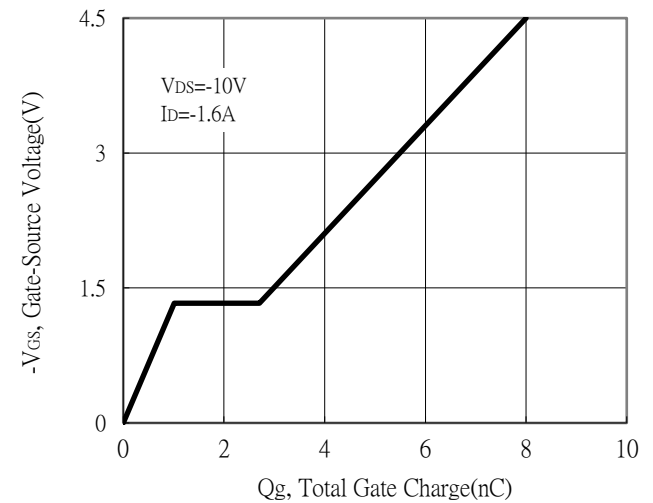
Threshold Voltage vs Junction Temperature



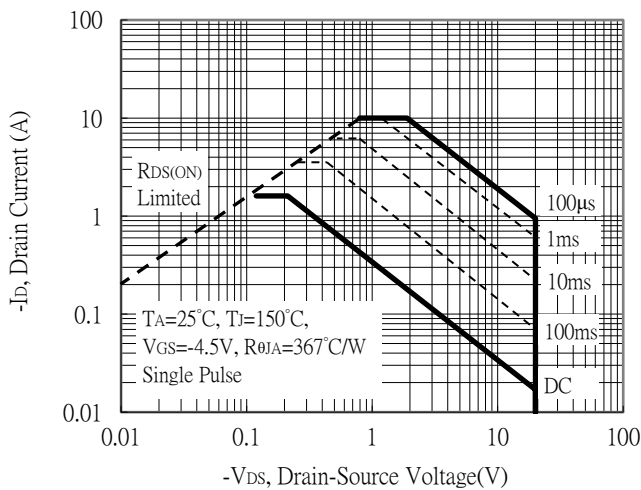
Forward Transfer Admittance vs Drain Current



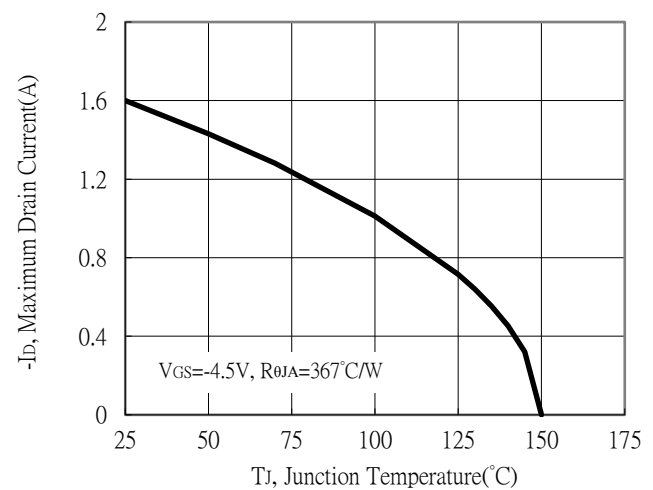
Gate Charge Characteristics



Maximum Safe Operating Area

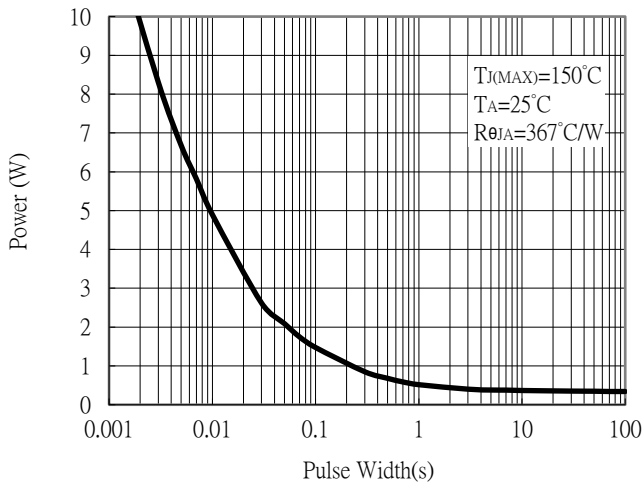


Maximum Drain Current vs Junction Temperature

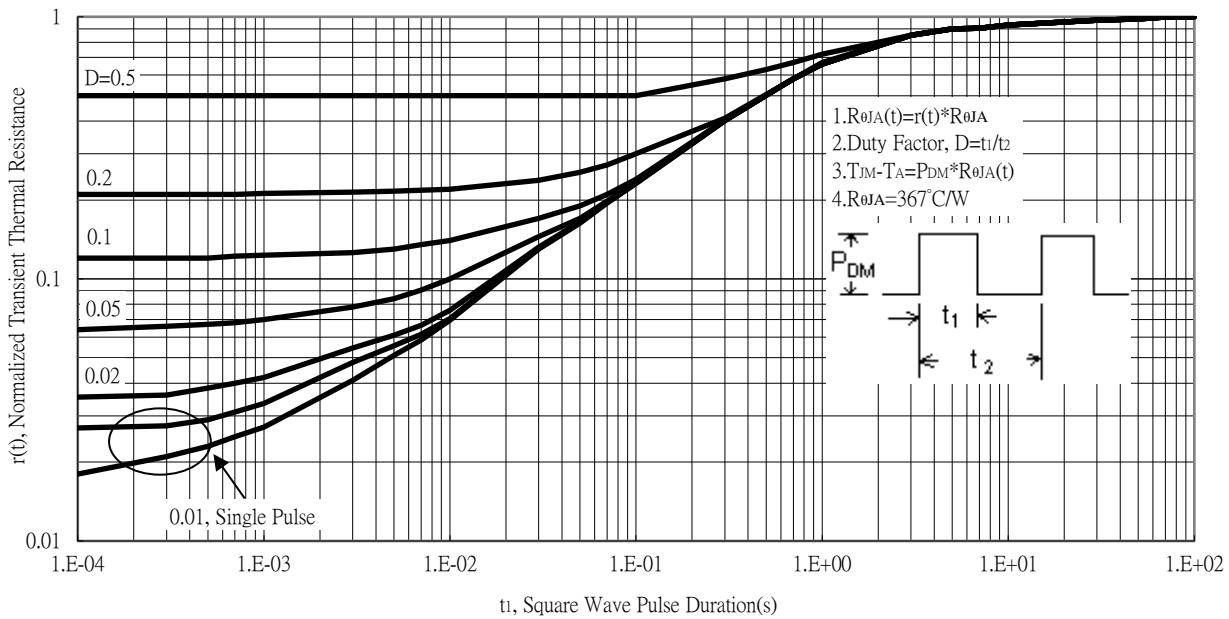


Typical Characteristics (Cont.)

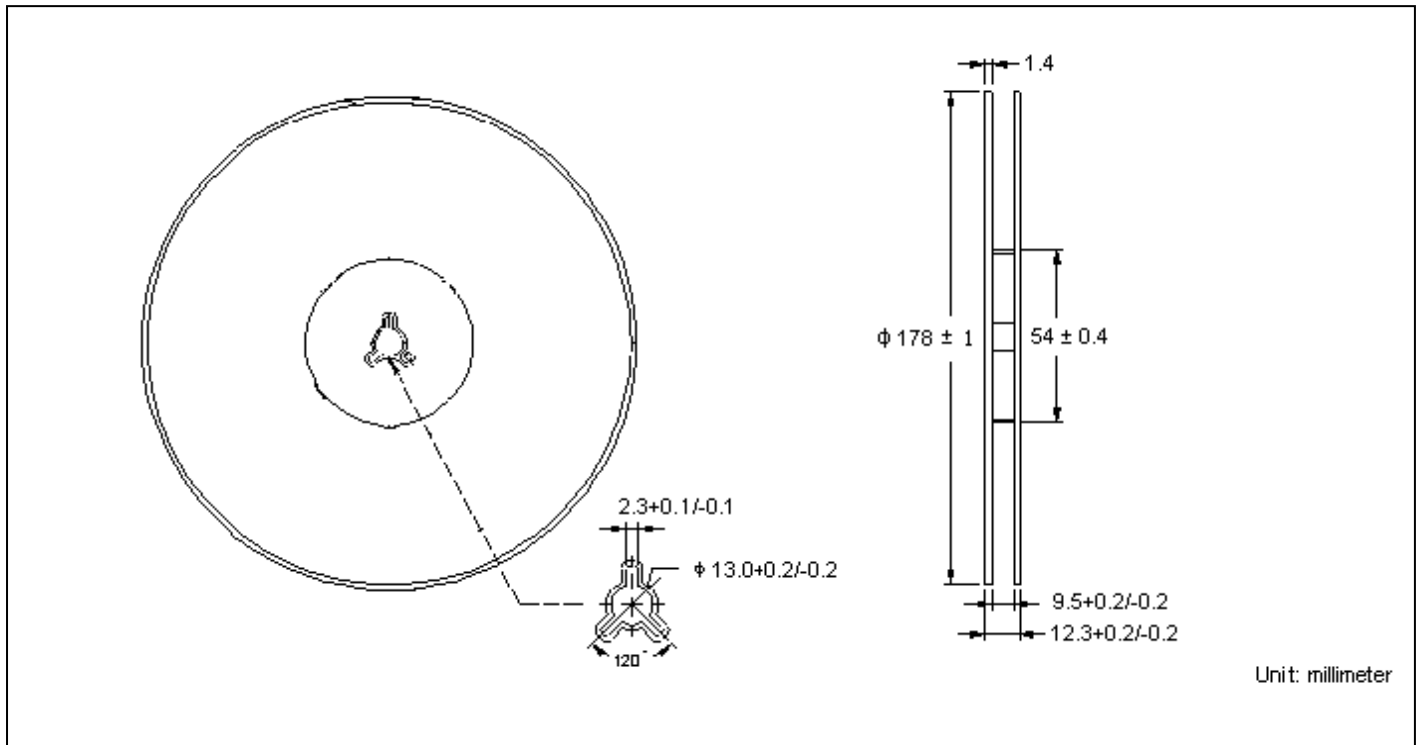
Single Pulse Power Rating, Junction to Ambient



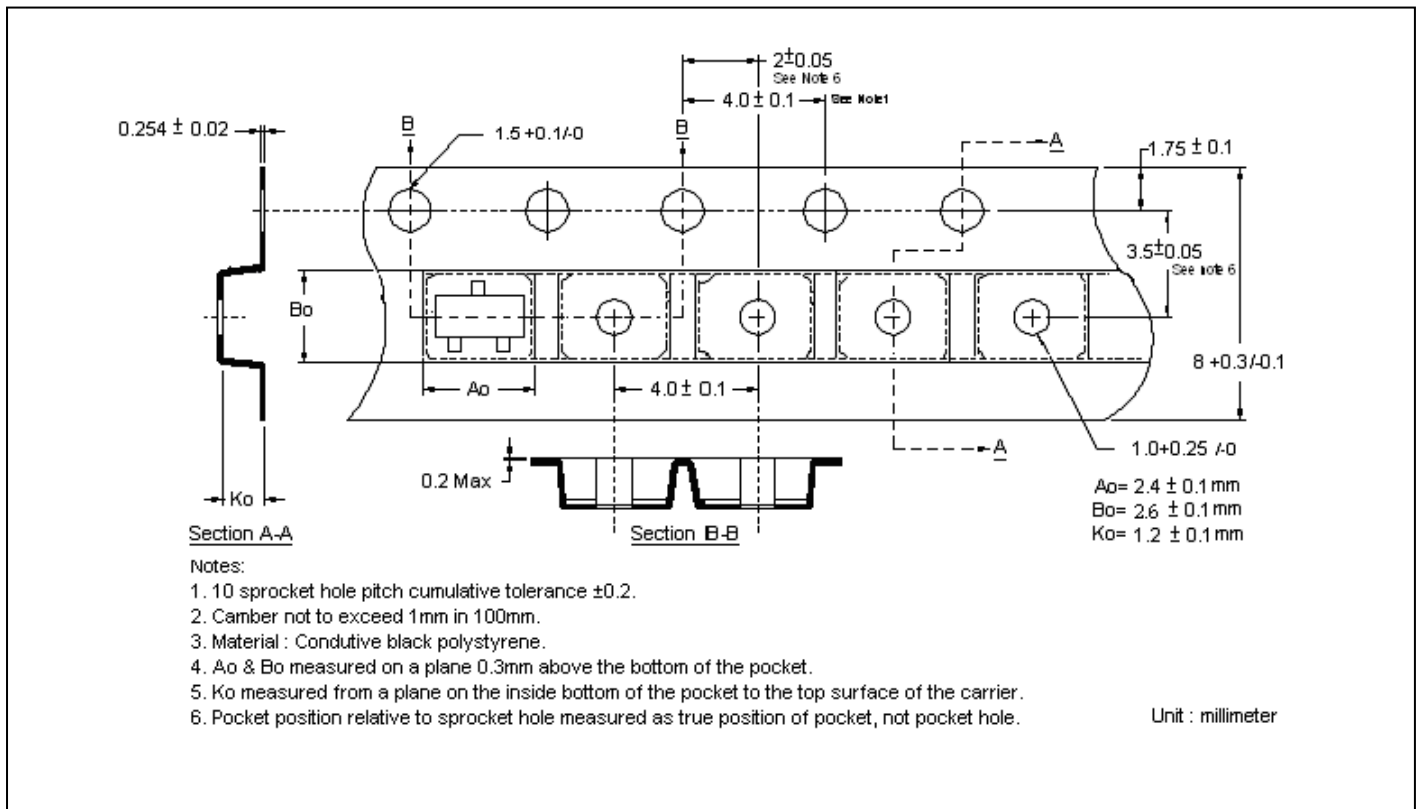
Transient Thermal Response Curves



Reel Dimension



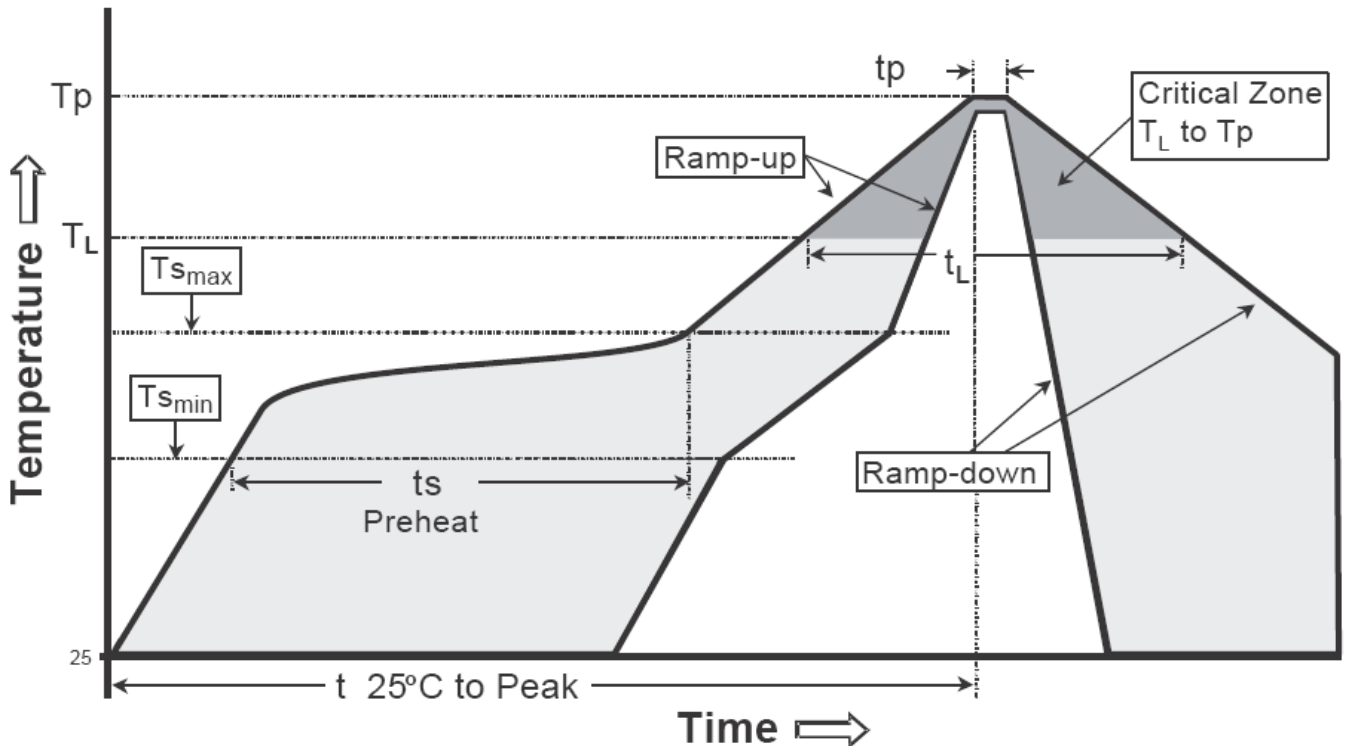
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

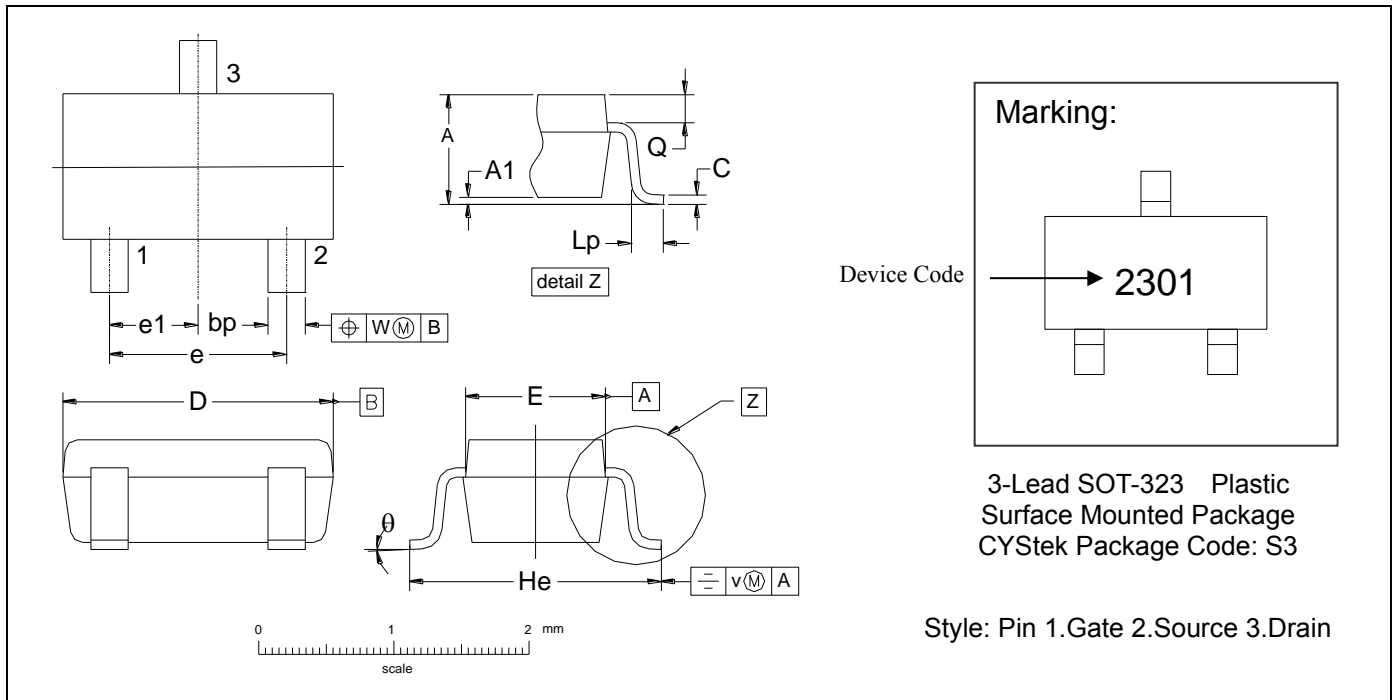
Recommended wave soldering condition



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-323 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0315	0.0433	0.80	1.10	e1	0.0256	-	0.65	-
A1	0.0000	0.0039	0.00	0.10	He	0.0787	0.0886	2.00	2.25
bp	0.0118	0.0157	0.30	0.40	Lp	0.0059	0.0177	0.15	0.45
C	0.0039	0.0098	0.10	0.25	Q	0.0051	0.0091	0.13	0.23
D	0.0709	0.0866	1.80	2.20	v	0.0079	-	0.2	-
E	0.0453	0.0531	1.15	1.35	w	0.0079	-	0.2	-
e	0.0512	-	1.3	-	θ	-	-	10°	0°

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.