

P-Channel Enhancement Mode MOSFET

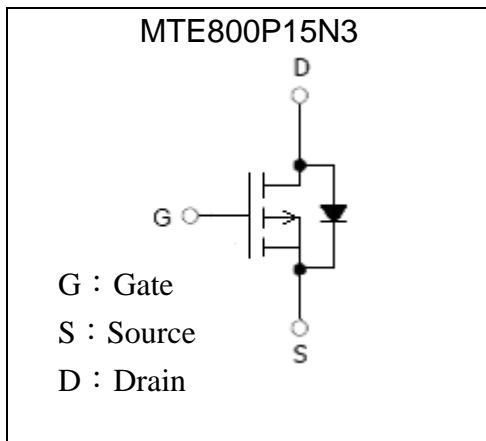
MTE800P15N3

BV _{DSS}		-150V
I _D @V _{GS} =-10V, T _A =25°C		-0.77A
R _{DS(on)(TYP)}	V _{GS} =-10V, I _D =-0.5A	0.8Ω

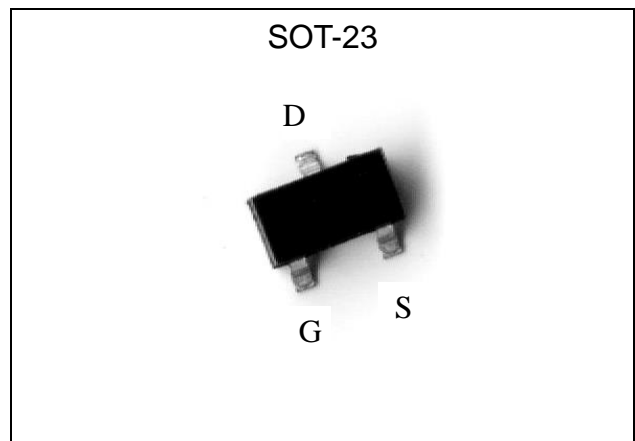
Features

- Advanced trench process technology
- High density cell design for ultra low on resistance
- Pb-free lead plating and halogen-free package

Equivalent Circuit

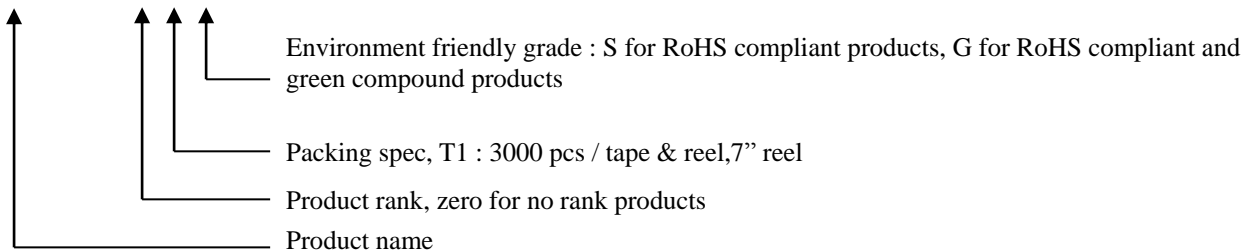


Outline



Ordering Information

Device	Package	Shipping
MTE800P15N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	-150	V	
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current @ V _{GS} =-10V	I _D	T _A =25°C	-0.77 (Note 4)	A
		T _A =70°C	-0.62 (Note 4)	
Pulsed Drain Current (Note 1&2)	I _{DM}	-3.2		
Maximum Power Dissipation	P _D	T _A =25°C	1.25	W
		T _A =70°C	0.8	
ESD susceptibility (Note 3)		2500	V	
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C	

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient , max (Note 4)	R _{θJA}	100	°C/W
Thermal Resistance, Junction-to-Case , max	R _{θJC}	50	

- Note : 1. Pulse width limited by maximum junction temperature.
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%
 3. Human body model, 1.5kΩ in series with 100pF.
 4. Surface mounted on 1 in² copper pad of FR-4 board, t ≤ 5s; 120°C/W at steady state; 417°C/W when mounted on minimum copper pad.

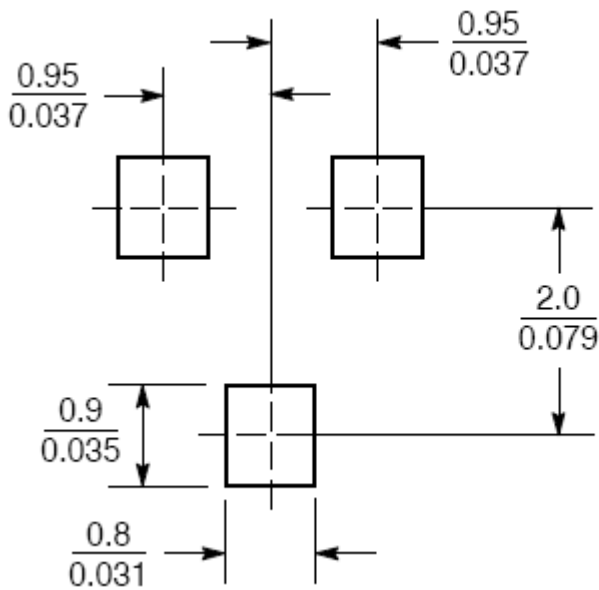
Electrical Characteristics (T_j=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-150	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-2.0	-	-4.0		V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-120V, V _{GS} =0V
	-	-	-10		V _{DS} =-120V, V _{GS} =0V, T _j =55°C
*R _{DS(ON)}	-	0.8	1.15	Ω	V _{GS} =-10V, I _D =-0.5A
*G _{FS}	-	1.6	-	S	V _{DS} =-10V, I _D =-0.5A
Dynamic					
C _{iss}	-	283	-	pF	V _{DS} =-75V, V _{GS} =0V, f=1MHz
C _{oss}	-	21	-		
C _{rss}	-	21	-		
*t _{d(ON)}	-	10.2	-	ns	V _{DS} =-75V, I _D =-0.5A, V _{GS} =-10V, R _G =6Ω
*t _r	-	1.4	-		
*t _{d(OFF)}	-	20.2	-		
*t _f	-	7	-		

*Qg	-	6.8	-	nC	V _{DS} =-75V, I _D =-0.5A, V _{GS} =-10V
*Qgs	-	1.6	-		
*Qgd	-	1.7	-		
Rg	-	5.6	-	Ω	f=1MHz
Source-Drain Diode					
I _s	-	-	-0.77	A	
I _{SM}	-	-	-3.2		
*V _{SD}	-	-0.81	-1.2	V	V _{GS} =0V, I _S =-1A
*trr	-	26.6	-	ns	I _F =-0.5A, V _{GS} =0V, dI _F /dt=100A/μs
*Q _{rr}	-	25	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

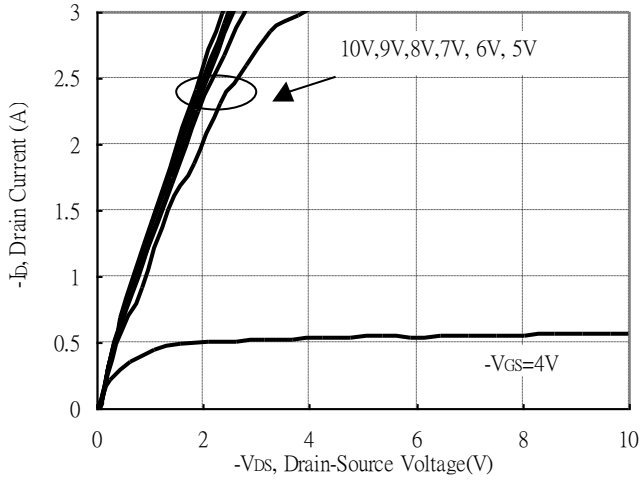
Recommended Soldering Footprint



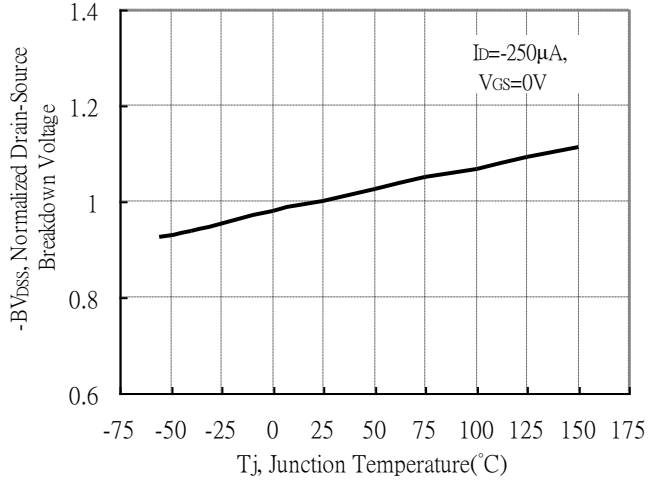
Unit : $\frac{\text{mm}}{\text{inches}}$

Typical Characteristics

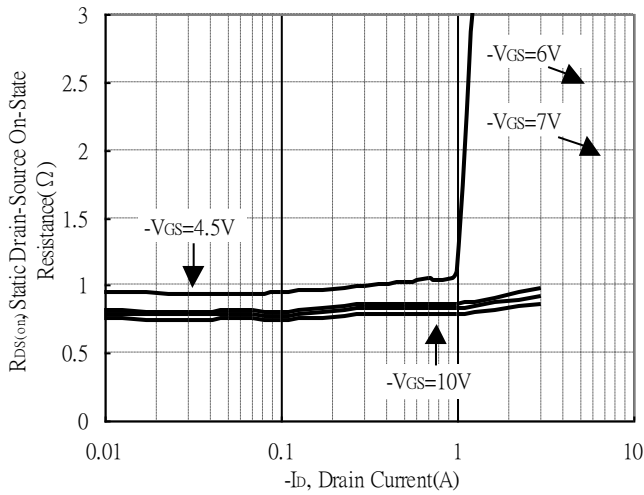
Typical Output Characteristics



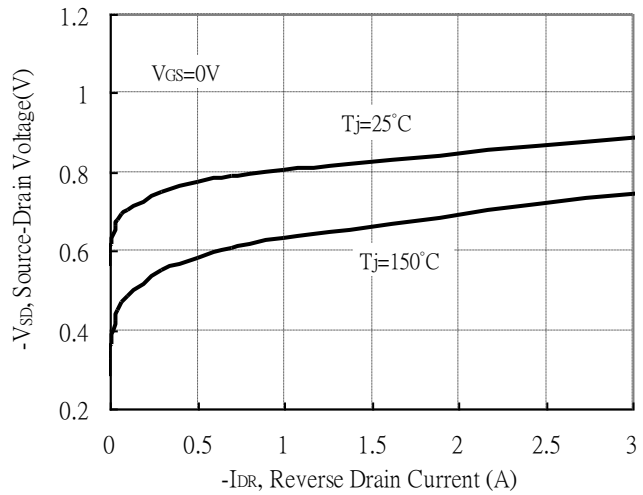
Breakdown Voltage vs Ambient Temperature



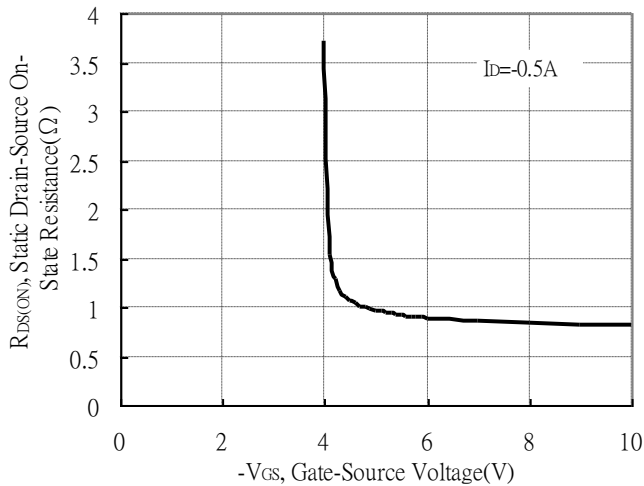
Static Drain-Source On-State resistance vs Drain Current



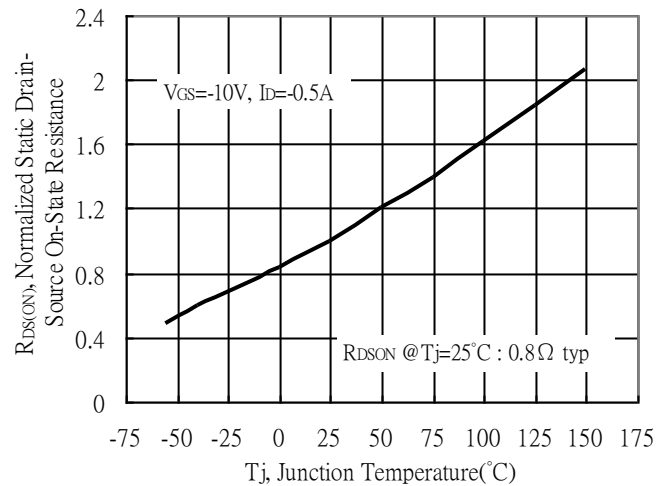
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

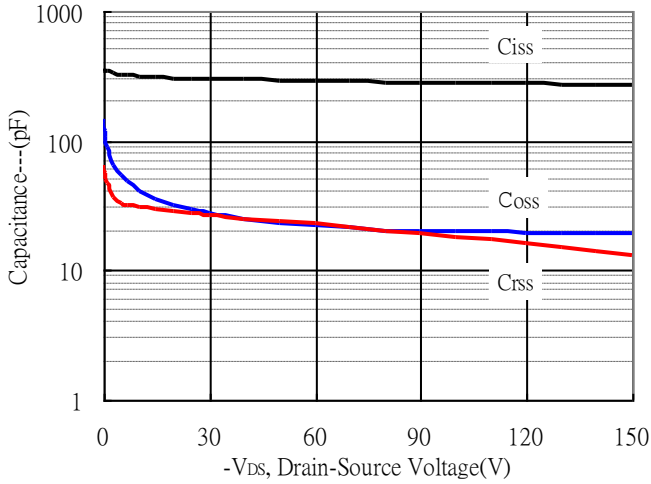


Drain-Source On-State Resistance vs Junction Temperature

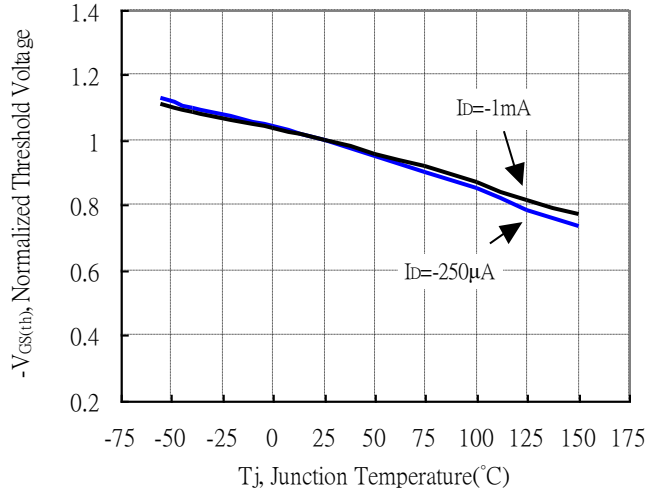


Typical Characteristics(Cont.)

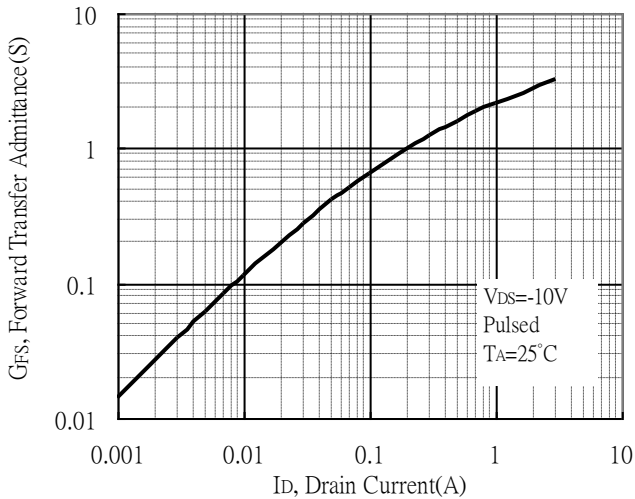
Capacitance vs Drain-to-Source Voltage



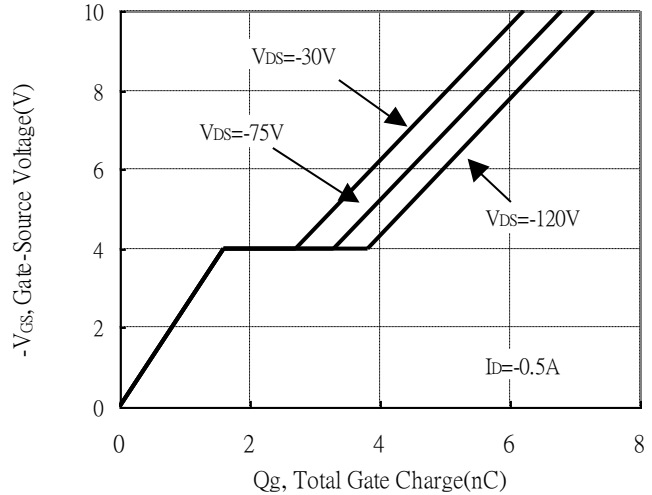
Threshold Voltage vs Junction Temperature



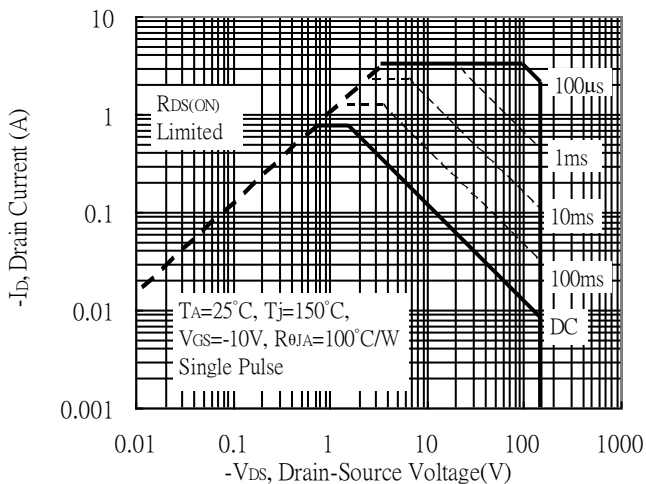
Forward Transfer Admittance vs Drain Current



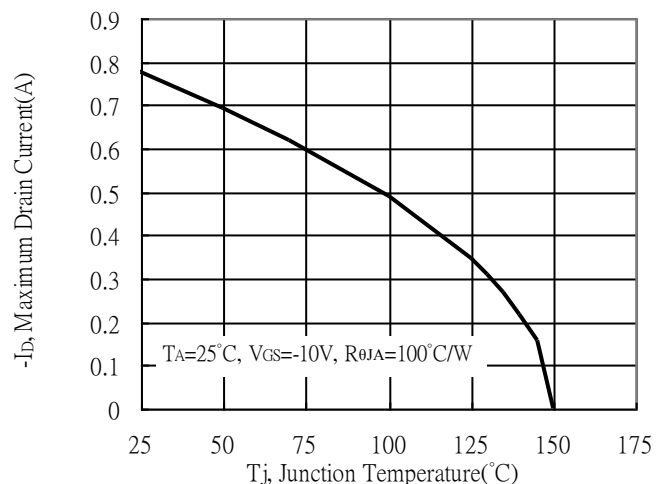
Gate Charge Characteristics



Maximum Safe Operating Area

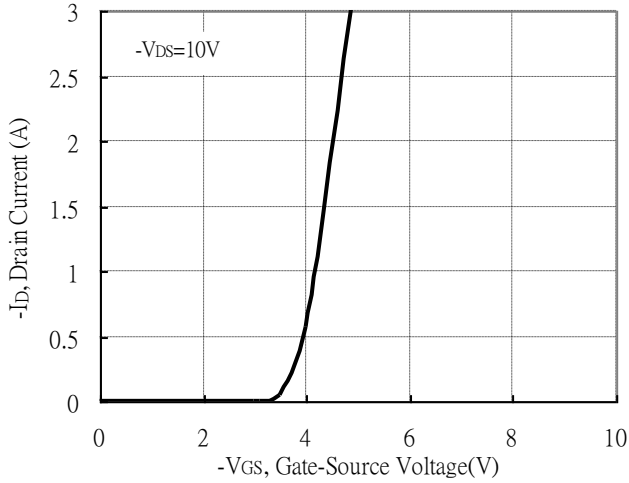


Maximum Drain Current vs Junction Temperature

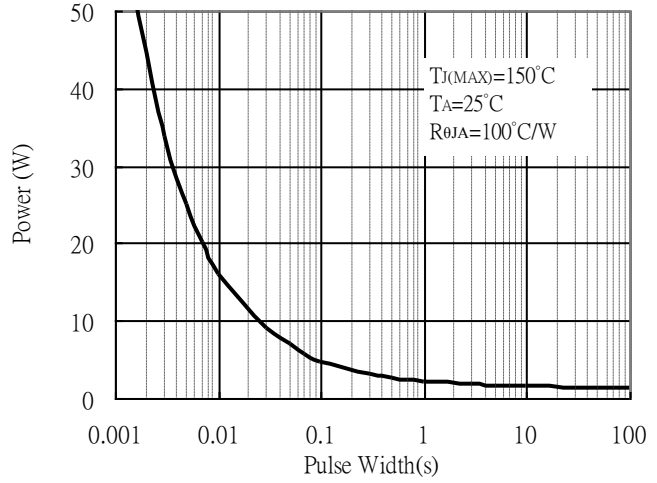


Typical Characteristics(Cont.)

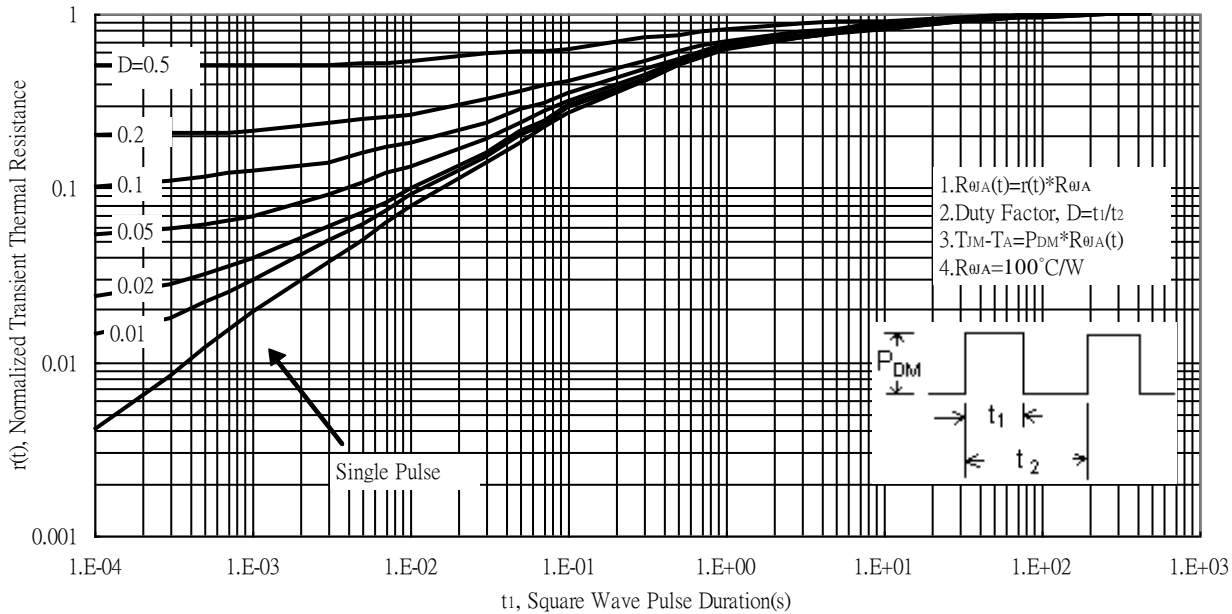
Typical Transfer Characteristics



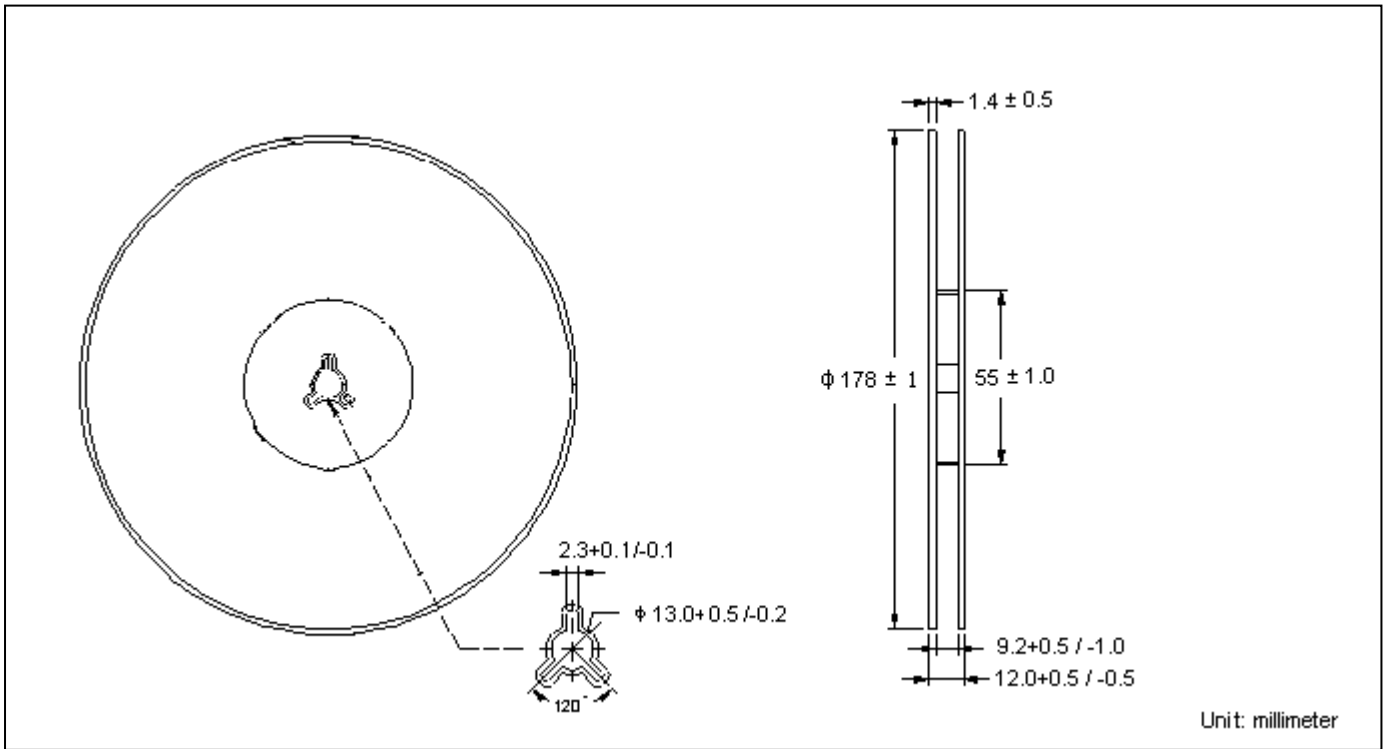
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



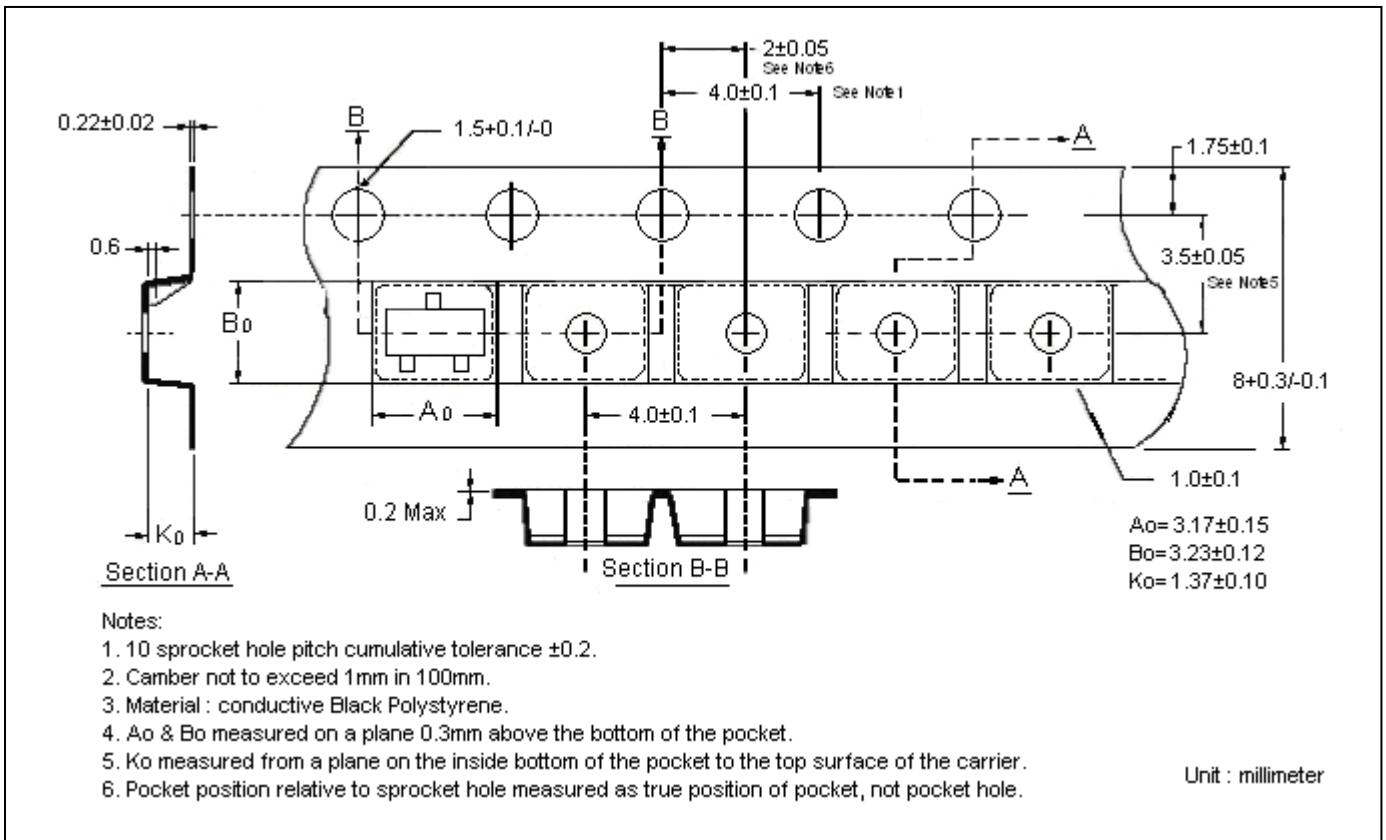
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



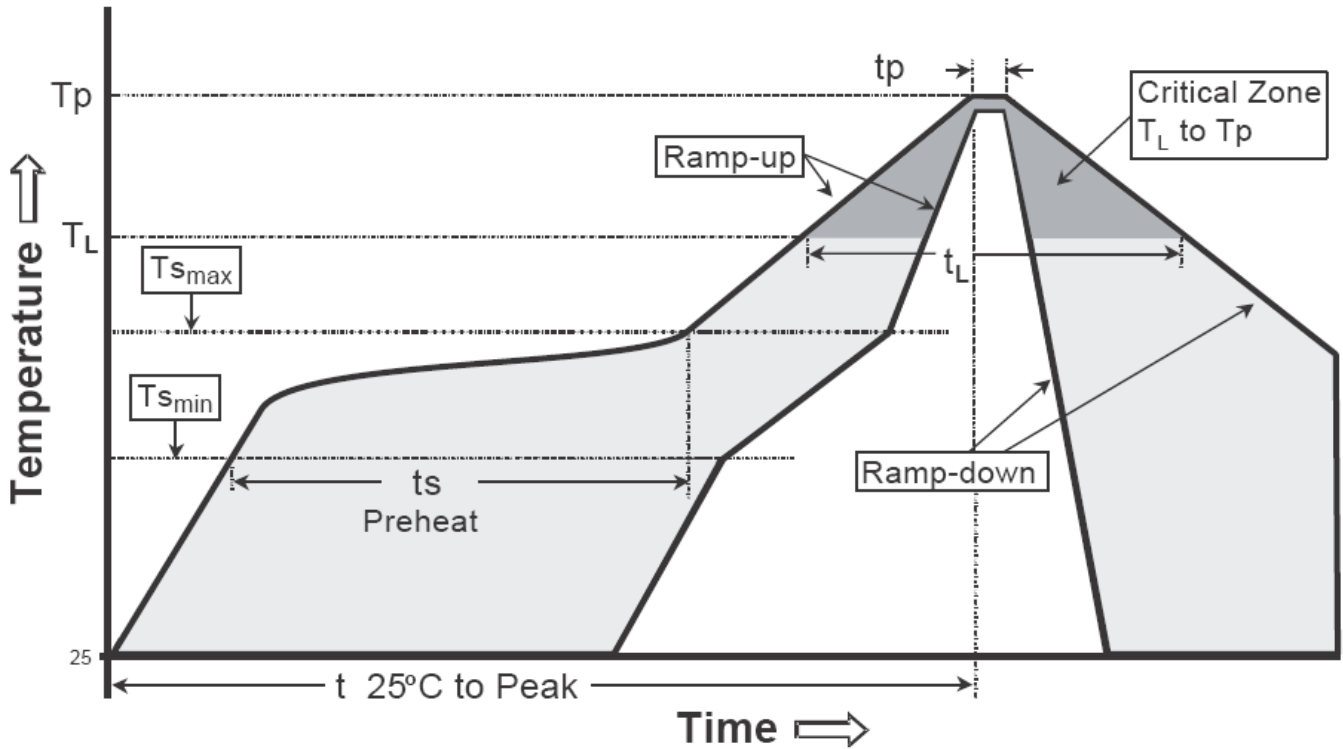
Notes:

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4. A_0 & B_0 measured on a plane 0.3mm above the bottom of the pocket.
5. K_0 measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

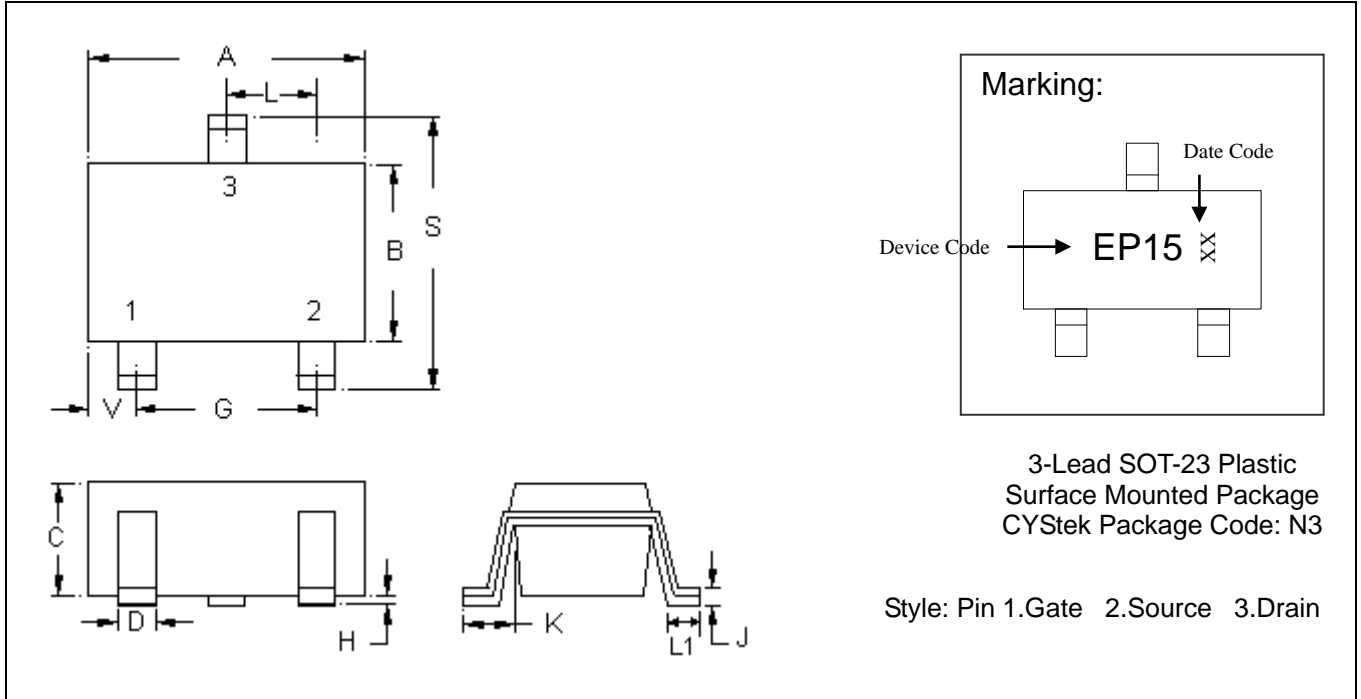
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-23 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10	L1	0.0118	0.0197	0.30	0.50

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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