

# N-Channel Enhancement Mode Power MOSFET

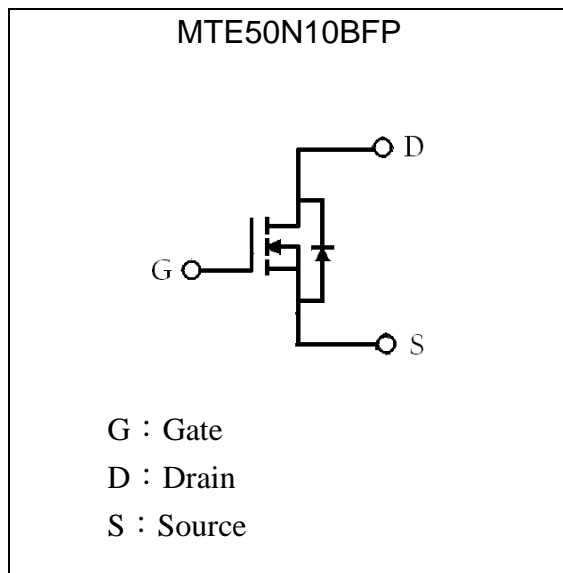
## MTE50N10BFP

### Features

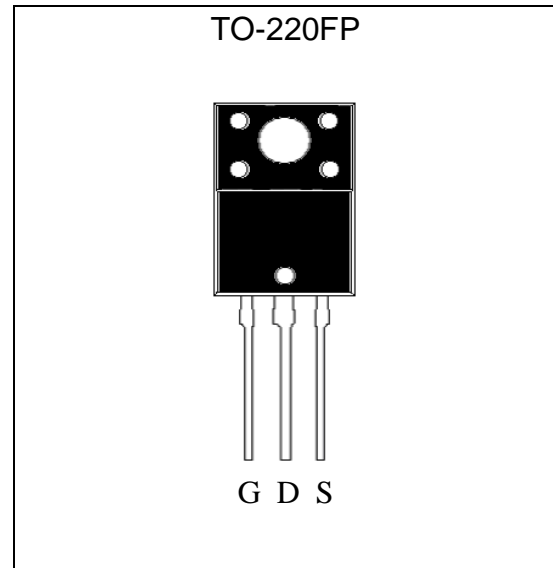
- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Insulating package, front/back side insulating voltage=2500V(AC)
- RoHS compliant package

|   |                      |
|---|----------------------|
| <b>B<sub>V</sub>D<sub>SS</sub></b>                              | <b>100V</b>          |
| <b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>C</sub>=25°C</b>     | <b>24A</b>           |
| <b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=10V, I<sub>D</sub>=15A</b> | <b>26.6 mΩ (typ)</b> |
| <b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=7V, I<sub>D</sub>=10A</b>  | <b>34.2 mΩ (typ)</b> |

### Symbol

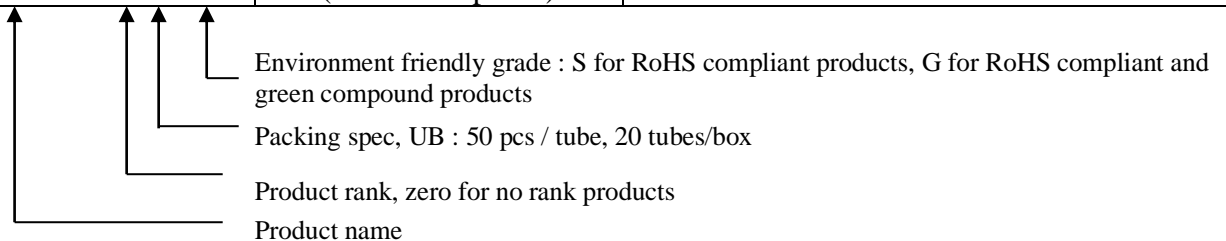


### Outline



### Ordering Information

| Device             | Package                      | Shipping                                    |
|--------------------|------------------------------|---|
| MTE50N10BFP-0-UB-S | TO-220FP<br>(RoHS compliant) | 50 pcs/tube, 20 tubes/box, 4 boxes / carton |



**Absolute Maximum Ratings** ( $T_C=25^\circ\text{C}$ )

| Parameter  | Symbol         | Limits                           | Unit             |   |
|--|----------------|----------------------------------|------------------|---|
| Drain-Source Voltage (Note 1)  | $V_{DS}$       | 100                              | V                |   |
| Gate-Source Voltage  | $V_{GS}$       | $\pm 20$                         |                  |   |
| Continuous Drain Current @ $T_C=25^\circ\text{C}$ , $V_{GS}=10\text{V}$ (Note 1)     | $I_D$          | 24*                              | A                |   |
| Continuous Drain Current @ $T_C=100^\circ\text{C}$ , $V_{GS}=10\text{V}$ (Note 1)    |                | 17*                              |                  |   |
| Continuous Drain Current @ $T_A=25^\circ\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)     | $I_{DSM}$      | 5.5                              |                  |   |
| Continuous Drain Current @ $T_A=70^\circ\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)     |                | 4.4                              |                  |   |
| Pulsed Drain Current @ $V_{GS}=10\text{V}$ (Note 3)                                  | $I_{DM}$       | 96*                              |                  |   |
| Avalanche Current (Note 3)   | $I_{AS}$       | 18                               |                  |   |
| Single Pulse Avalanche Energy @ $L=0.5\text{mH}$ (Note 2)                            | $E_{AS}$       | 25                               | mJ               |   |
| Power Dissipation  | $P_D$          | $T_C=25^\circ\text{C}$ (Note 1)  | 42               | W |
|  |                | $T_C=100^\circ\text{C}$ (Note 1) | 21               |   |
|  | $P_{DSM}$      | $T_A=25^\circ\text{C}$ (Note 2)  | 2.1              |   |
|  |                | $T_A=70^\circ\text{C}$ (Note 2)  | 1.4              |   |
| Maximum Temperature for Soldering @ Lead at 0.063 in(1.6mm) from case for 10 seconds | $T_L$          | 300                              | $^\circ\text{C}$ |   |
| Maximum Temperature for Soldering @ Package Body for 10 seconds                      | $T_{PKG}$      | 260                              |                  |   |
| Operating Junction and Storage Temperature   | $T_j, T_{stg}$ | -55~+175                         |                  |   |

\*Drain current limited by maximum junction temperature

**Thermal Data**

| Parameter   | Symbol          | Value | Unit                      |
|---|-----------------|-------|---------------------------|
| Thermal Resistance, Junction-to-case, max             | $R_{\theta JC}$ | 3.5   | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-ambient, max (Note 2) | $R_{\theta JA}$ | 58    |                           |

- Note : 1. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=175^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^\circ\text{C}$ . The value in any given application depends on the user's specific board design, and the maximum temperature of  $175^\circ\text{C}$  may be used if the PCB allows it.
3. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=175^\circ\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^\circ\text{C}$ .



**Characteristics (Tj=25°C, unless otherwise specified)**

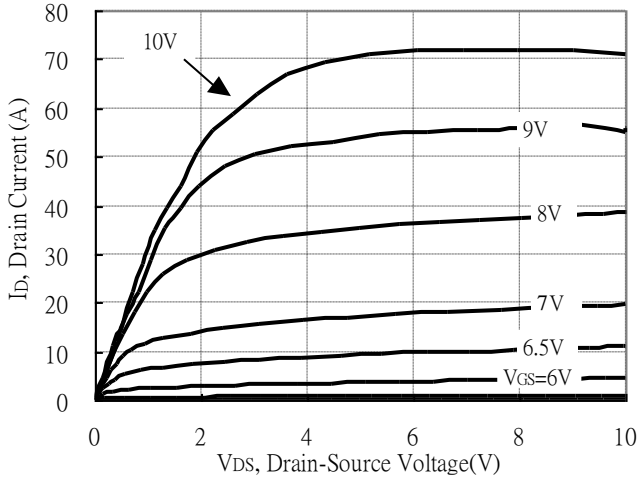
| Symbol                              | Min. | Typ. | Max. | Unit  | Test Conditions   |
|-------------------------------------|------|------|------|-------|---|
| <b>Static</b>                       |      |      |      |       |   |
| BV <sub>DSS</sub>                   | 100  | -    | -    | V     | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA  |
| ΔBV <sub>DSS</sub> /ΔT <sub>j</sub> | -    | 76   | -    | mV/°C | Reference to 25°C, I <sub>D</sub> =250μA  |
| V <sub>GS(th)</sub>                 | 2.0  | -    | 4.0  | V     | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA                           |
| *G <sub>FS</sub>                    | -    | 10   | -    | S     | V <sub>DS</sub> =10V, I <sub>D</sub> =15A   |
| I <sub>GSS</sub>                    | -    | -    | ±100 | nA    | V <sub>GS</sub> =±20V   |
| I <sub>DSS</sub>                    | -    | -    | 1    | μA    | V <sub>DS</sub> =80V, V <sub>GS</sub> =0V   |
|                                     | -    | -    | 25   |       | V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C                    |
| *R <sub>DS(ON)</sub>                | -    | 26.6 | 36   | mΩ    | V <sub>GS</sub> =10V, I <sub>D</sub> =15A   |
|                                     | -    | 34.2 | 46   |       | V <sub>GS</sub> =7V, I <sub>D</sub> =10A  |
| <b>Dynamic</b>                      |      |      |      |       |   |
| *Q <sub>g</sub>                     | -    | 19.1 | -    | nC    | V <sub>DD</sub> =50V, I <sub>D</sub> =24A, V <sub>GS</sub> =10V                     |
| *Q <sub>gs</sub>                    | -    | 4    | -    |       |   |
| *Q <sub>gd</sub>                    | -    | 9.7  | -    |       |   |
| *t <sub>d(ON)</sub>                 | -    | 13.2 | -    | ns    | V <sub>DD</sub> =50V, I <sub>D</sub> =15A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω |
| *t <sub>r</sub>                     | -    | 27.2 | -    |       |   |
| *t <sub>d(OFF)</sub>                | -    | 20.6 | -    |       |   |
| *t <sub>f</sub>                     | -    | 14.4 | -    |       |   |
| C <sub>iss</sub>                    | -    | 646  | -    | pF    | V <sub>GS</sub> =0V, V <sub>DS</sub> =30V, f=1MHz                                   |
| C <sub>oss</sub>                    | -    | 118  | -    |       |   |
| C <sub>rss</sub>                    | -    | 104  | -    |       |   |
| <b>Source-Drain Diode</b>           |      |      |      |       |   |
| *I <sub>S</sub>                     | -    | -    | 24   | A     |   |
| *I <sub>SM</sub>                    | -    | -    | 96   |       |   |
| *V <sub>SD</sub>                    | -    | 0.92 | 1.2  | V     | I <sub>S</sub> =15A, V <sub>GS</sub> =0V  |
| *t <sub>rr</sub>                    | -    | 28   | -    | ns    | V <sub>GS</sub> =0V, I <sub>F</sub> =24A, dI <sub>F</sub> /dt=100A/μs               |
| *Q <sub>rr</sub>                    | -    | 32   | -    | nC    |   |

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

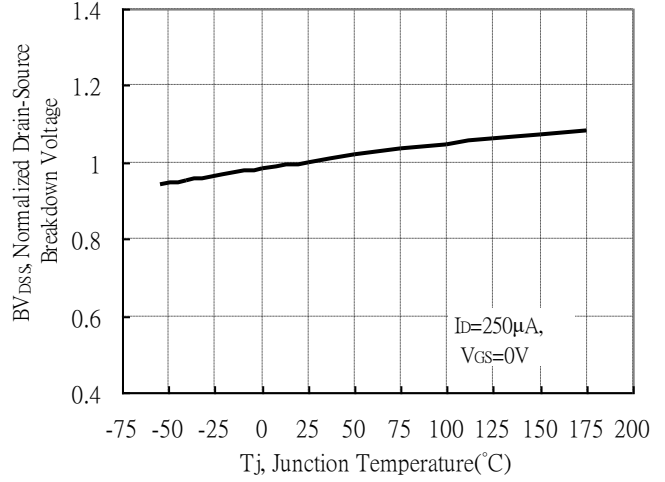


**Typical Characteristics**

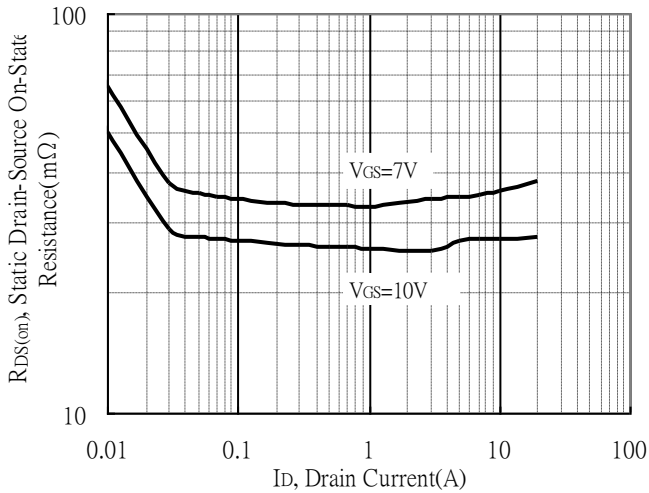
Typical Output Characteristics



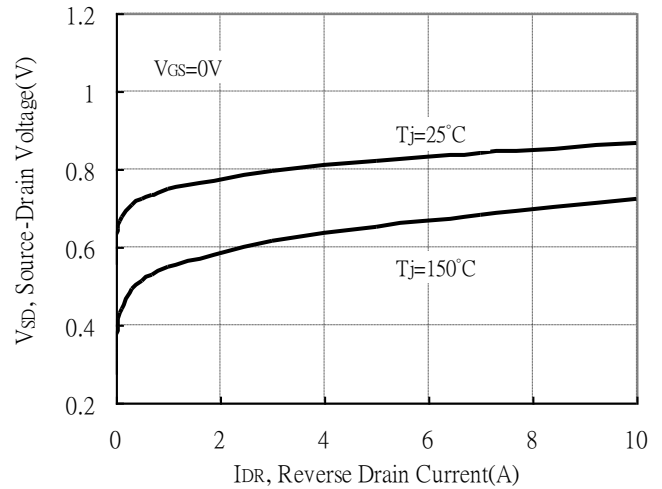
Brekdown Voltage vs Ambient Temperature



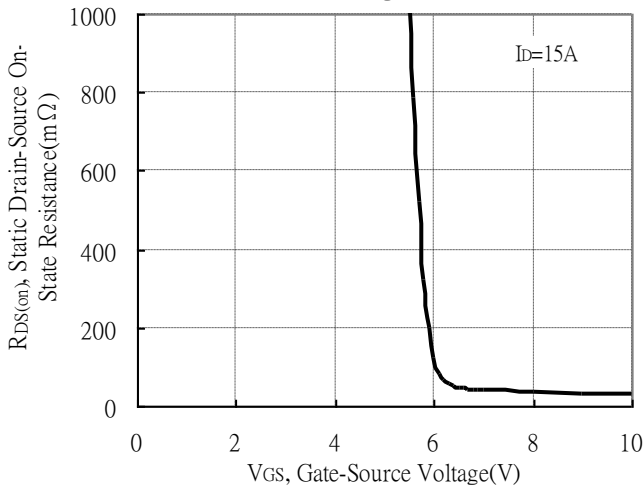
Static Drain-Source On-State resistance vs Drain Current



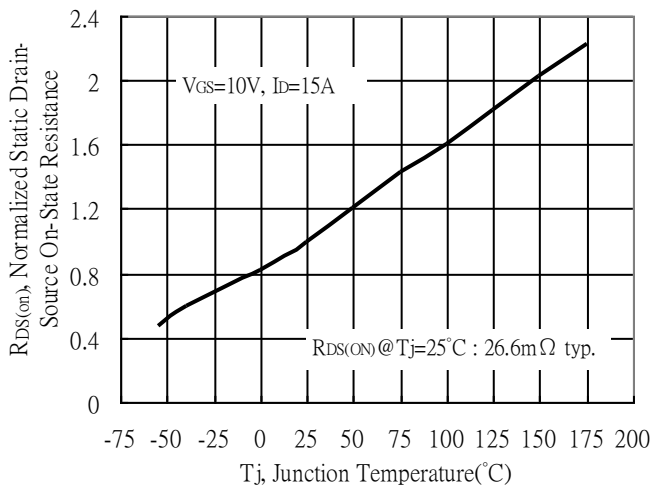
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

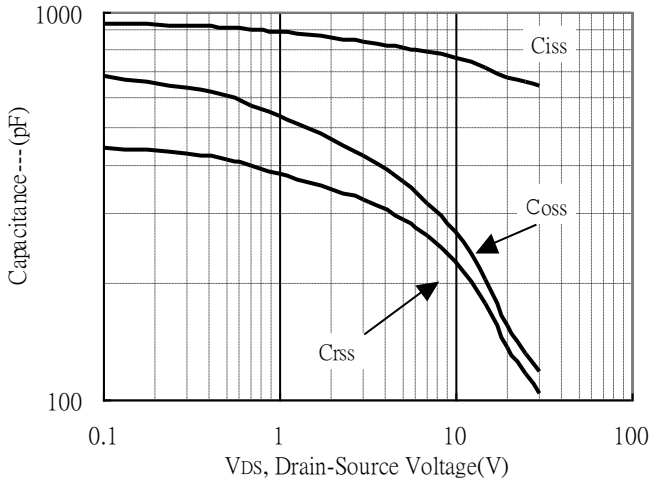


Drain-Source On-State Resistance vs Junction Temperature

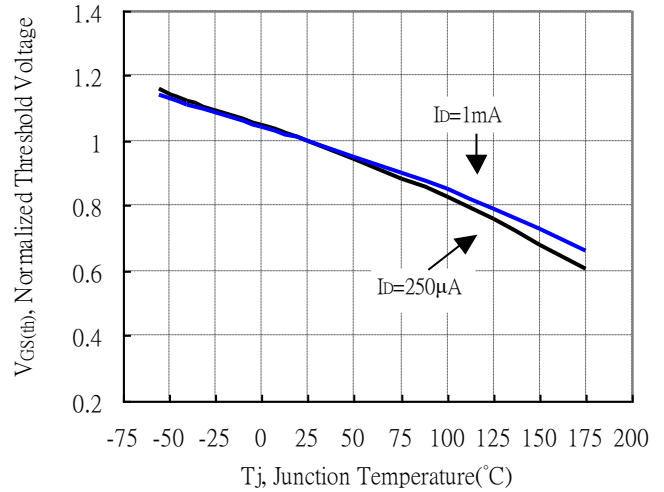


**Typical Characteristics(Cont.)**

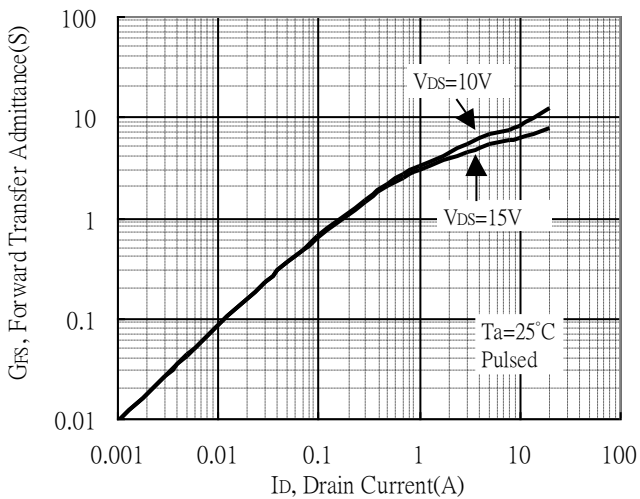
Capacitance vs Drain-to-Source Voltage



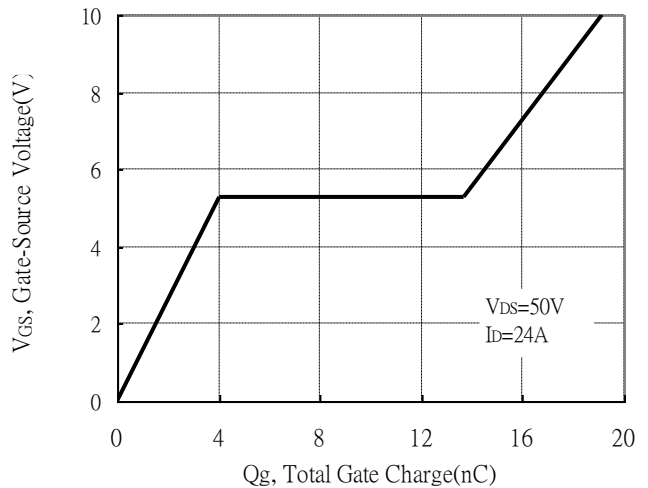
Threshold Voltage vs Junction Temperature



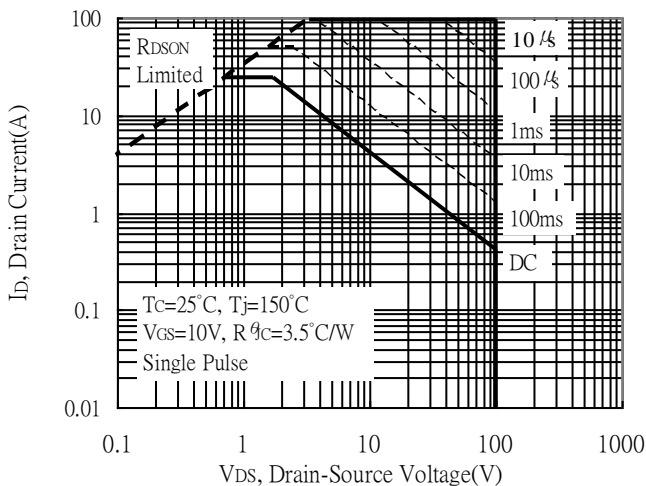
Forward Transfer Admittance vs Drain Current



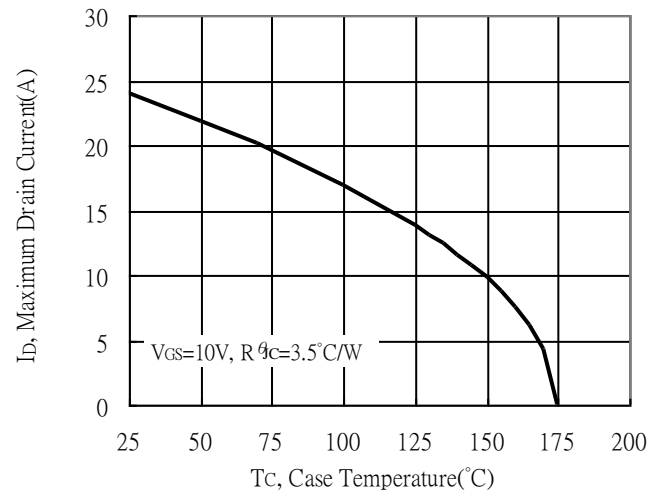
Gate Charge Characteristics



Maximum Safe Operating Area



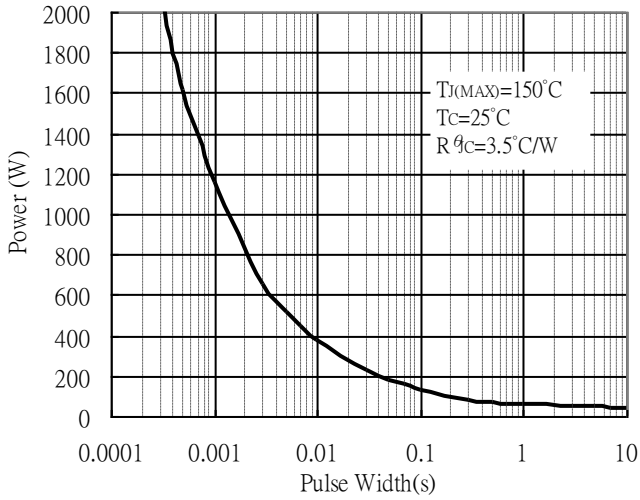
Maximum Drain Current vs Case Temperature



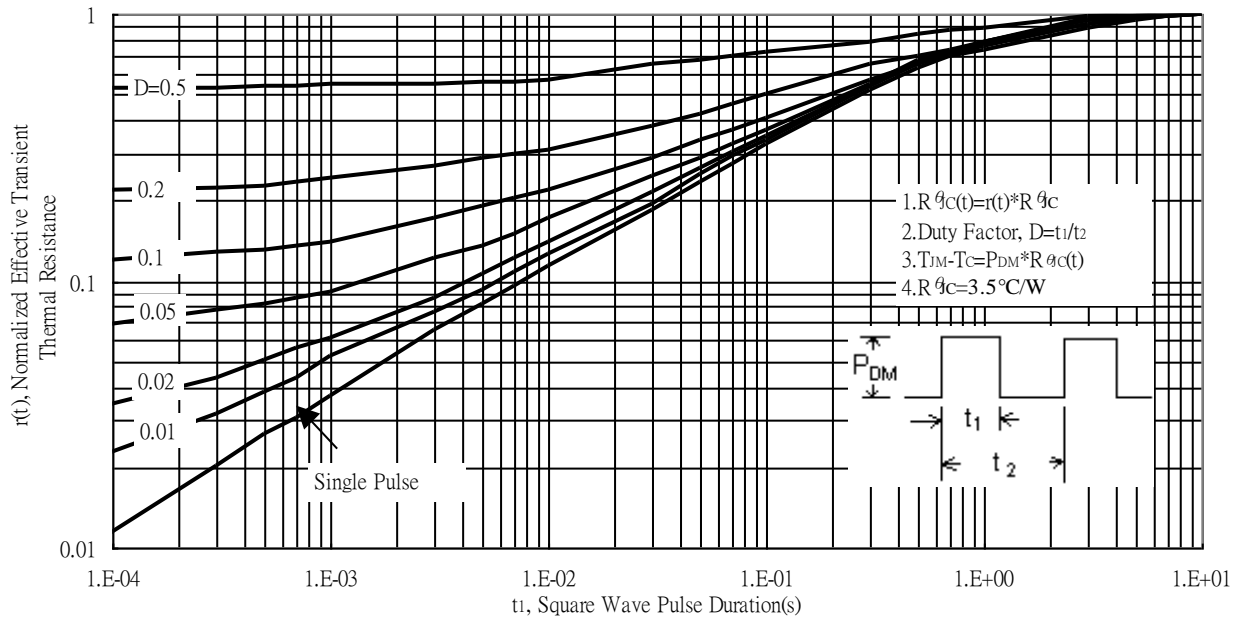


### Typical Characteristics(Cont.)

Single Pulse Power Rating, Junction to Case



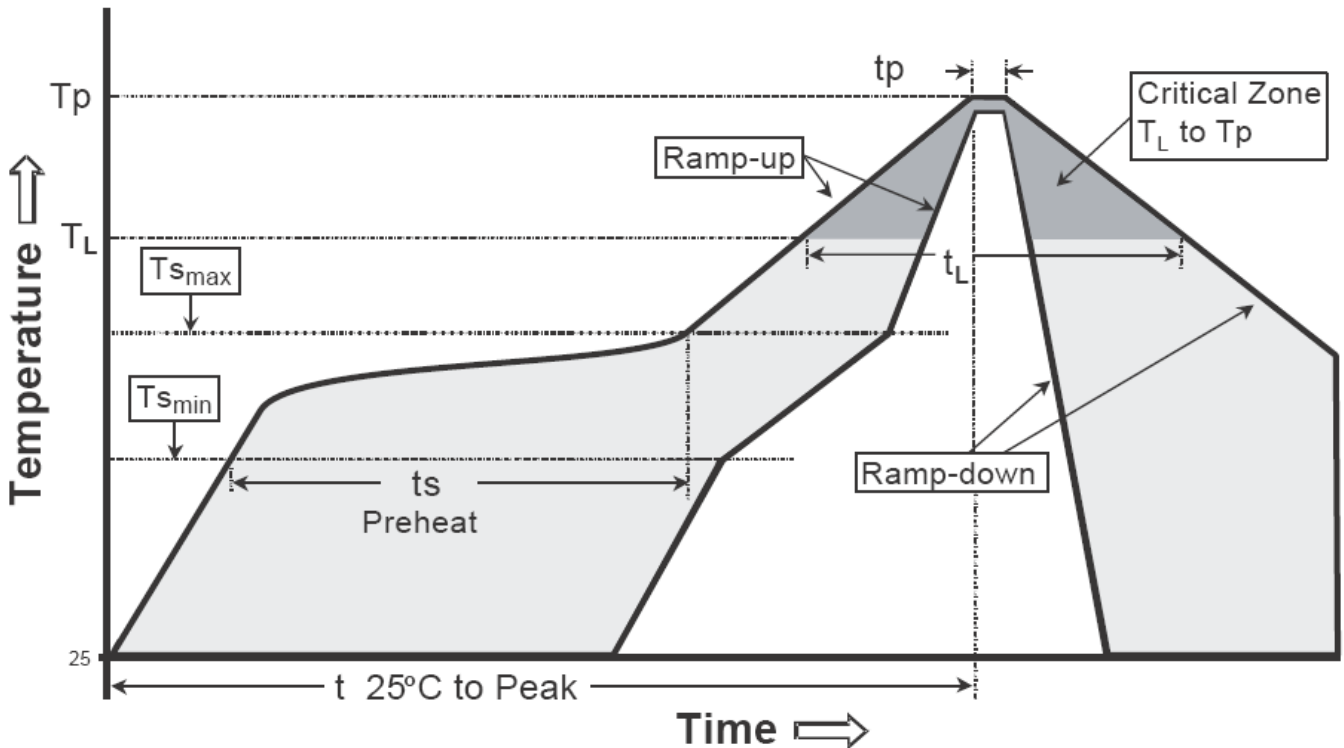
Transient Thermal Response Curves



**Recommended wave soldering condition**

|                 |                  |                 |
|-----------------|------------------|-----------------|
| Product         | Peak Temperature | Soldering Time  |
| Pb-free devices | 260 +0/-5 °C     | 5 +1/-1 seconds |

**Recommended temperature profile for IR reflow**



| Profile feature                                | Sn-Pb eutectic Assembly | Pb-free Assembly |
|--|-------------------------|------------------|
| Average ramp-up rate (Tsmax to Tp)             | 3°C/second max.         | 3°C/second max.  |
| Preheat  |                         |                  |
| -Temperature Min(Ts min)                       | 100°C                   | 150°C            |
| -Temperature Max(Ts max)                       | 150°C                   | 200°C            |
| -Time(ts min to ts max)                        | 60-120 seconds          | 60-180 seconds   |
| Time maintained above:                         |                         |                  |
| -Temperature (TL)                              | 183°C                   | 217°C            |
| - Time (tL)                                    | 60-150 seconds          | 60-150 seconds   |
| Peak Temperature(Tp)                           | 240 +0/-5 °C            | 260 +0/-5 °C     |
| Time within 5°C of actual peak temperature(tp) | 10-30 seconds           | 20-40 seconds    |
| Ramp down rate                                 | 6°C/second max.         | 6°C/second max.  |
| Time 25 °C to peak temperature                 | 6 minutes max.          | 8 minutes max.   |

Note : All temperatures refer to topside of the package, measured on the package body surface.

**TO-220FP Dimension**

**3-Lead TO-220FP Plastic Package**  
 CYStek Package Code: FP

**Marking:**

Device Name → E50N10B  
 Date Code → □□□□

Style: Pin 1.Gate 2.Drain 3.Source

\*Typical

| DIM | Inches    |       | Millimeters |       | DIM | Inches    |       | Millimeters |       |
|-----|-----------|-------|-------------|-------|-----|-----------|-------|-------------|-------|
|     | Min.      | Max.  | Min.        | Max.  |     | Min.      | Max.  | Min.        | Max.  |
| A   | 0.171     | 0.183 | 4.35        | 4.65  | G   | 0.246     | 0.258 | 6.25        | 6.55  |
| A1  | 0.051 REF |       | 1.300 REF   |       | H   | 0.138 REF |       | 3.50 REF    |       |
| A2  | 0.112     | 0.124 | 2.85        | 3.15  | H1  | 0.055 REF |       | 1.40 REF    |       |
| A3  | 0.102     | 0.110 | 2.60        | 2.80  | H2  | 0.256     | 0.272 | 6.50        | 6.90  |
| b   | 0.020     | 0.030 | 0.50        | 0.75  | J   | 0.031 REF |       | 0.80 REF    |       |
| b1  | 0.031     | 0.041 | 0.80        | 1.05  | K   | 0.020     |       | 0.50 REF    |       |
| b2  | 0.047 REF |       | 1.20 REF    |       | L   | 1.102     | 1.118 | 28.00       | 28.40 |
| c   | 0.020     | 0.030 | 0.500       | 0.750 | L1  | 0.043     | 0.051 | 1.10        | 1.30  |
| D   | 0.396     | 0.404 | 10.06       | 10.26 | L2  | 0.036     | 0.043 | 0.92        | 1.08  |
| E   | 0.583     | 0.598 | 14.80       | 15.20 | M   | 0.067 REF |       | 1.70 REF    |       |
| e   | 0.100 *   |       | 2.54*       |       | N   | 0.012 REF |       | 0.30 REF    |       |
| F   | 0.106 REF |       | 2.70 REF    |       |     |           |       |             |       |

- Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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