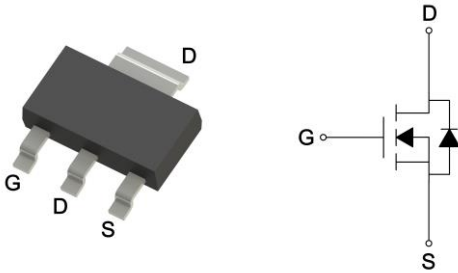


## Product Summary

$BV_{DSS}$	100	V
$R_{DS(ON)}$ typ. @ $V_{GS}=10V, I_D=5A$	28	m $\Omega$
$I_D$ @ $V_{GS}=10V, T_C=25^\circ C$	7	A
$I_D$ @ $V_{GS}=10V, T_A=25^\circ C$	5	

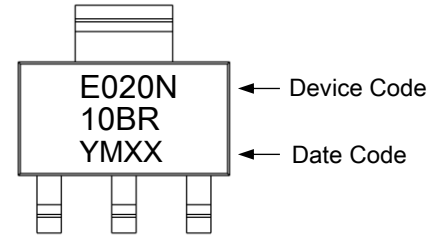
## SOT-223



## Features

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free

## Marking



YMXX: Date Code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

A: Jan	B: Feb	C: Mar	D: Apr	E: May	F: Jun
G: Jul	H: Aug	J: Sep	K: Oct	L: Nov	M: Dec

XX: Production Serial Number, 01~99

## Ordering Information

Device	Package	Shipping
MTE020N10BRL3-0-T3-G	SOT-223	2500pcs / Tape & Reel

0: Product rank, zero for no rank products.

T3: Packing spec, T3 : 2500pcs / tape & reel, 13" reel

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

## Absolute Maximum Ratings ( $T_A=25^\circ C$ )

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DS}$	100	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$		
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$ (silicon limit)	$I_D$	15	A	
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$ (package limit)		7		
Continuous Drain Current @ $V_{GS}=10V, T_C=100^\circ C$		7		
Continuous Drain Current @ $V_{GS}=10V, T_A=25^\circ C$		5		
Continuous Drain Current @ $V_{GS}=10V, T_A=70^\circ C$		4		
Pulsed Drain Current		$I_{DM}$		28
Continuous Body Diode Forward Current @ $T_C=25^\circ C$	$I_S$	7	A	
Pulsed Body Diode Forward Current @ $T_C=25^\circ C$	$I_{SM}$	28		
Avalanche Current @ $L=0.1mH$	$I_{AS}$	15	mJ	
Avalanche Energy @ $L=0.5mH$	$E_{AS}$	16		
Total Power Dissipation	$P_D$	$T_C=25^\circ C$	17	W
		$T_C=100^\circ C$	6.8	
		$T_A=25^\circ C$	2	
		$T_A=70^\circ C$	1.3	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	$^\circ C$	
Steady State Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	7.2	$^\circ C/W$	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	63		



**Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)**

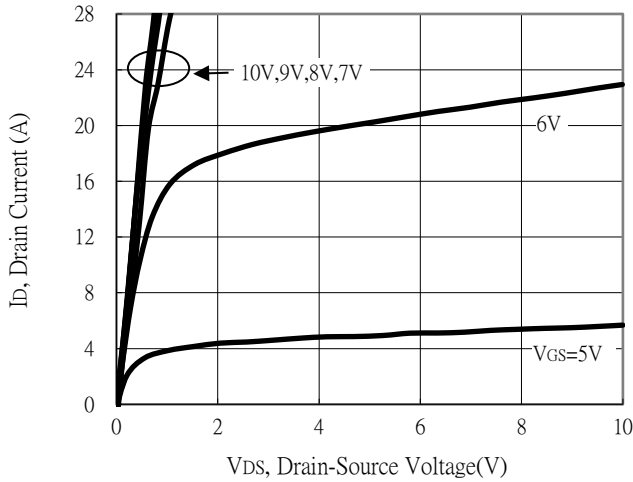
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	-	4		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	5.2	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =5A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
R <sub>DS(ON)</sub>	-	28	37	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =5A
<b>Dynamic</b>					
C <sub>iss</sub>	-	830	-	pF	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	108	-		
C <sub>rss</sub>	-	16	-		
R <sub>g</sub>	-	1.3	-	Ω	f=1MHz
Q <sub>g</sub> *d,e	-	13	-	nC	V <sub>DS</sub> =50V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V
Q <sub>gs</sub> *d,e	-	6	-		
Q <sub>gd</sub> *d,e	-	2.9	-		
t <sub>d(ON)</sub> *d,e	-	14	-	ns	V <sub>DS</sub> =50V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V, R <sub>GS</sub> =3Ω
tr *d,e	-	15	-		
t <sub>d(OFF)</sub> *d,e	-	17	-		
t <sub>f</sub> *d,e	-	5.9	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *d	-	0.82	1.2	V	I <sub>S</sub> =5A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	32	-	ns	I <sub>F</sub> =5A, di/dt=100A/μs
Q <sub>rr</sub>	-	48	-	nC	

**Note:**

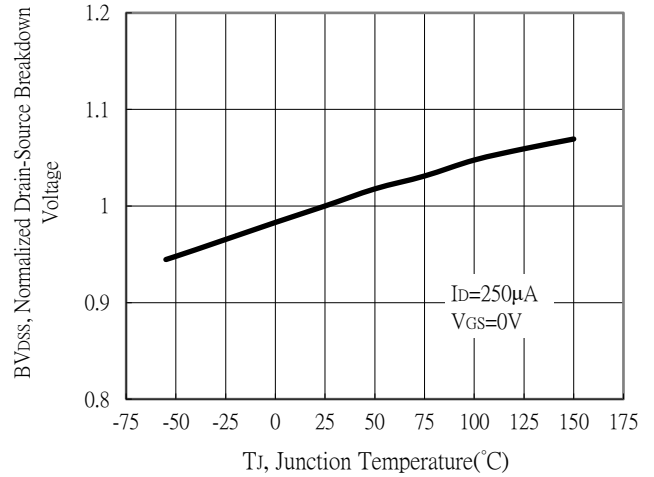
- \*a. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- \*b. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.
- \*d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- \*e. Independent of operating temperature.

## Typical Characteristics

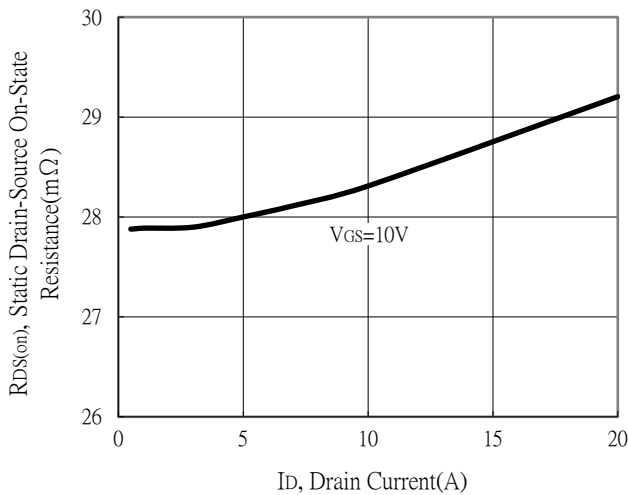
Typical Output Characteristics



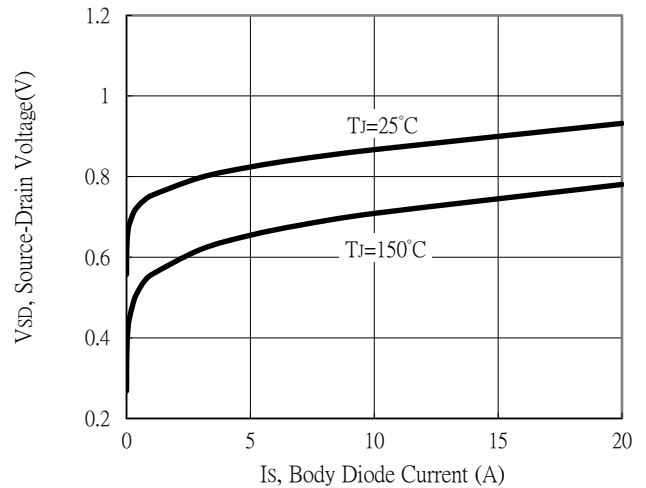
Breakdown Voltage vs Ambient Temperature



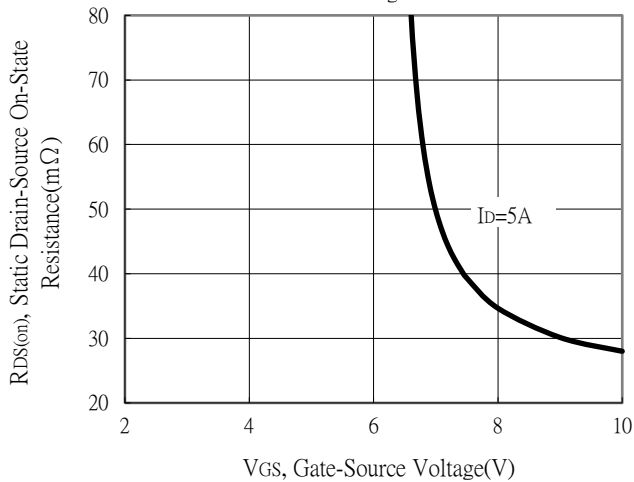
Static Drain-Source On-State resistance vs Drain Current



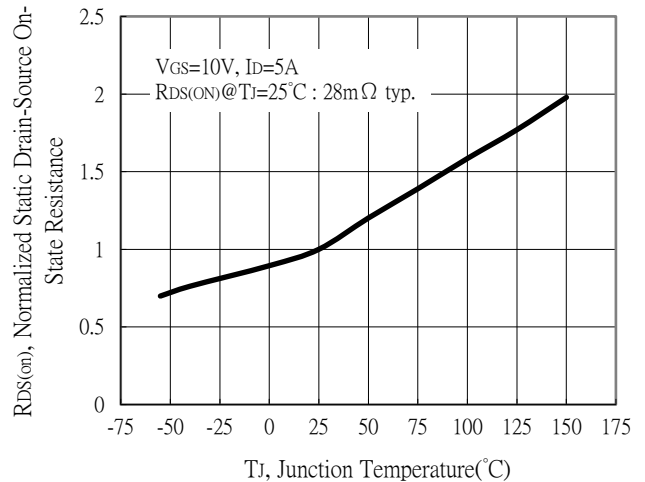
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



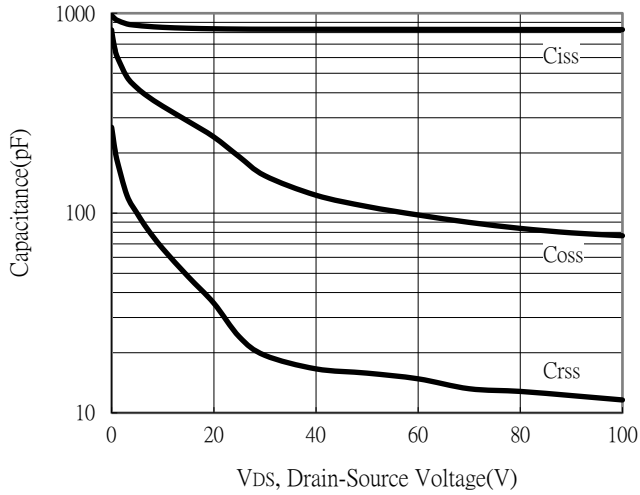
Drain-Source On-State Resistance vs Junction Temperature



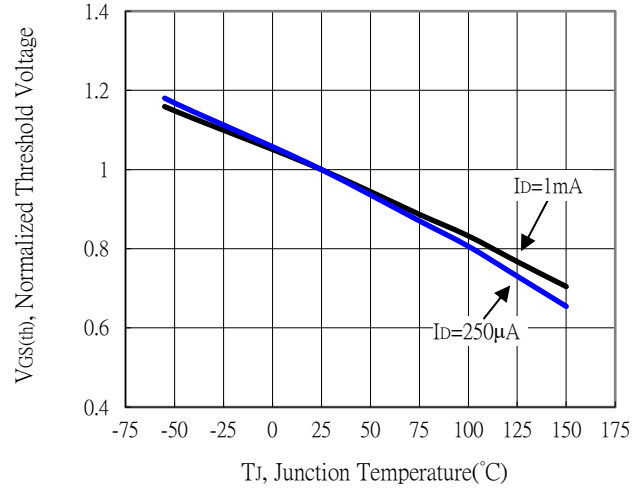


## Typical Characteristics

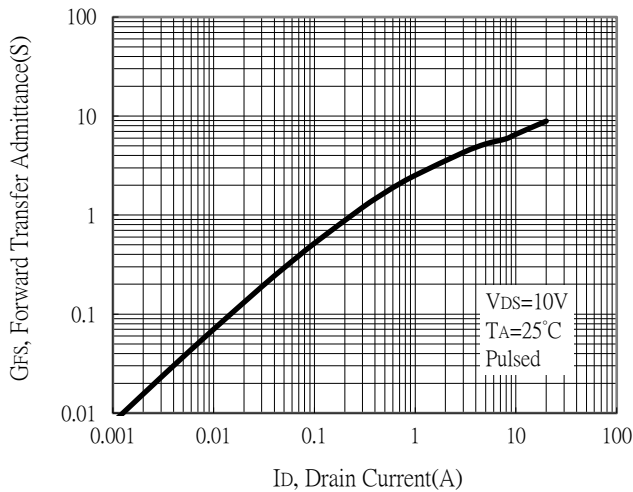
Capacitance vs Drain-to-Source Voltage



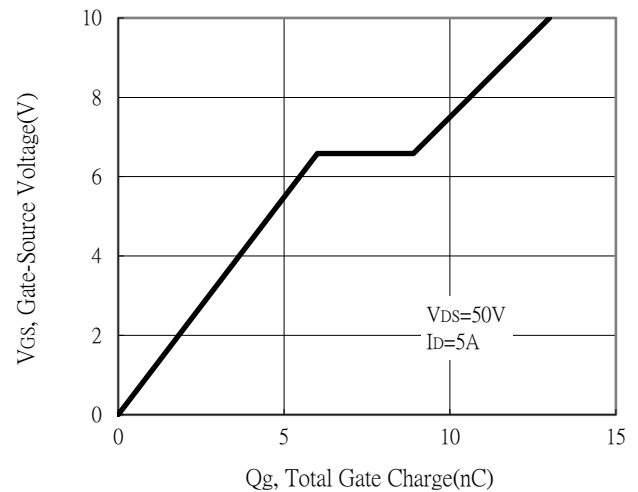
Threshold Voltage vs Junction Temperature



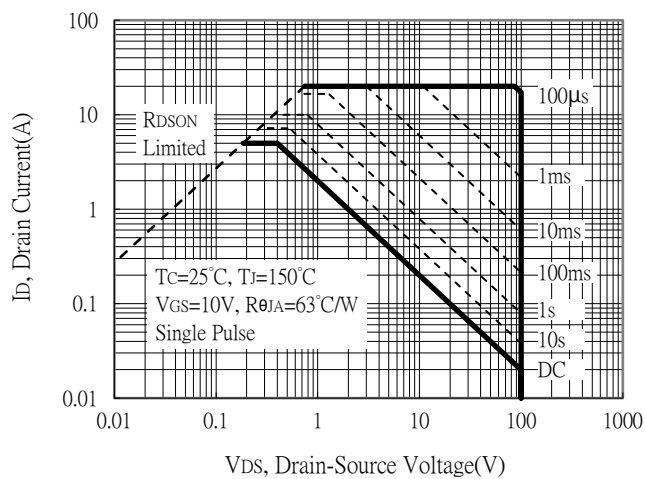
Forward Transfer Admittance vs Drain Current



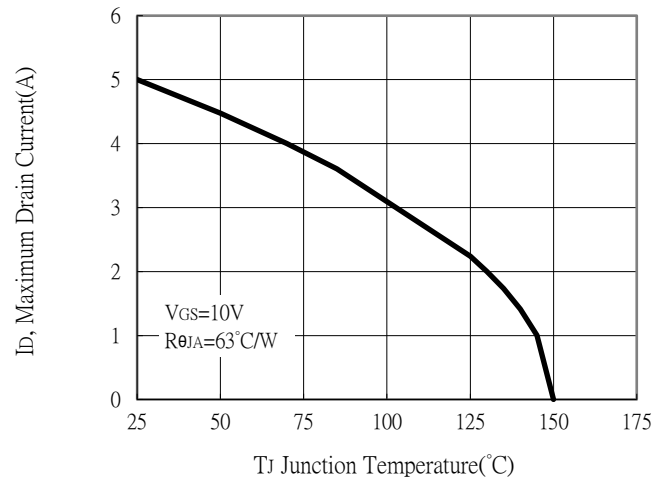
Gate Charge Characteristics



Maximum Safe Operating Area



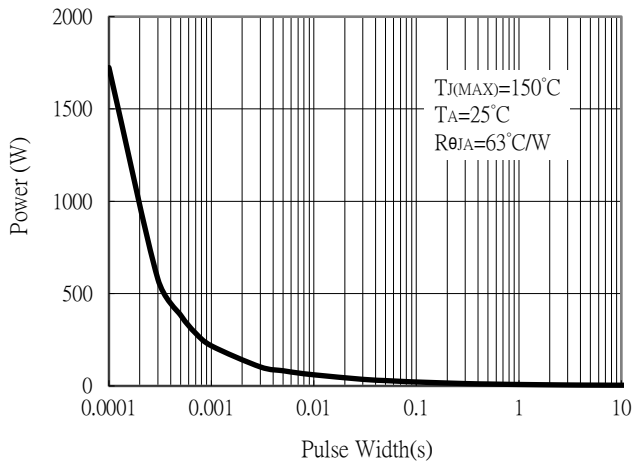
Maximum Drain Current vs Junction Temperature



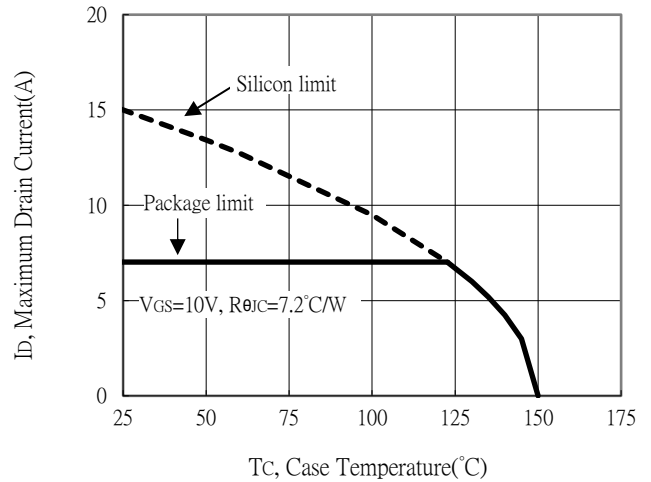


## Typical Characteristics

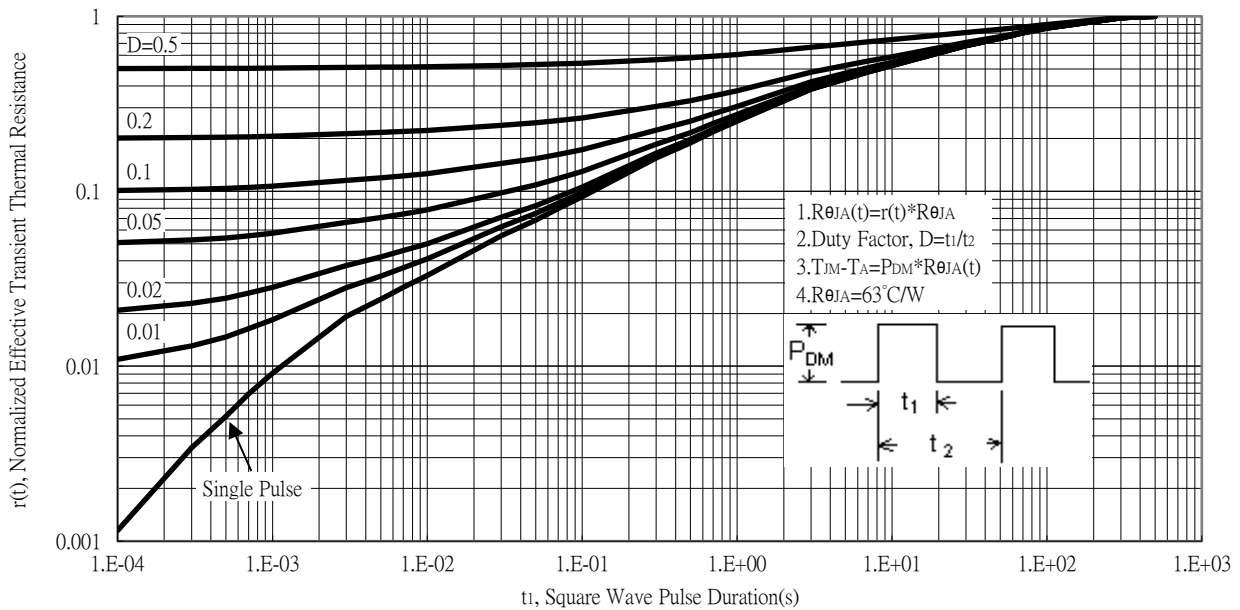
Single Pulse Power Rating, Junction to Ambient



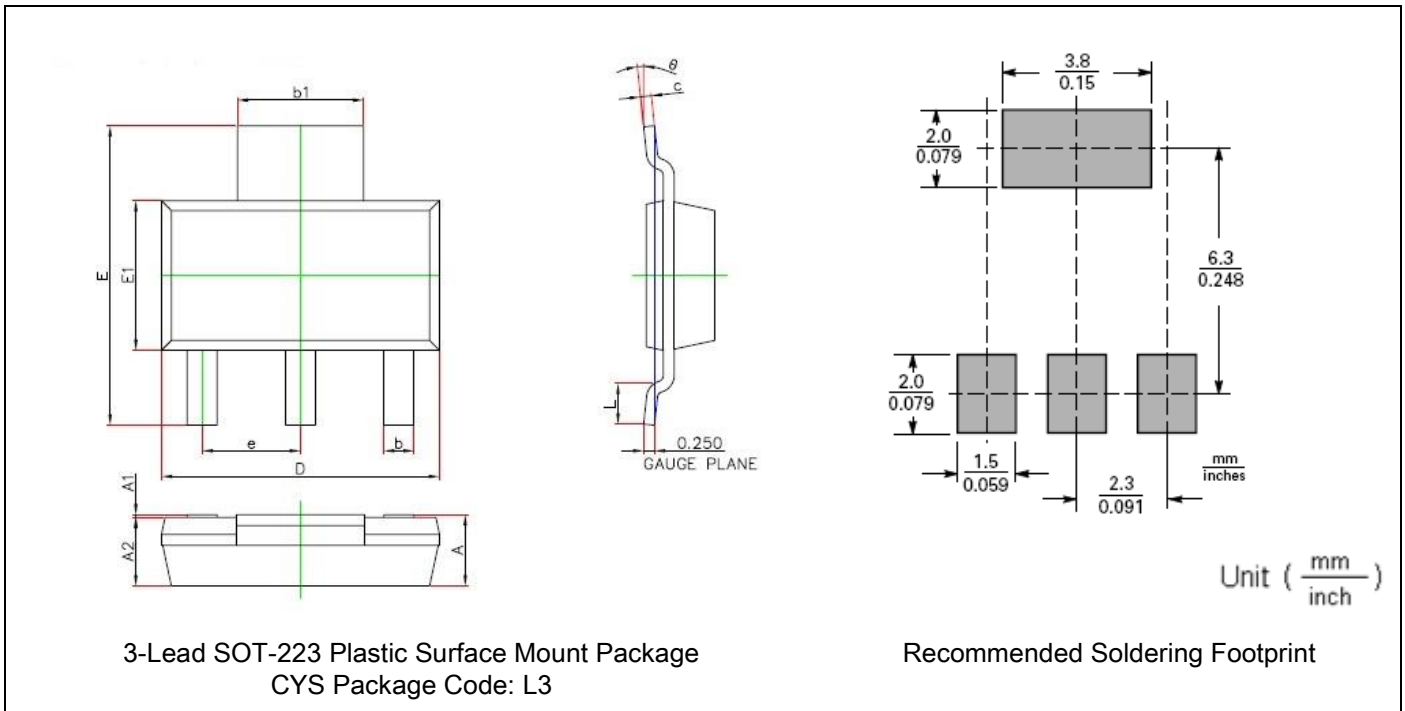
Maximum Drain Current vs Case Temperature



Transient Thermal Response Curves



## SOT-223 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	-	0.071	-	1.800	D	0.248	0.264	6.300	6.700
A1	0.001	0.004	0.020	0.100	E	0.264	0.287	6.700	7.300
A2	0.059	0.067	1.500	1.700	E1	0.130	0.146	3.300	3.700
b	0.026	0.033	0.660	0.840	e	0.091	BSC	2.300	BSC
b1	0.114	0.122	2.900	3.100	L	0.030	-	0.750	-
c	0.009	0.014	0.230	0.350	theta	0°	10°	0°	10°

**Note:**

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

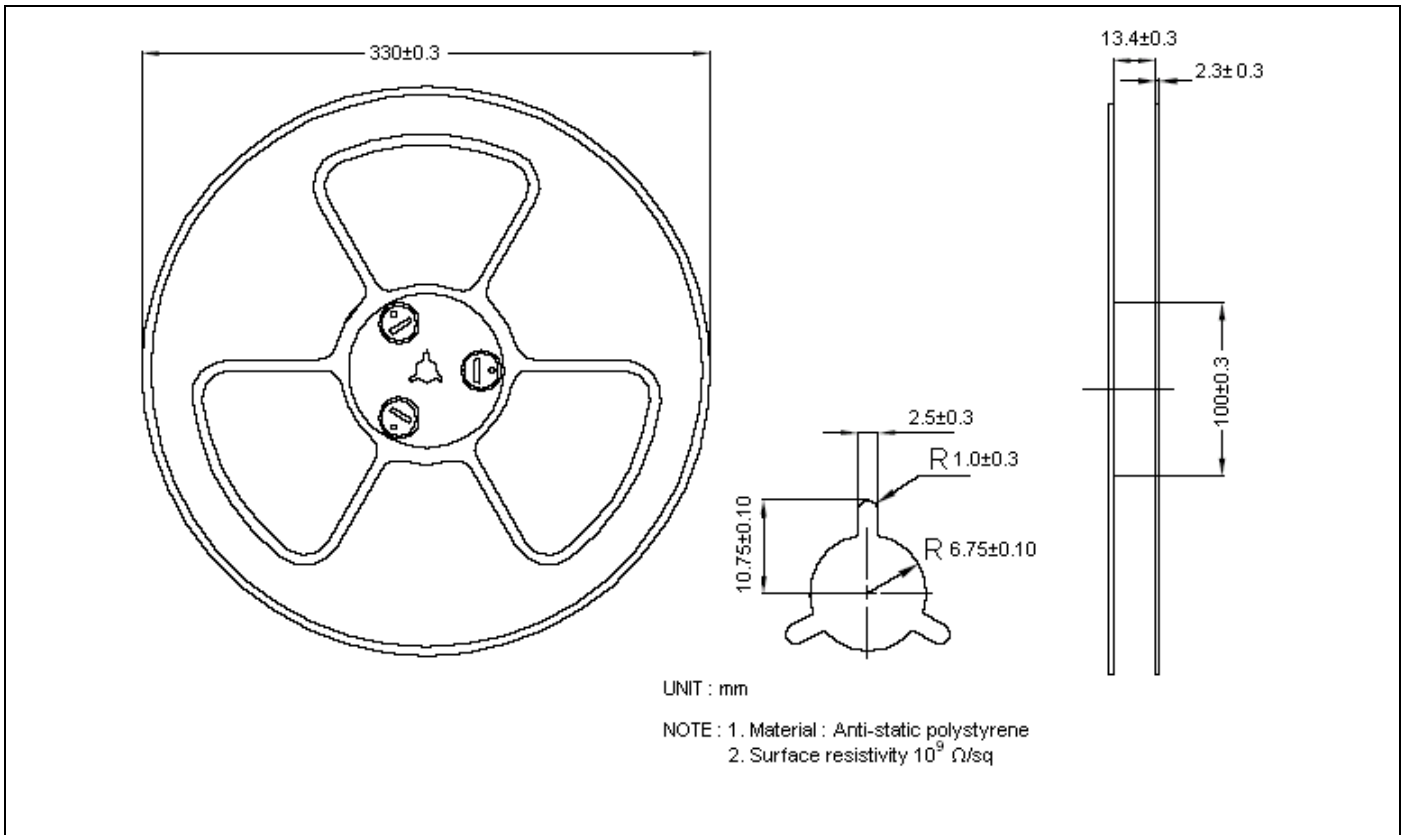
**Material:**

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

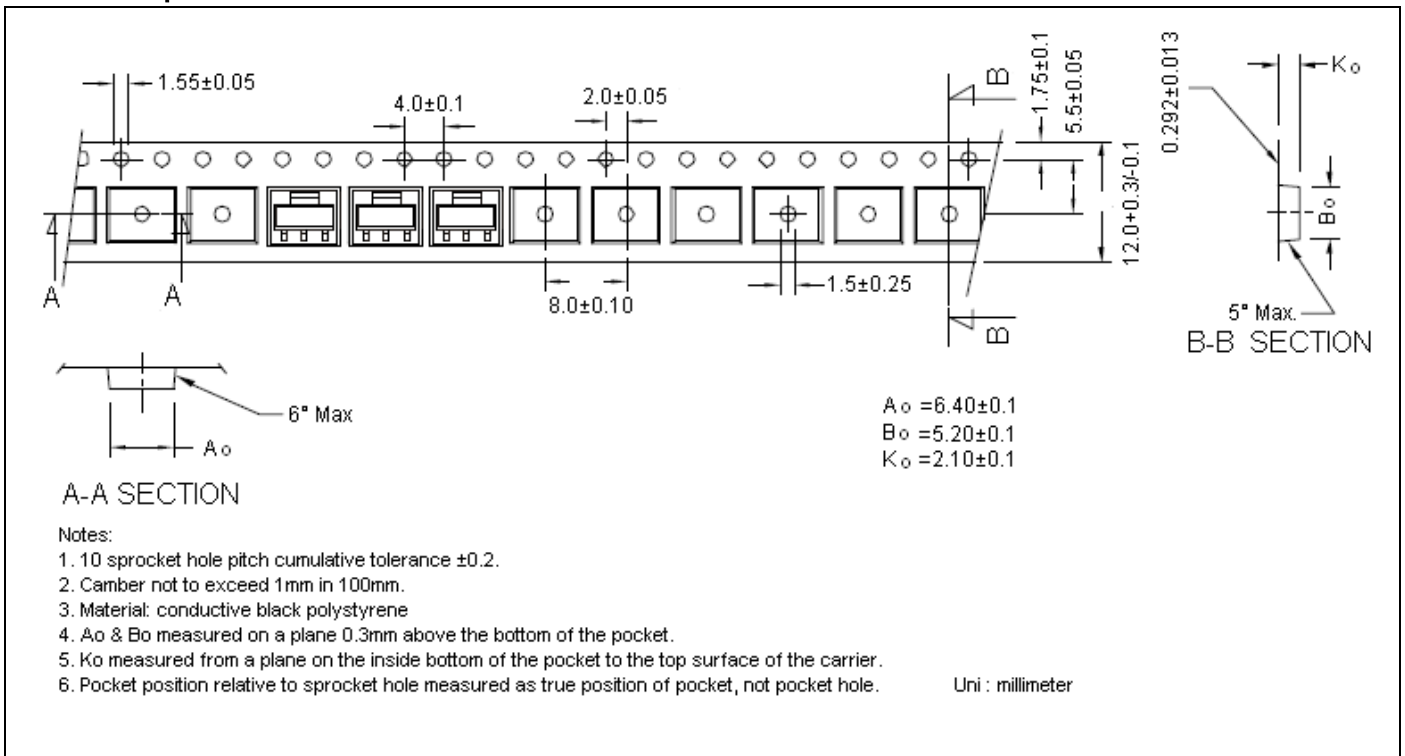
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## Reel Dimension



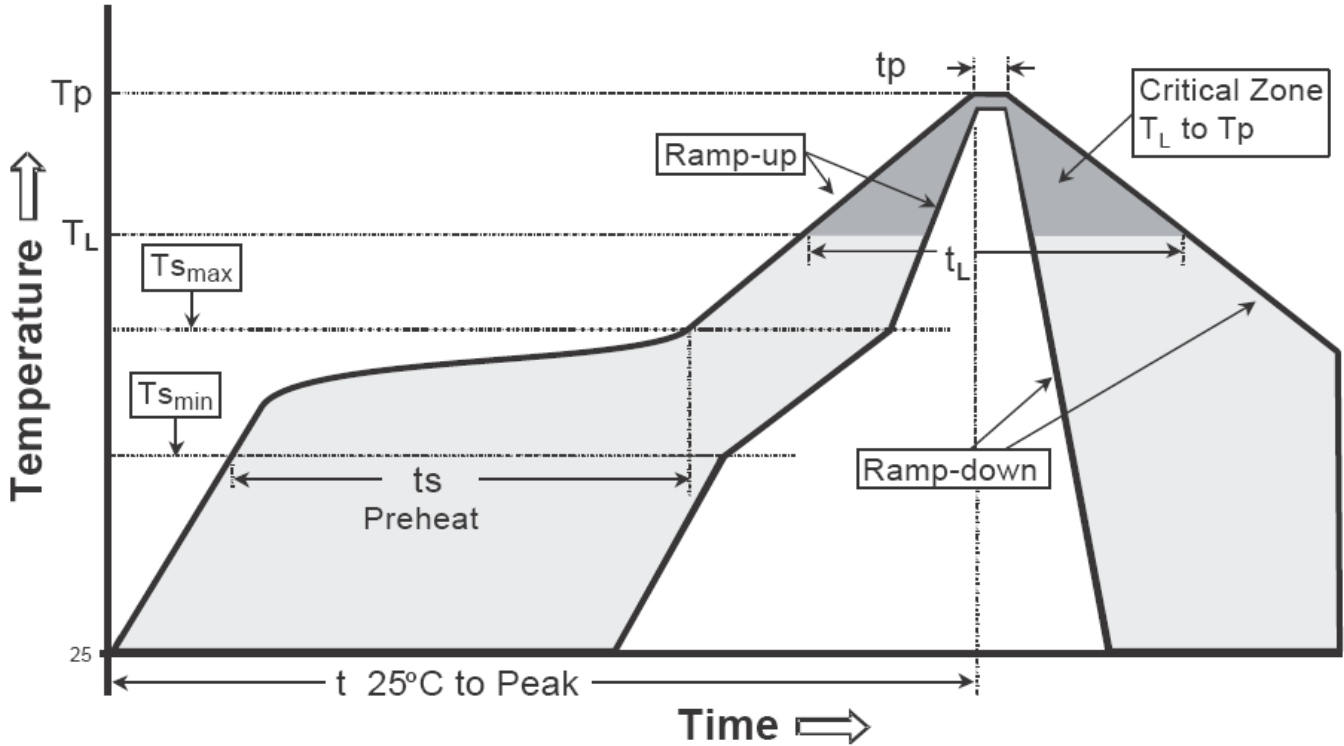
## Carrier Tape Dimension



### Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

### Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>S</sub> max to T <sub>P</sub> )	3°C/second max.	3°C/second max.
Preheat -Temperature Min (T <sub>S</sub> min) -Temperature Max (T <sub>S</sub> max) -Time (t <sub>s</sub> min to t <sub>s</sub> max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T <sub>L</sub> ) -Time (t <sub>L</sub> )	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (t <sub>p</sub> )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

**Note:**

- All temperatures refer to topside of the package, measured on the package body surface.