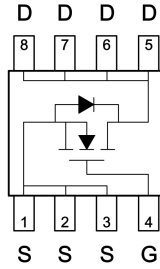
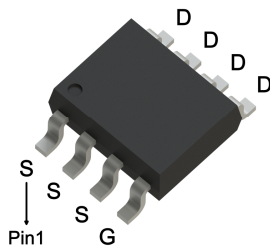


## Product Summary

$BV_{DSS}$	40	V
$R_{DS(ON)}$ typ. @ $V_{GS}=10V, I_D=10A$	6.8	mΩ
$R_{DS(ON)}$ typ. @ $V_{GS}=4.5V, I_D=8A$	10.5	
$I_D$ @ $V_{GS}=10V, T_C=25^\circ C$	20	A
$I_D$ @ $V_{GS}=10V, T_A=25^\circ C$	11	

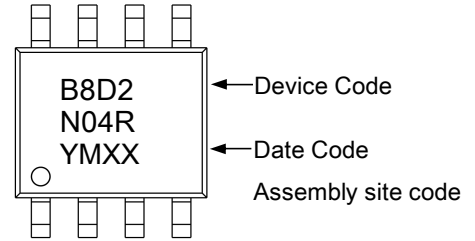
## SOP-8



## Features

- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free

## Marking



YMXX: Date Code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

A: Jan	B: Feb	C: Mar	D: Apr	E: May	F: Jun
G: Jul	H: Aug	J: Sep	K: Oct	L: Nov	M: Dec

XX: Production Serial Number, 01~99

## Ordering Information

Device	Package	Shipping
MTB8D2N04RQ8-0-TF-G	SOP-8	4000pcs / Tape & Reel

0: Product rank, zero for no rank products.

TF: Packing spec, TF: 4000pcs / tape & reel, 13" reel.

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

## Absolute Maximum Ratings ( $T_A=25^\circ C$ )

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$ (silicon limit)	$I_D$	26	A
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$ (package limit)		20	
Continuous Drain Current @ $V_{GS}=10V, T_C=100^\circ C$		16	
Continuous Drain Current @ $V_{GS}=10V, T_A=25^\circ C$		11	
Continuous Drain Current @ $V_{GS}=10V, T_A=70^\circ C$		8.8	
Pulsed Drain Current		$I_{DM}$	
Continuous Body Diode Forward Current @ $T_C=25^\circ C$	$I_S$	9	
Pulsed Body Diode Forward Current @ $T_C=25^\circ C$	$I_{SM}$	36	
Avalanche Current @ $L=0.1mH$	$I_{AS}$	13	
Avalanche Energy @ $L=0.5mH$	$E_{AS}$	12	mJ
Total Power Dissipation	$P_D$	$T_C=25^\circ C$	W
		$T_C=100^\circ C$	
		$T_A=25^\circ C$	
		$T_A=70^\circ C$	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	$^\circ C$
Steady State Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	11	$^\circ C/W$
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	59	



Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)

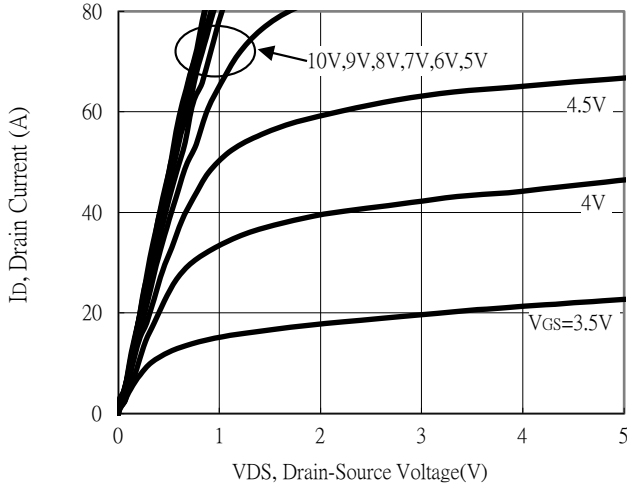
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	40	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1	-	2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	14	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =10A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =32V, V <sub>GS</sub> =0V
R <sub>DS(ON)</sub>	-	6.8	8.8	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =10A
	-	10.5	15		V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A
<b>Dynamic</b>					
C <sub>iSS</sub>	-	575	-	pF	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	290	-		
C <sub>rSS</sub>	-	30	-		
R <sub>g</sub>	-	0.7	-	Ω	f=1MHz
Q <sub>g</sub> *d,e	-	5.4	-	nC	V <sub>DS</sub> =20V, I <sub>D</sub> =10A, V <sub>GS</sub> =4.5V
Q <sub>g</sub> *d,e	-	11	-		
Q <sub>gs</sub> *d,e	-	2.6	-		
Q <sub>gd</sub> *d,e	-	1.9	-		
t <sub>d(ON)</sub> *d,e	-	7	-	ns	V <sub>DS</sub> =20V, I <sub>D</sub> =10A, V <sub>GS</sub> =10V, R <sub>GS</sub> =6Ω
t <sub>r</sub> *d,e	-	13	-		
t <sub>d(OFF)</sub> *d,e	-	20	-		
t <sub>f</sub> *d,e	-	6.5	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *d	-	0.84	1.2	V	I <sub>S</sub> =10A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	12	-	ns	I <sub>F</sub> =10A, di/dt=100A/μs
Q <sub>rr</sub>	-	2.7	-	nC	

Note:

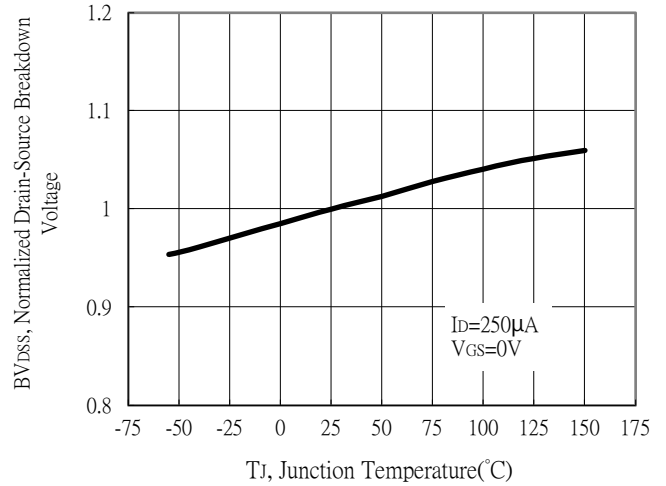
- \*a. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- \*b. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.
- \*d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- \*e. Independent of operating temperature.

## Typical Characteristics

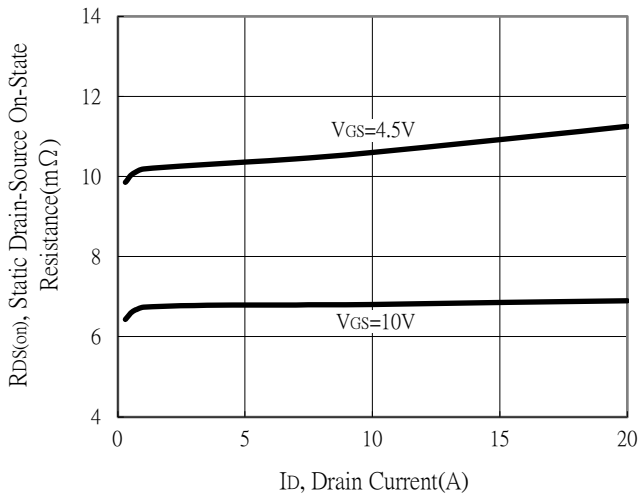
Typical Output Characteristics



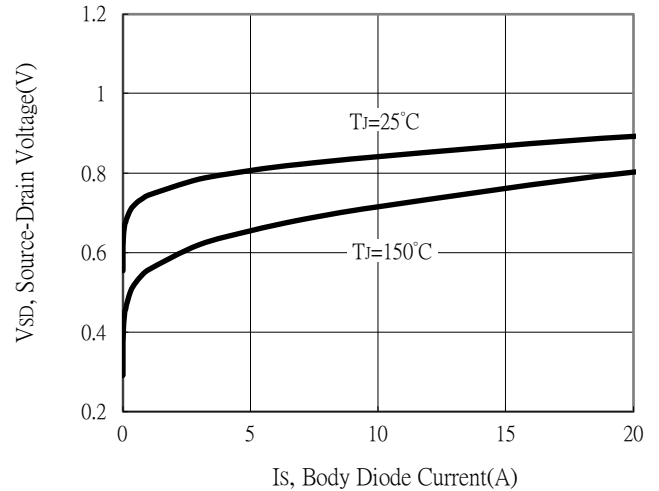
Breakdown Voltage vs Junction Temperature



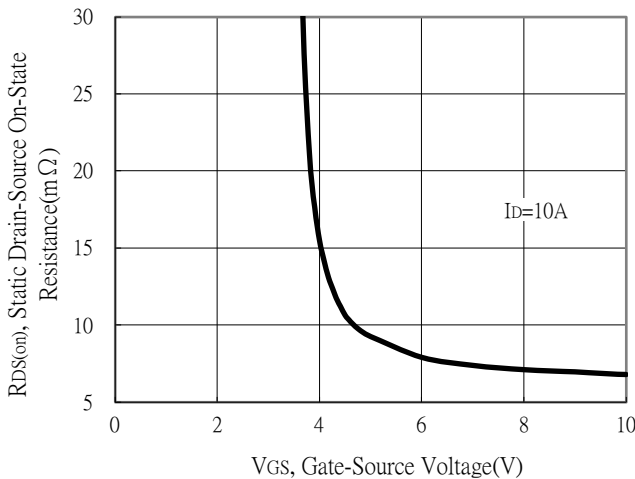
Static Drain-Source On-State resistance vs Drain Current



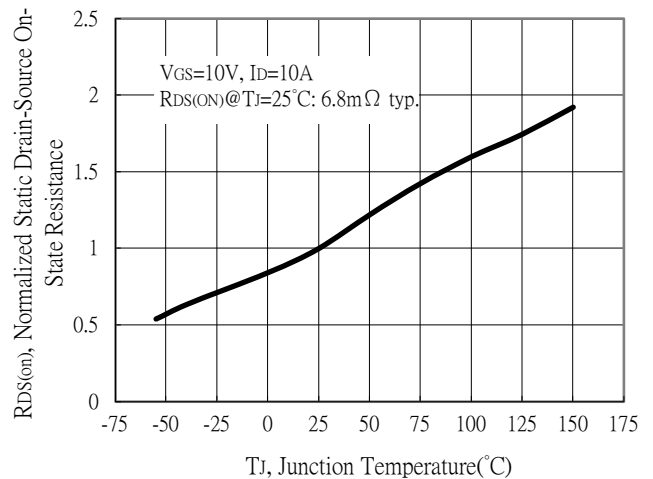
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



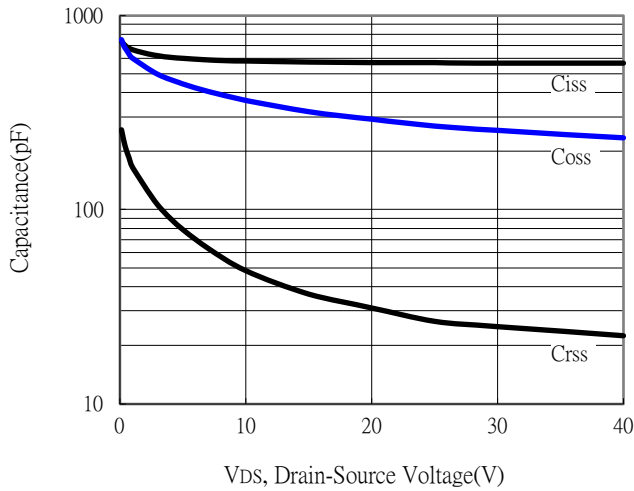
Drain-Source On-State Resistance vs Junction Temperature



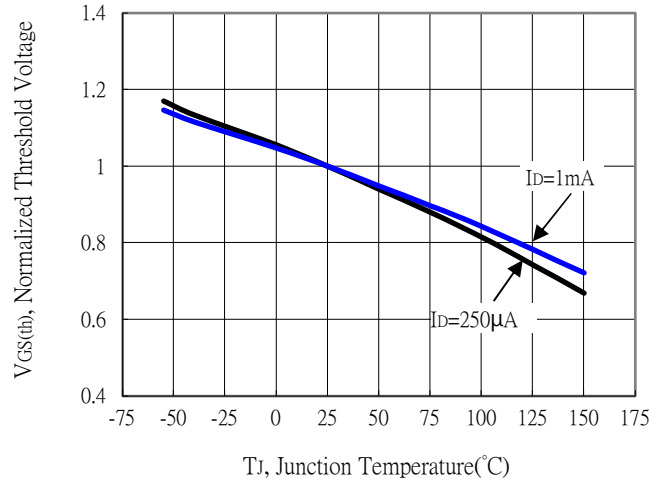


### Typical Characteristics

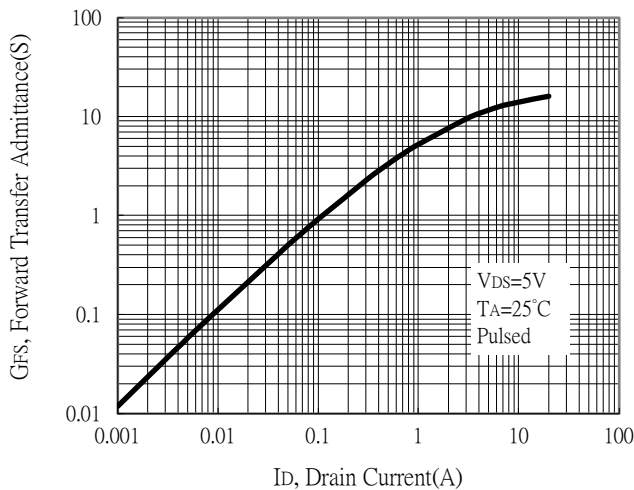
Capacitance vs Drain-to-Source Voltage



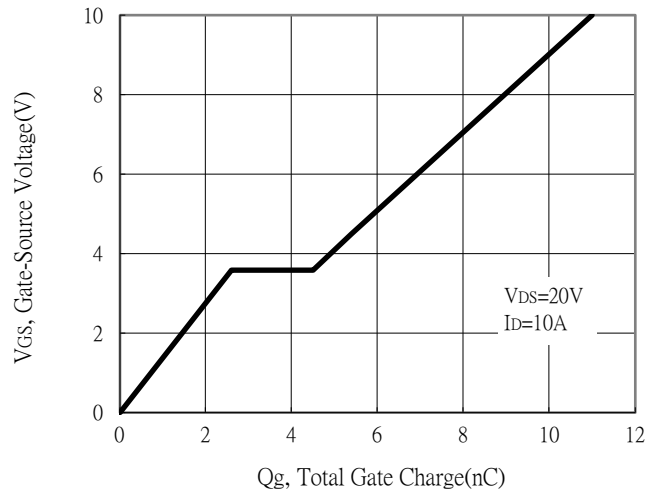
Threshold Voltage vs Junction Temperature



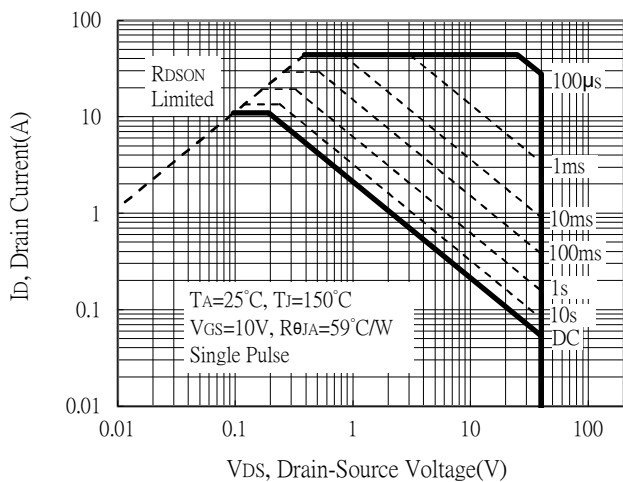
Forward Transfer Admittance vs Drain Current



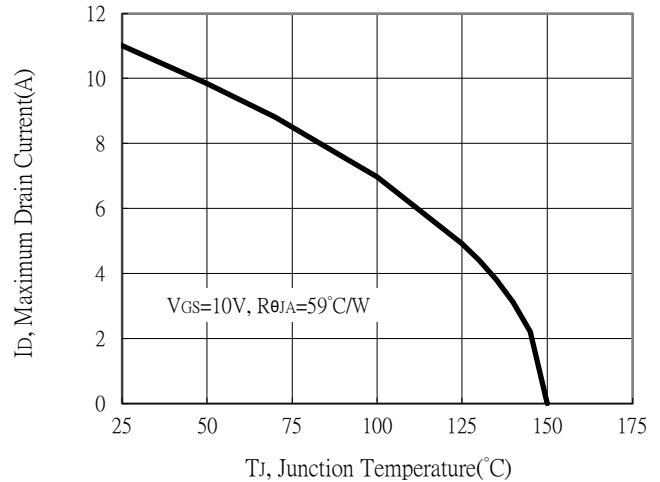
Gate Charge Characteristics



Maximum Safe Operating Area

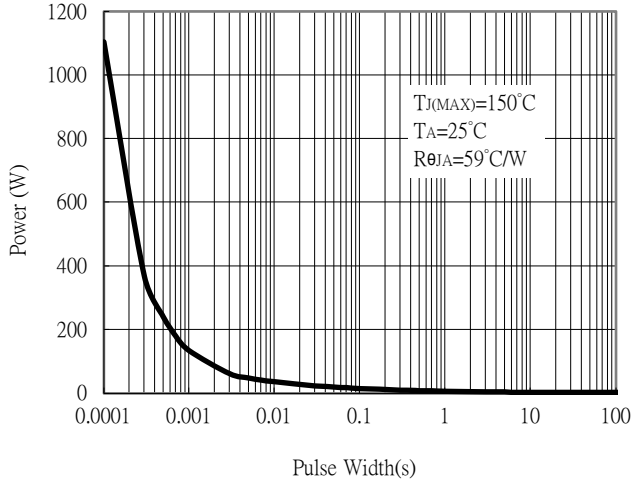


Maximum Drain Current vs Junction Temperature

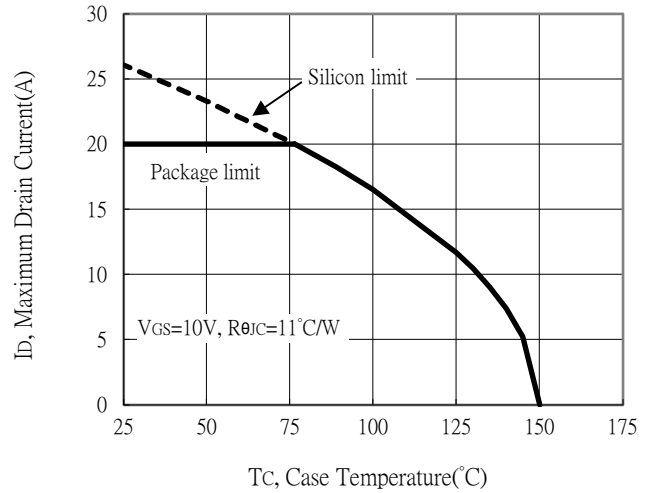


## Typical Characteristics

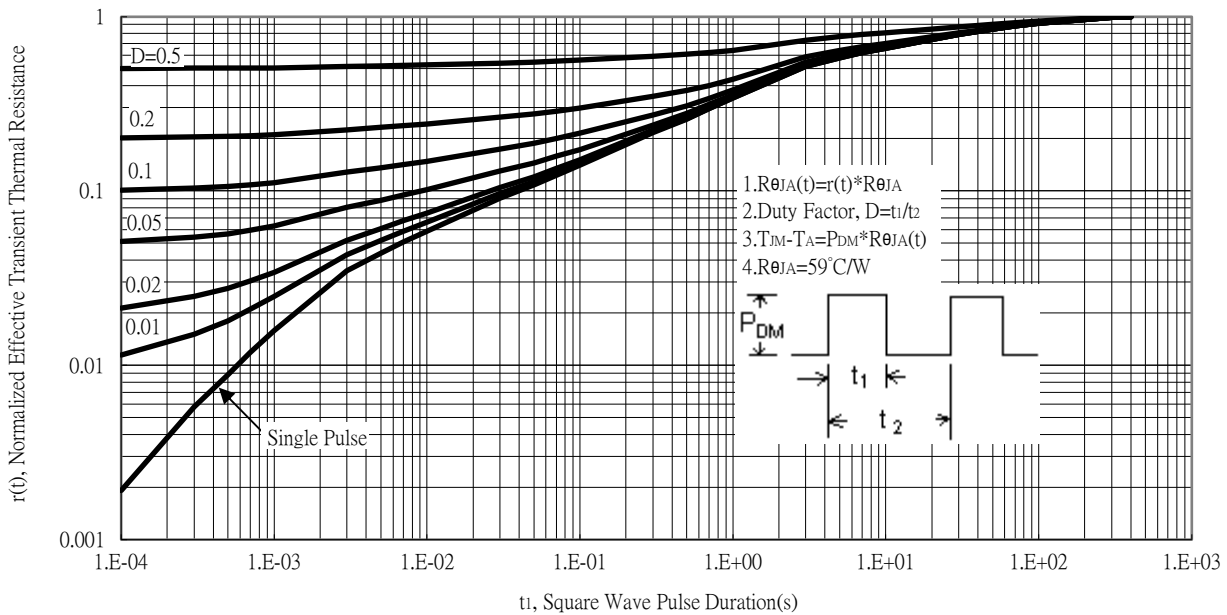
Single Pulse Power Rating, Junction to Ambient



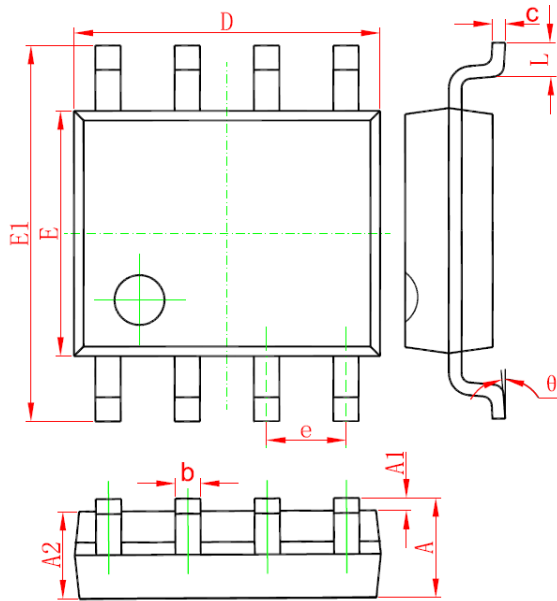
Maximum Drain Current vs Case Temperature



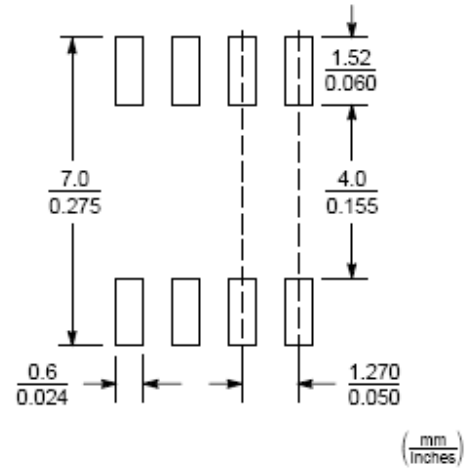
Transient Thermal Response Curves



## SOP-8 Dimension



8-Lead SOP-8 Plastic Package  
CYStek Package Code: Q8



Recommended Soldering Footprint

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Min.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	1.1270	TYP.	0.050	TYP.
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	θ	0°	8°	0°	8°
D	4.700	5.100	0.185	0.200					

**Note:**

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

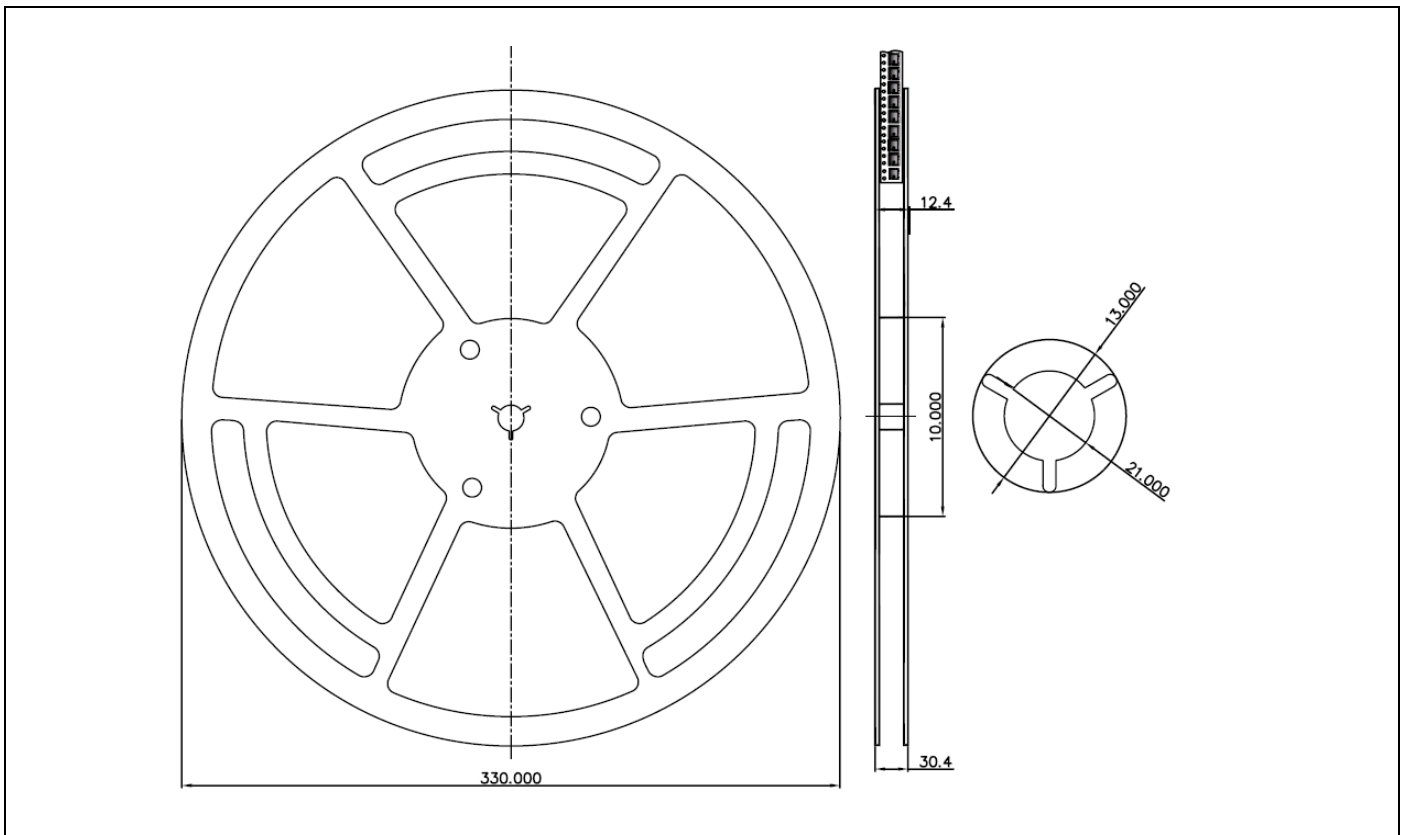
**Material:**

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

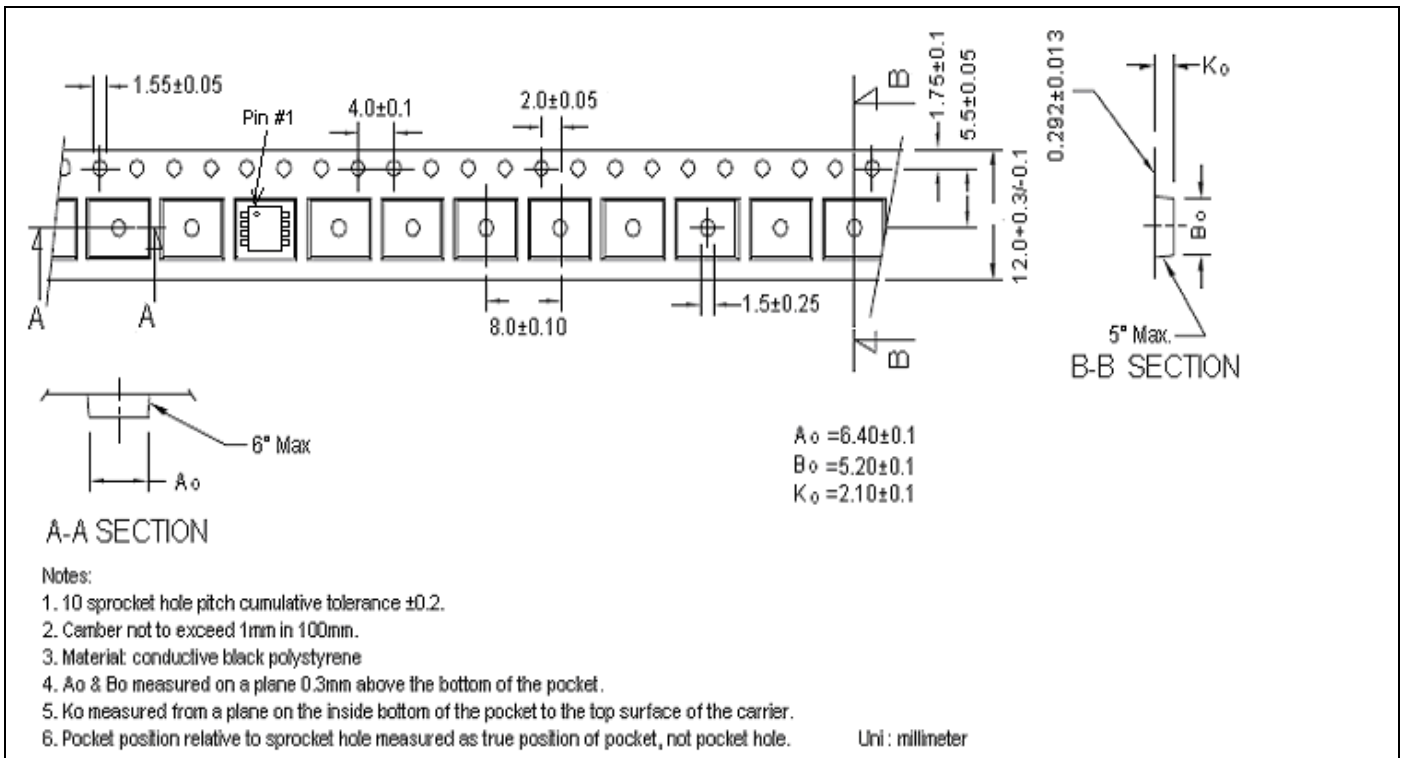
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## Reel Dimension



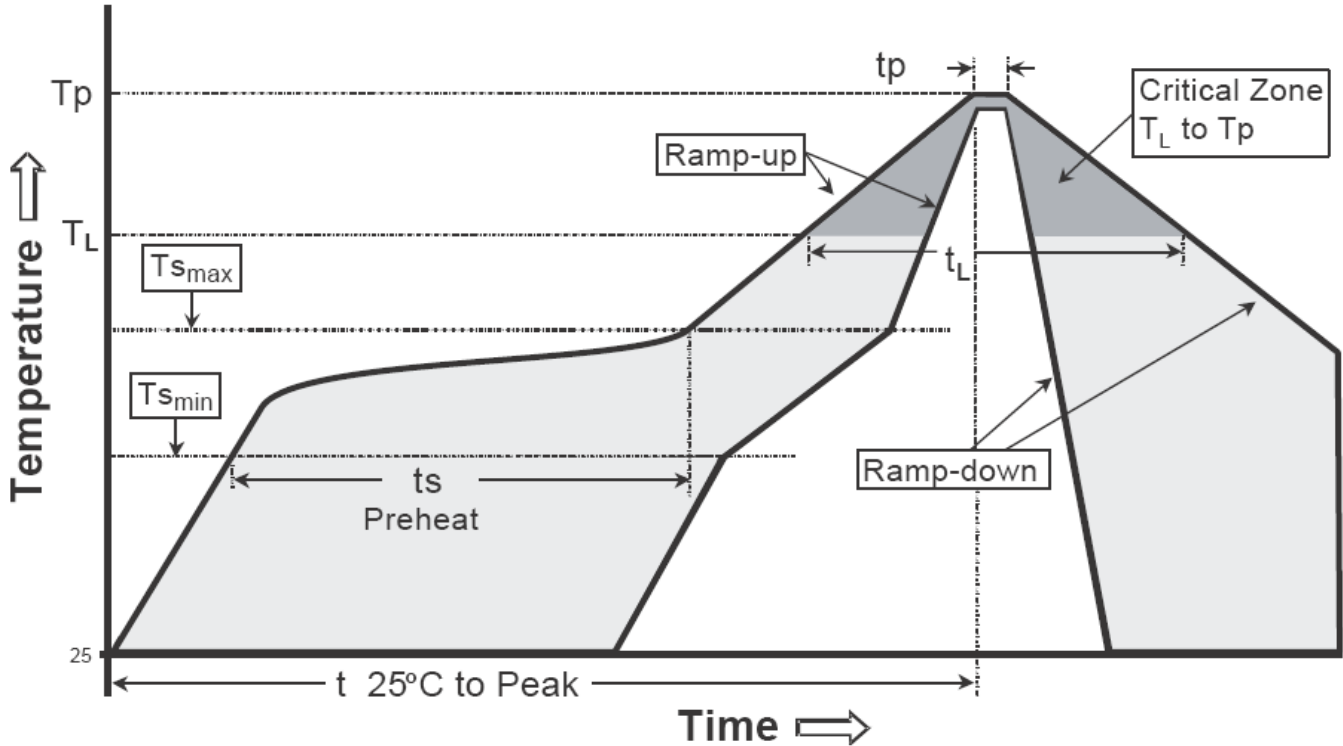
## Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>S</sub> max to T <sub>P</sub> )	3°C/second max.	3°C/second max.
Preheat -Temperature Min (T <sub>S</sub> min) -Temperature Max (T <sub>S</sub> max) -Time (t <sub>S</sub> min to t <sub>S</sub> max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T <sub>L</sub> ) -Time (t <sub>L</sub> )	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (t <sub>P</sub> )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

**Note:**

- All temperatures refer to topside of the package, measured on
- For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.