

N-Channel Enhancement Mode Power MOSFET

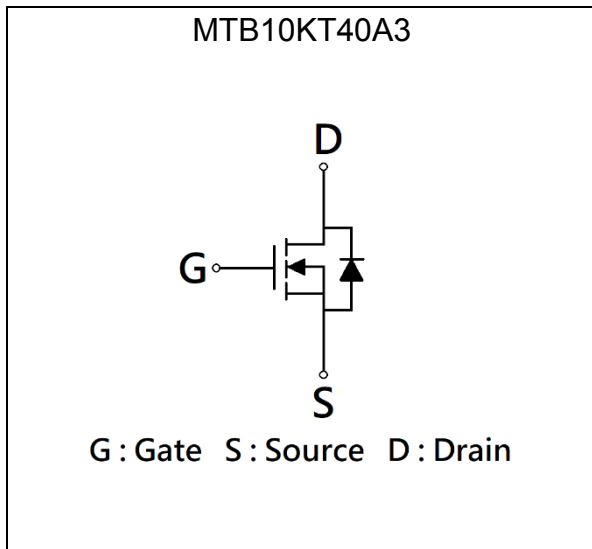
MTB10KT40A3

Features

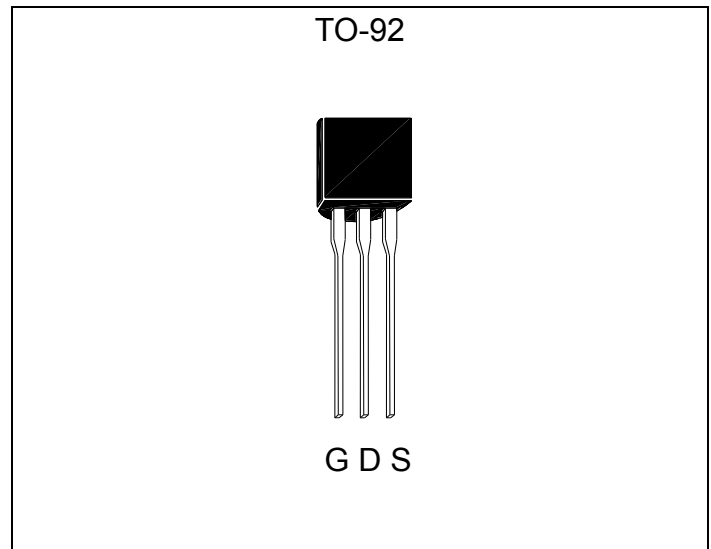
- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic

BV_{DSS}	400V
$I_D @ V_{GS}=10V, T_A=25^\circ C$	0.16A
$R_{DS(ON) typ. @ V_{GS}=7V, I_D=0.12A}$	10Ω
$R_{DS(ON) typ. @ V_{GS}=5V, I_D=0.12A}$	10Ω

Equivalent Circuit

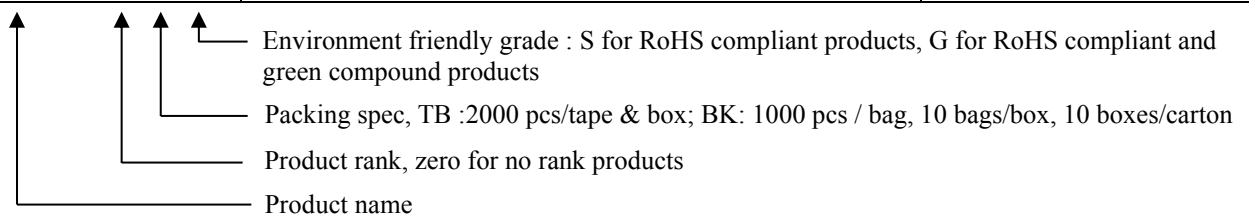


Outline



Ordering Information

Device	Package	Shipping
MTB10KT40A3-0-TB-G	TO-92 (Pb-free lead plating and RoHS compliant package)	2000 pcs / Tape & Box
MTB10KT40A3-0-BK-G		1000 pcs/ bag, 10 bags/box, 10 boxes/carton





Absolute Maximum Ratings (T_A=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V _{DS}	400	V	
Gate-Source Voltage	V _{GS}	±20		
Continuous Drain Current @ V _{GS} =10V, T _A =25°C	I _D	0.16	A	
Continuous Drain Current @ V _{GS} =10V, T _A =70°C		0.13		
Pulsed Drain Current	I _{DM}	0.48		
Continuous Body Diode Forward Current @ T _A =25°C	I _S	0.16		
Total Power Dissipation	P _D	T _A =25°C	0.8	W
		T _A =70°C	0.5	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C	

Thermal Data

Parameter	Symbol	Steady State	Unit
Thermal Resistance, Junction-to-ambient	R _{θJA}	155	°C/W

Note:

- *a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *b. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.



Electrical Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	400	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	-	2.5		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	0.65	-	S	V _{DS} =10V, I _D =0.1A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =320V, V _{GS} =0V
R _{DS(ON)}	-	10	13	Ω	V _{GS} =7V, I _D =120mA
	-	10	14		V _{GS} =5V, I _D =120mA
Dynamic					
C _{iss}	-	79	-	pF	V _{DS} =200V, V _{GS} =0V, f=1MHz
C _{oss}	-	8	-		
C _{rss}	-	6	-		
R _g	-	5.2	-	Ω	f=1MHz
Q _g *1, 2	-	3.5	-	nC	V _{DS} =200V, I _D =0.2A, V _{GS} =10V
Q _{gs} *1, 2	-	0.7	-		
Q _{gd} *1, 2	-	0.7	-		
t _{d(ON)} *1, 2	-	3.3	-	ns	V _{DS} =200V, I _D =0.1A, V _{GS} =10V, R _{GS} =25 Ω
t _r *1, 2	-	7	-		
t _{d(OFF)} *1, 2	-	51	-		
t _f *1, 2	-	49	-		
Source-Drain Diode					
V _{SD} *1	-	0.75	1.2	V	I _S =0.1A, V _{GS} =0V
t _{rr}	-	77	-	ns	I _F =0.5A, dI _F /dt=100A/μs
Q _{rr}	-	59	-	nC	

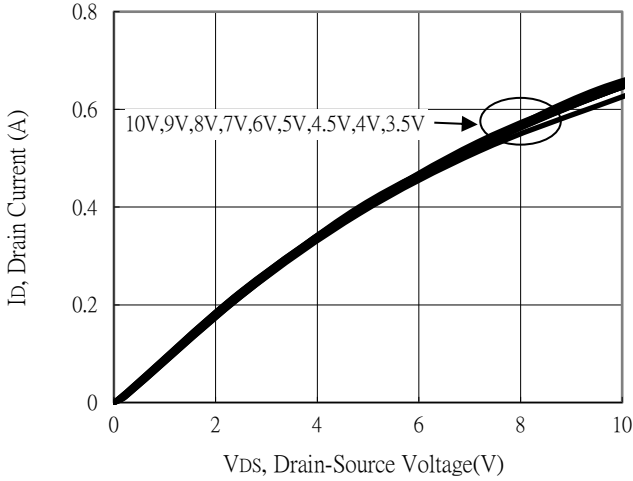
Note:

*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

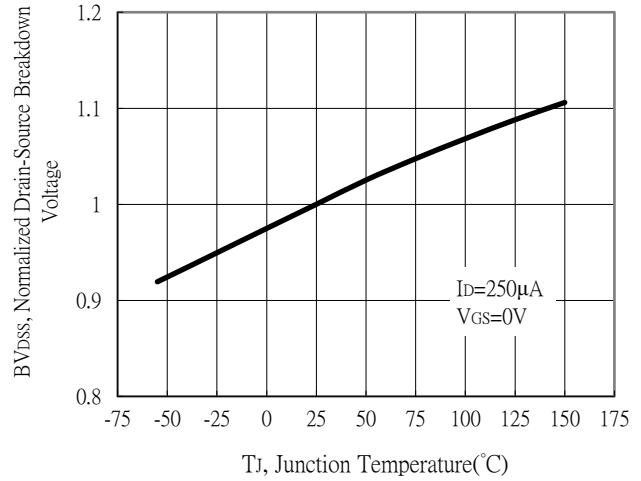
*2. Independent of operating temperature

Typical Characteristics

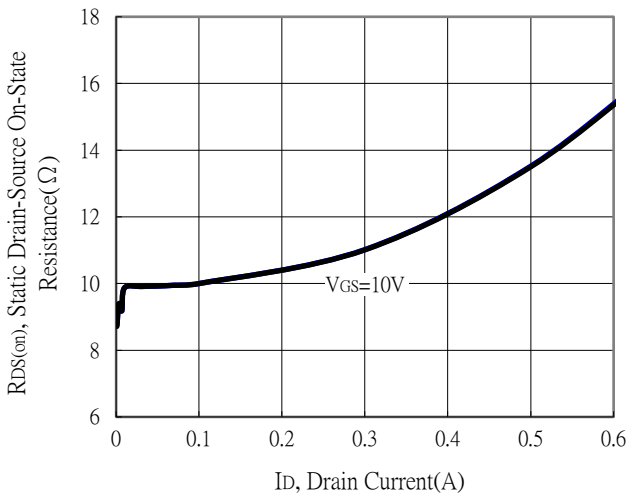
Typical Output Characteristics



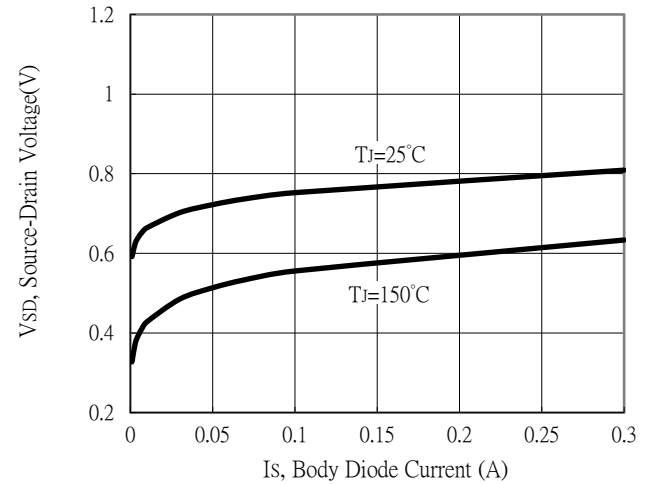
Breakdown Voltage vs Ambient Temperature



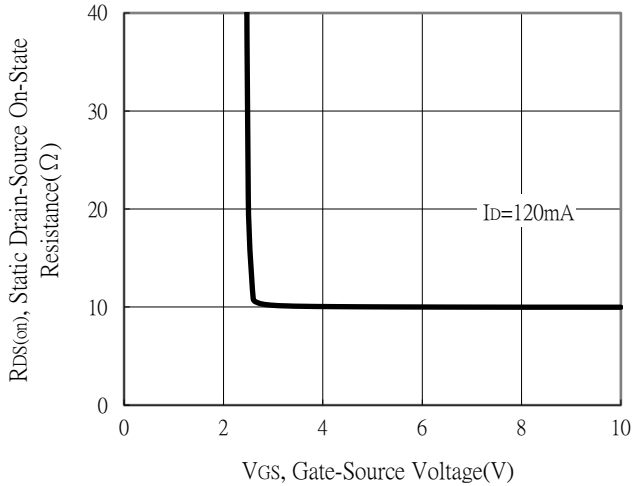
Static Drain-Source On-State resistance vs Drain Current



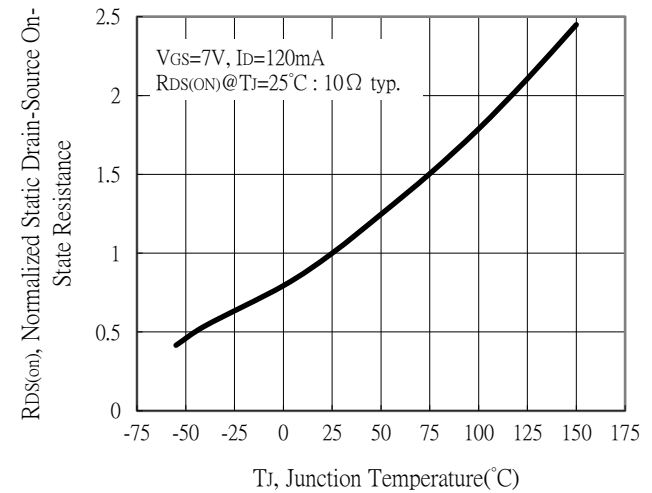
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



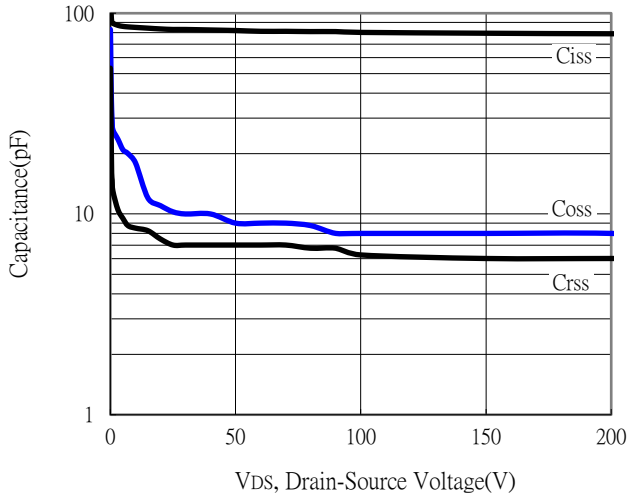
Drain-Source On-State Resistance vs Junction Temperature



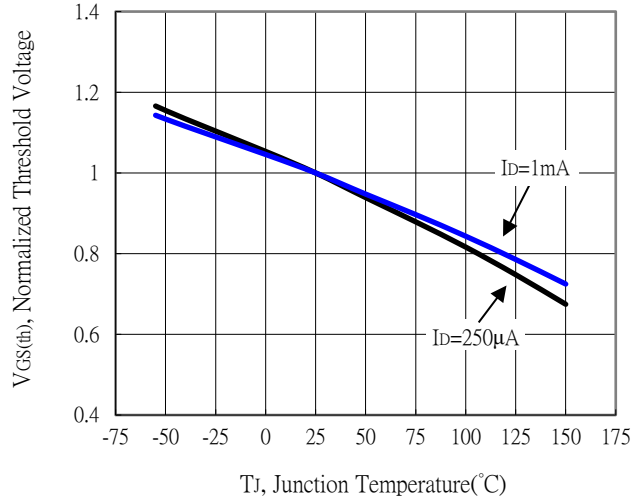


Typical Characteristics (Cont.)

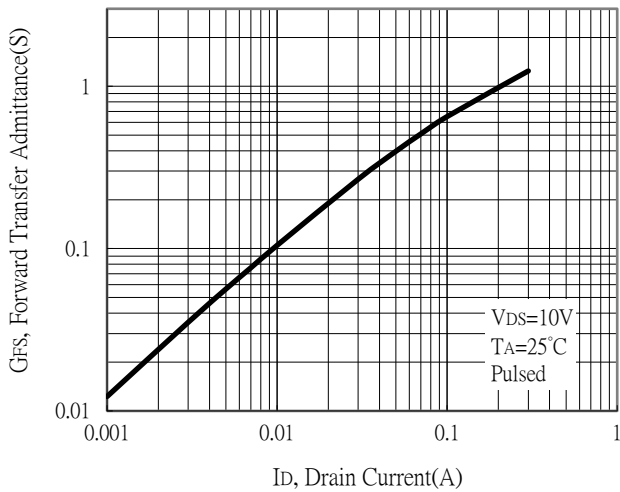
Capacitance vs Drain-to-Source Voltage



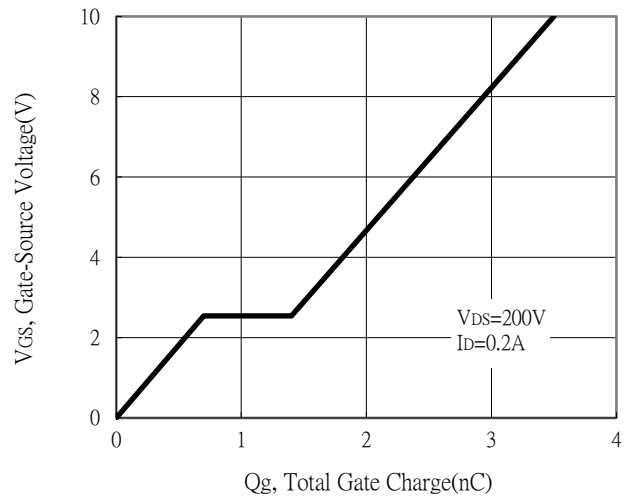
Threshold Voltage vs Junction Temperature



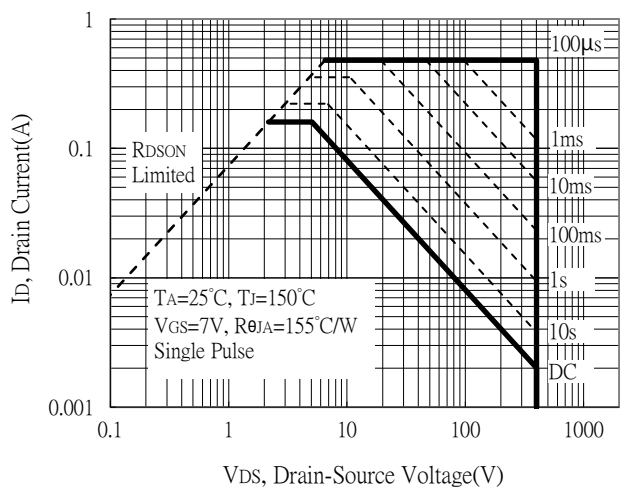
Forward Transfer Admittance vs Drain Current



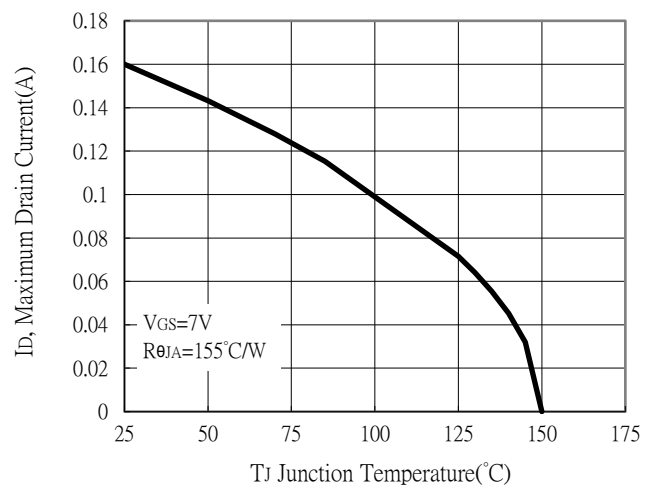
Gate Charge Characteristics



Maximum Safe Operating Area

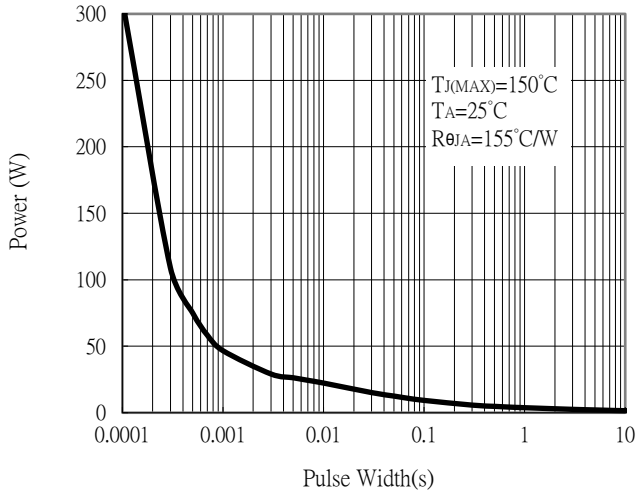


Maximum Drain Current vs Junction Temperature

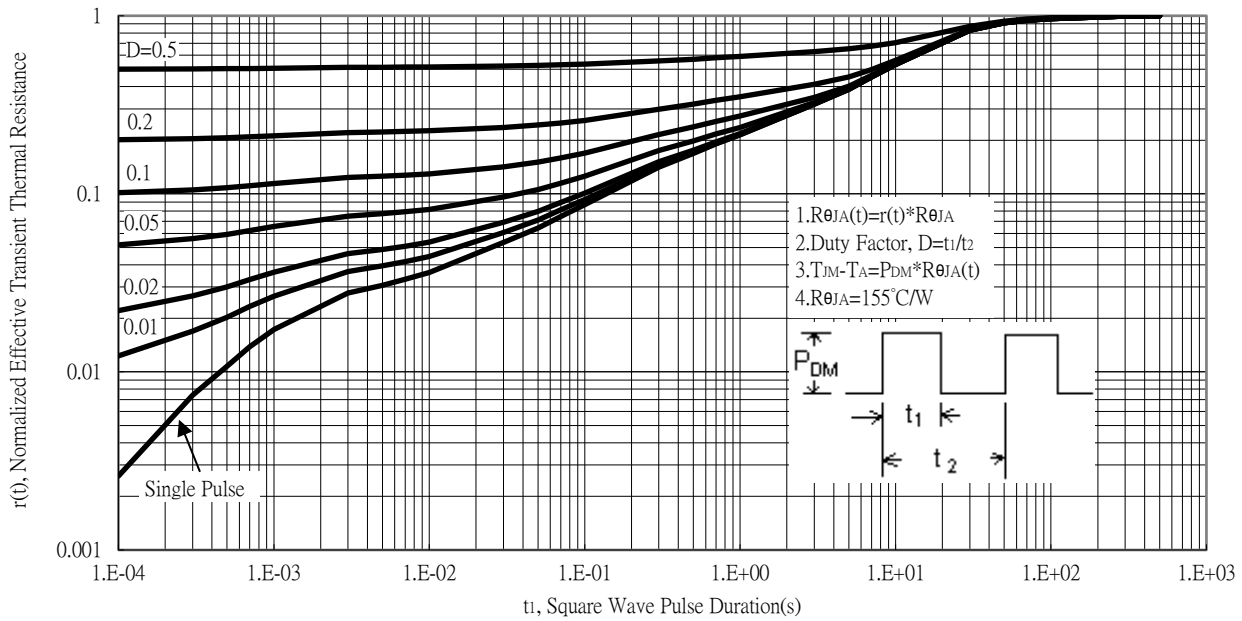


Typical Characteristics (Cont.)

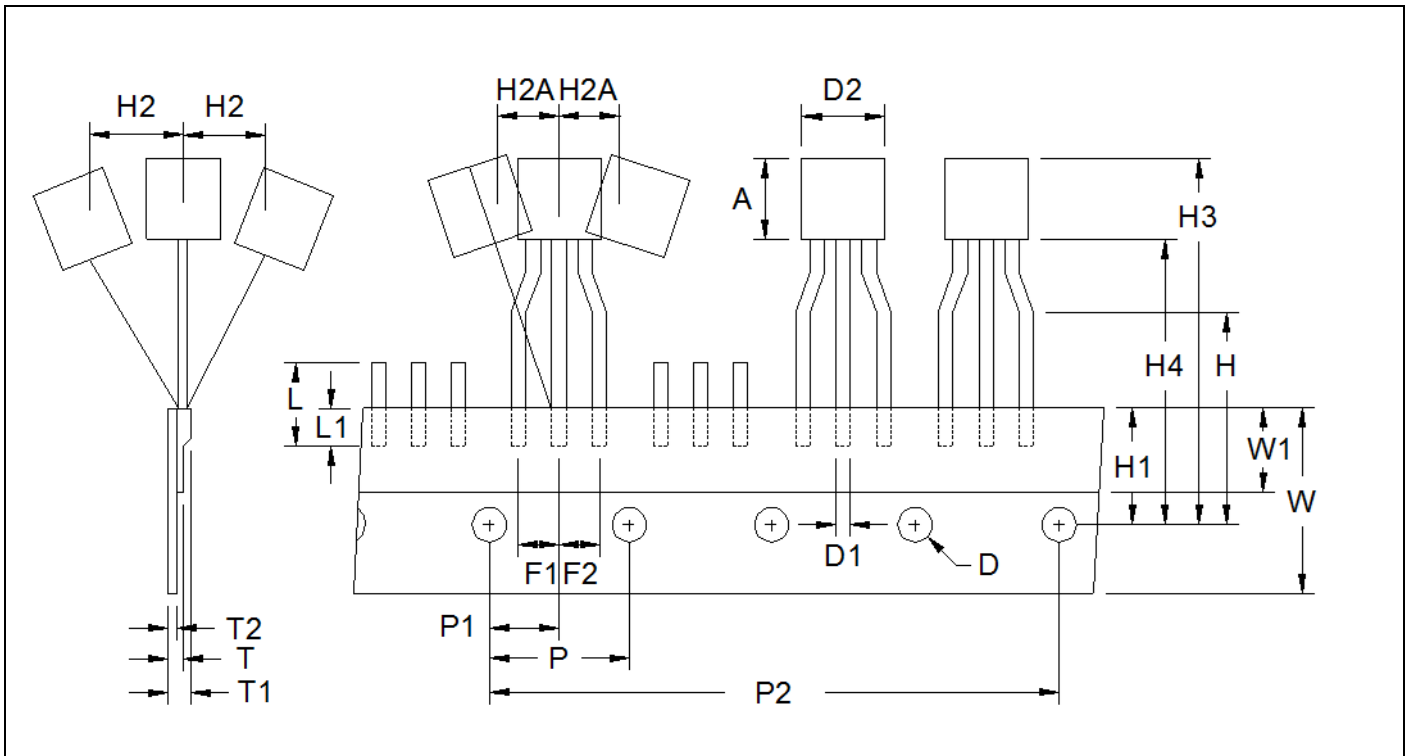
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



TO-92 Taping Outline



DIM	Item	Millimeters	
		Min.	Max.
A	Component body height	4.33	4.83
D	Tape Feed Diameter	3.80	4.20
D1	Lead Diameter	0.36	0.53
D2	Component Body Diameter	4.33	4.83
F1,F2	Component Lead Pitch	2.40	2.90
F1,F2	F1-F2	-	±0.3
H	Height Of Seating Plane	15.50	16.50
H1	Feed Hole Location	8.50	9.50
H2	Front To Rear Deflection	-	1
H2A	Deflection Left Or Right	-	1
H3	Component Height	-	27
H4	Feed Hole To Bottom Of Component	-	21
L	Lead Length After Component Removal	-	11
L1	Lead Wire Enclosure	2.50	-
P	Feed Hole Pitch	12.50	12.90
P1	Center Of Seating Plane Location	5.95	6.75
P2	4 Feed Hole Pitch	50.30	51.30
T	Over All Tape Thickness	-	0.55
T1	Total Taped Package Thickness	-	1.42
T2	Carrier Tape Thickness	0.36	0.68
W	Tape Width	17.50	19.00
W1	Adhesive Tape Width	5.00	7.00
-	20 pcs Pitch	253	255

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

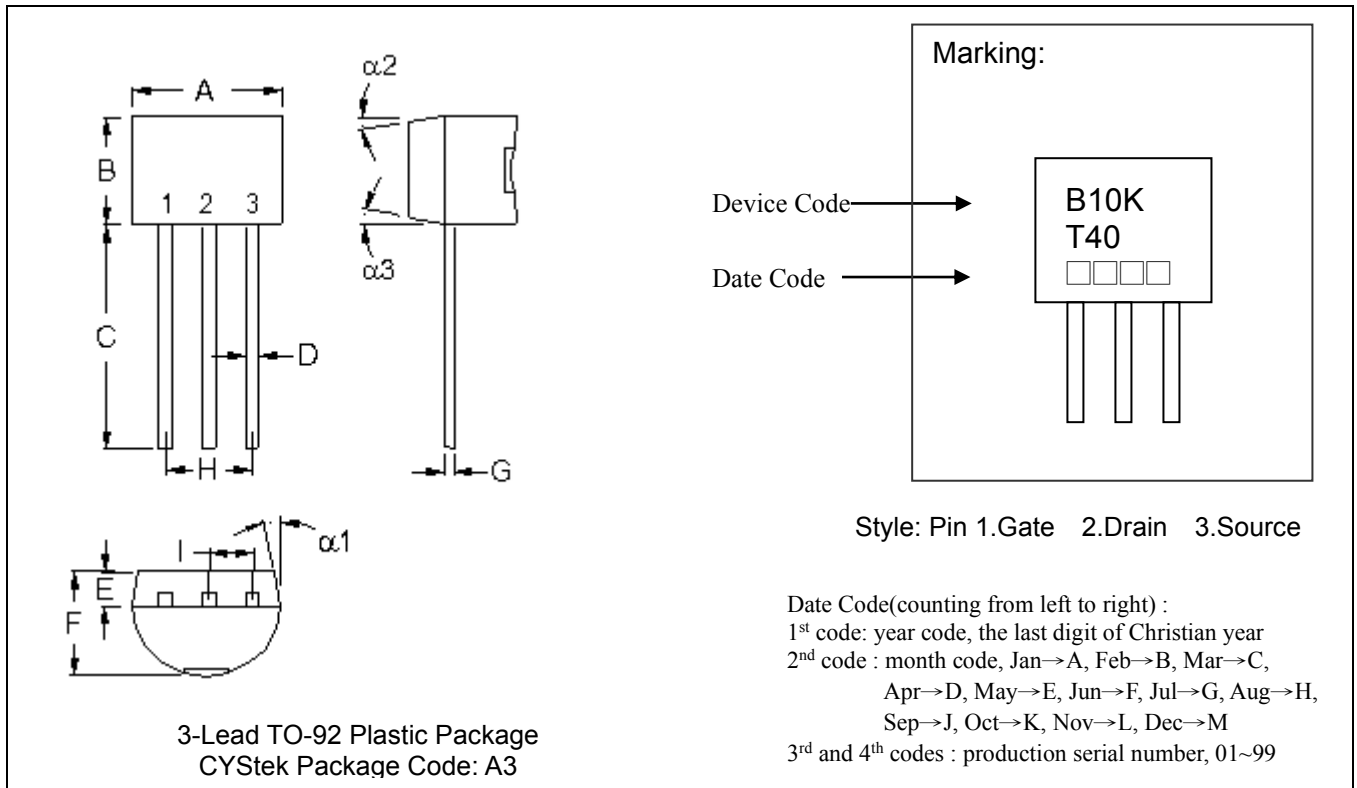
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-92 Dimension



3-Lead TO-92 Plastic Package
 CYStek Package Code: A3

Style: Pin 1.Gate 2.Drain 3.Source

Date Code(counting from left to right) :
 1st code: year code, the last digit of Christian year
 2nd code : month code, Jan→A, Feb→B, Mar→C,
 Apr→D, May→E, Jun→F, Jul→G, Aug→H,
 Sep→J, Oct→K, Nov→L, Dec→M
 3rd and 4th codes : production serial number, 01~99

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1704	0.1902	4.33	4.83	G	0.0142	0.0220	0.36	0.56
B	0.1704	0.1902	4.33	4.83	H	-	*0.1000	-	*2.54
C	0.5000	-	12.70	-	I	-	*0.0500	-	*1.27
D	0.0142	0.0220	0.36	0.56	$\alpha 1$	-	*5°	-	*5°
E	-	*0.0500	-	*1.27	$\alpha 2$	-	*2°	-	*2°
F	0.1323	0.1480	3.36	3.76	$\alpha 3$	-	*2°	-	*2°

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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