

**100V N-Channel Enhancement Mode MOSFET**

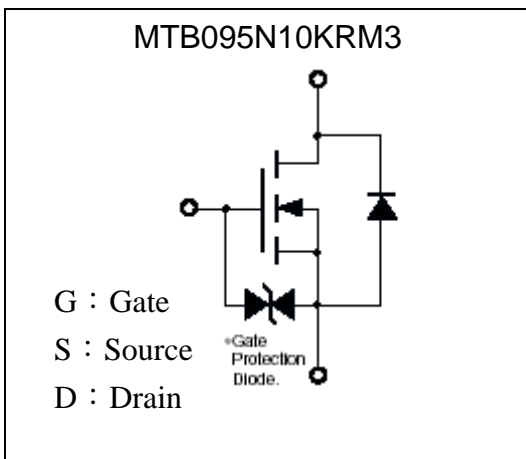
# MTB095N10KRM3

BV <sub>DSS</sub>	100V
I <sub>D</sub> @V <sub>GS</sub> =10V, T <sub>A</sub> =25°C	2.9A
R <sub>DS(on)</sub> @V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A	95.8mΩ (typ)
R <sub>DS(on)</sub> @V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A	121.6mΩ (typ)

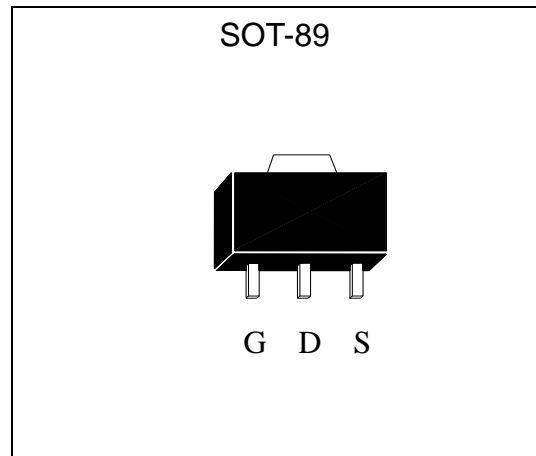
**Features**

- Simple drive requirement
- Small package outline
- ESD protected gate
- Pb-free lead plating and halogen-free package

**Symbol**

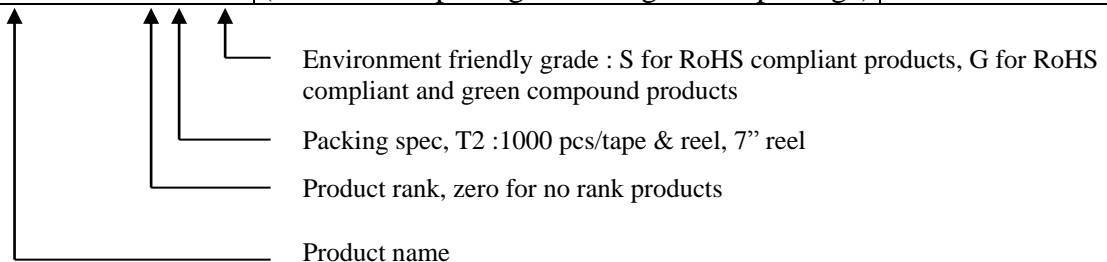


**Outline**



**Ordering Information**

Device	Package	Shipping
MTB095N10KRM3-0-T2-G	SOT-89 (Pb-free lead plating and halogen-free package)	1000 pcs / Tape & Reel





**Absolute Maximum Ratings (Ta=25°C)**

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	
Continuous Drain Current @ TA=25°C, VGS=10V (Note 3)	I <sub>D</sub>	2.9	A
Continuous Drain Current @ TA=70°C, VGS=10V (Note 3)		2.3	
Pulsed Drain Current (Notes 1, 2)	I <sub>DM</sub>	16	
Maximum Power Dissipation @ TA=25°C (Note 3)	P <sub>D</sub>	2	W
Linear Derating Factor		0.016	W/°C
Operating Junction and Storage Temperature Range	T <sub>j</sub> ; T <sub>stg</sub>	-55~+150	°C

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient, max (Note 3)	R <sub>θJA</sub>	62.5	°C/W
Thermal Resistance, Junction-to-Case, max	R <sub>θJC</sub>	20	

- Note : 1. Pulse width limited by maximum junction temperature.  
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%.  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board; 270°C/W when mounted on minimum copper pad

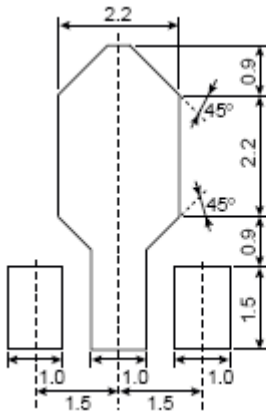
**Electrical Characteristics (Tj=25°C, unless otherwise noted)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1.0	-	2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
	-	-	25		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V(T <sub>j</sub> =70°C)
*R <sub>DSON</sub>	-	95.8	126	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =1.5A
	-	121.6	195		V <sub>GS</sub> =4.5V, I <sub>D</sub> =1A
*G <sub>FS</sub>	-	2.9	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =1A
<b>Dynamic</b>					
C <sub>iSS</sub>	-	301	452	pF	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oSS</sub>	-	29	44		
C <sub>rSS</sub>	-	8	12		
t <sub>d(ON)</sub>	-	5.2	7.8	ns	V <sub>DS</sub> =50V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V, R <sub>G</sub> =1Ω
t <sub>r</sub>	-	16.6	24.9		
t <sub>d(OFF)</sub>	-	17	25.5		
t <sub>f</sub>	-	5.2	7.8		

Qg	-	6.7	10	nC	V <sub>DS</sub> =80V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V
Qgs	-	1.1	1.7		
Qgd	-	1.6	2.4		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	2.3	A	
*I <sub>SM</sub>	-	-	9.2		
*V <sub>SD</sub>	-	0.81	1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =1A
T <sub>rr</sub>	-	18.3	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =2A, dI <sub>F</sub> /dt=100A/μs
Q <sub>rr</sub>	-	9.8	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

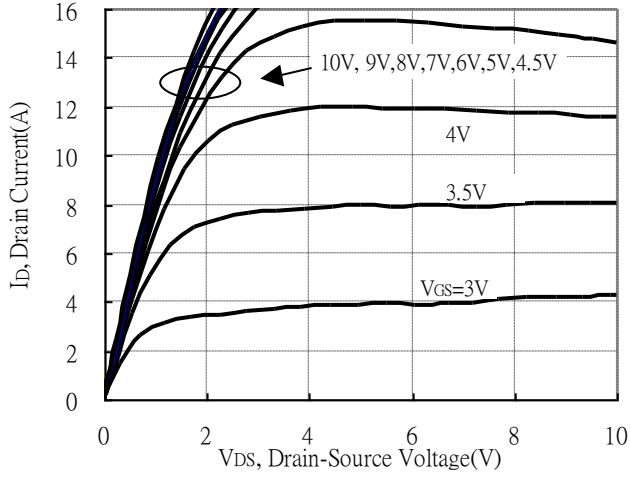
**Recommended soldering footprint**



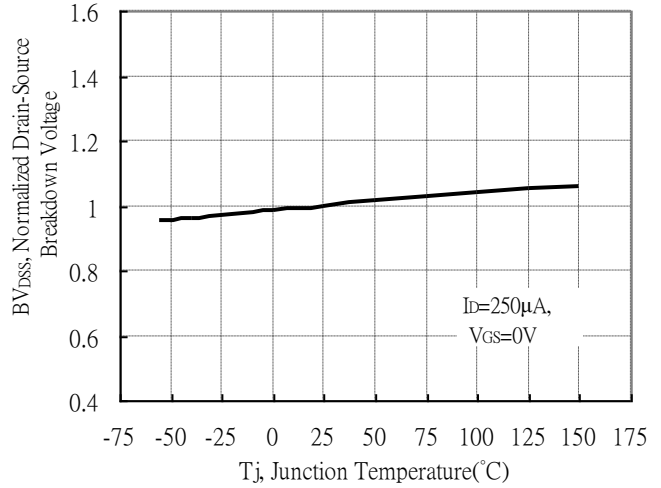
unit : mm

**Typical Characteristics**

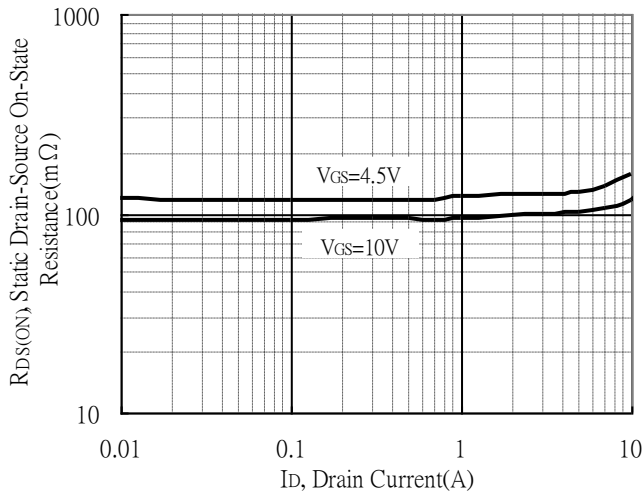
Typical Output Characteristics



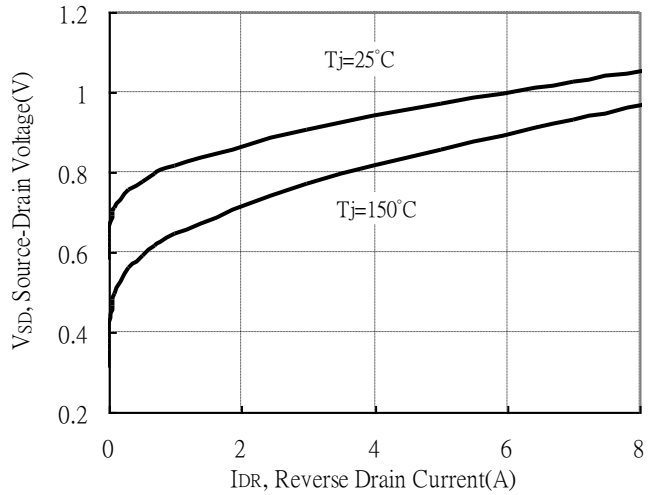
Brekdown Voltage vs Ambient Temperature



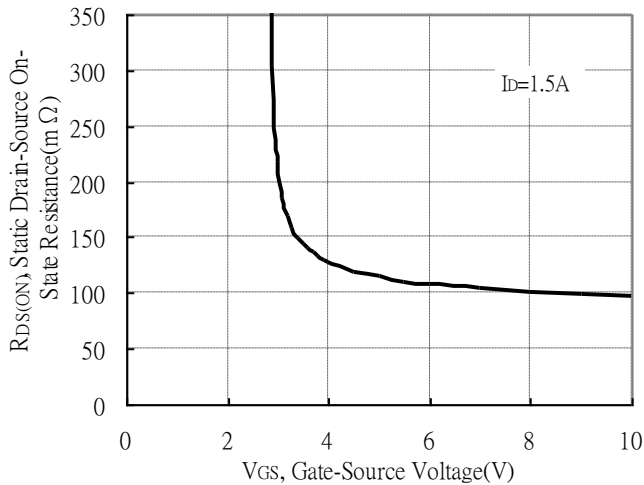
Static Drain-Source On-State resistance vs Drain Current



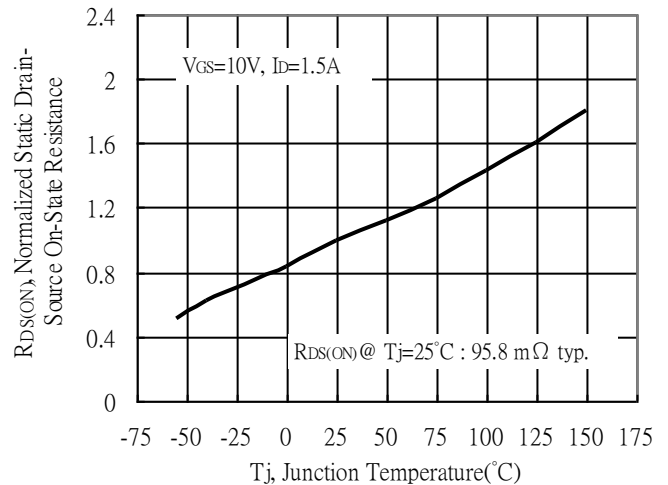
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

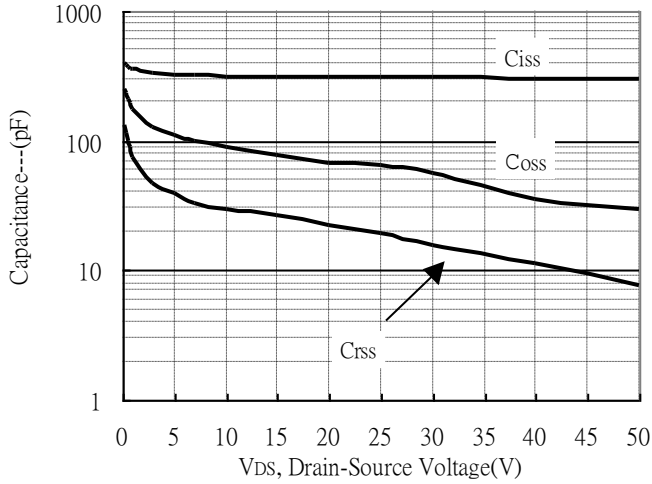


Drain-Source On-State Resistance vs Junction Temperature

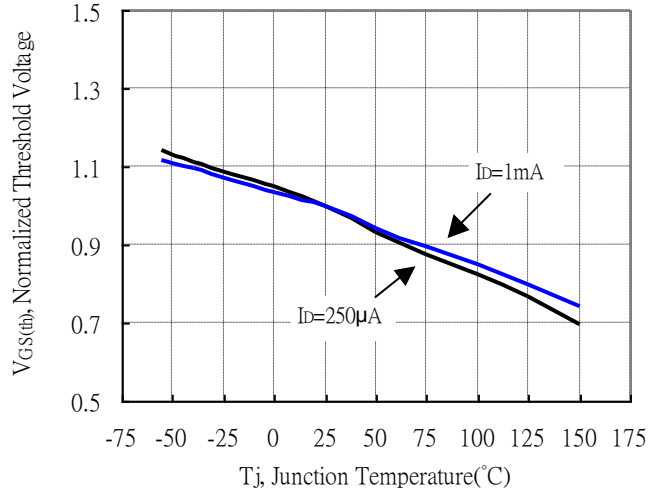


**Typical Characteristics(Cont.)**

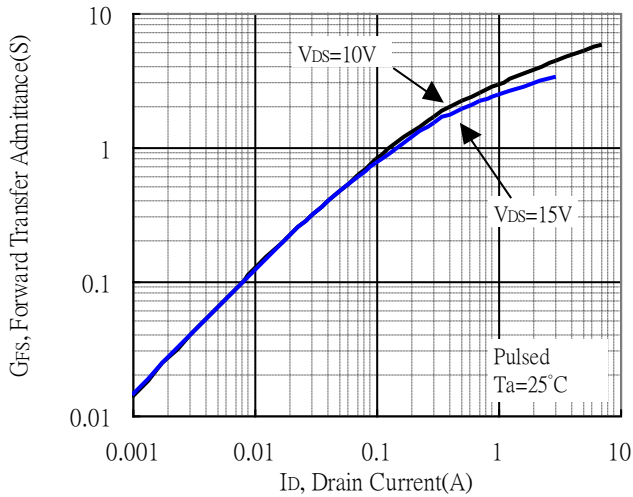
Capacitance vs Drain-to-Source Voltage



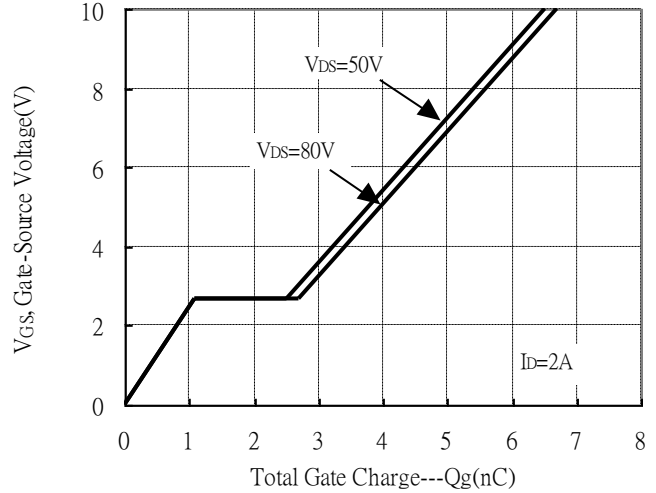
Threshold Voltage vs Junction Temperature



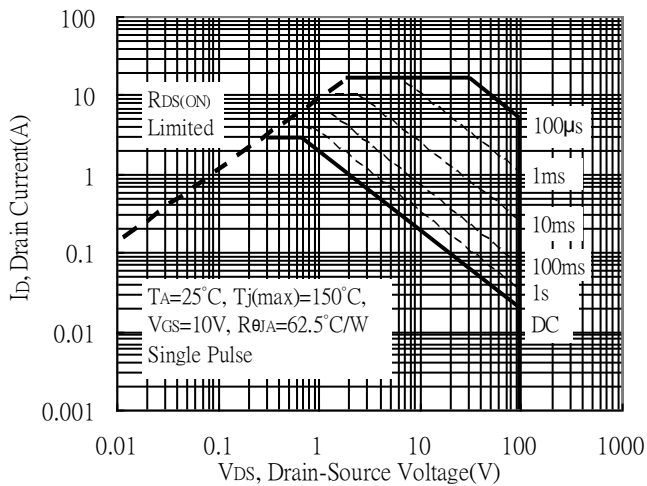
Forward Transfer Admittance vs Drain Current



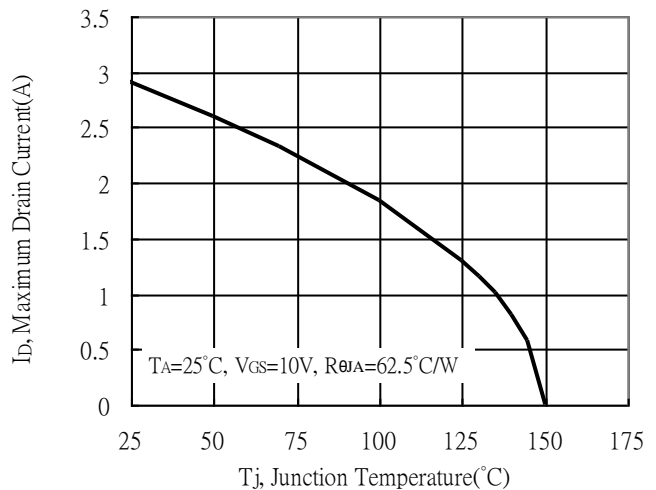
Gate Charge Characteristics



Maximum Safe Operating Area

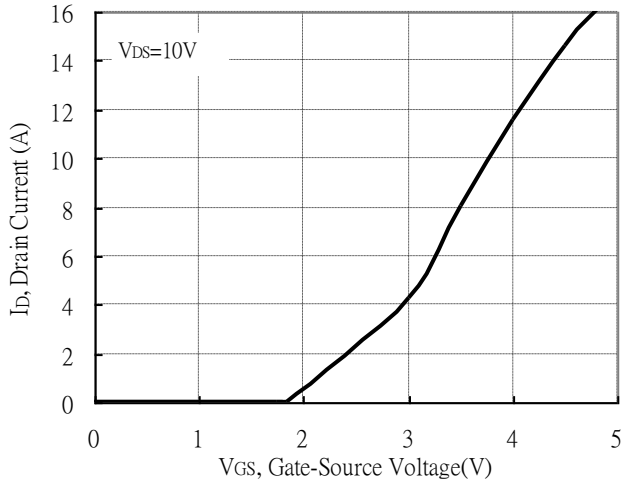


Maximum Drain Current vs Junction Temperature

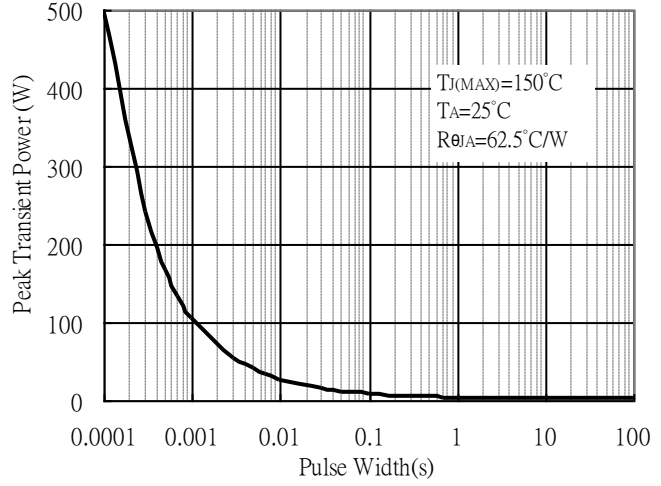


**Typical Characteristics(Cont.)**

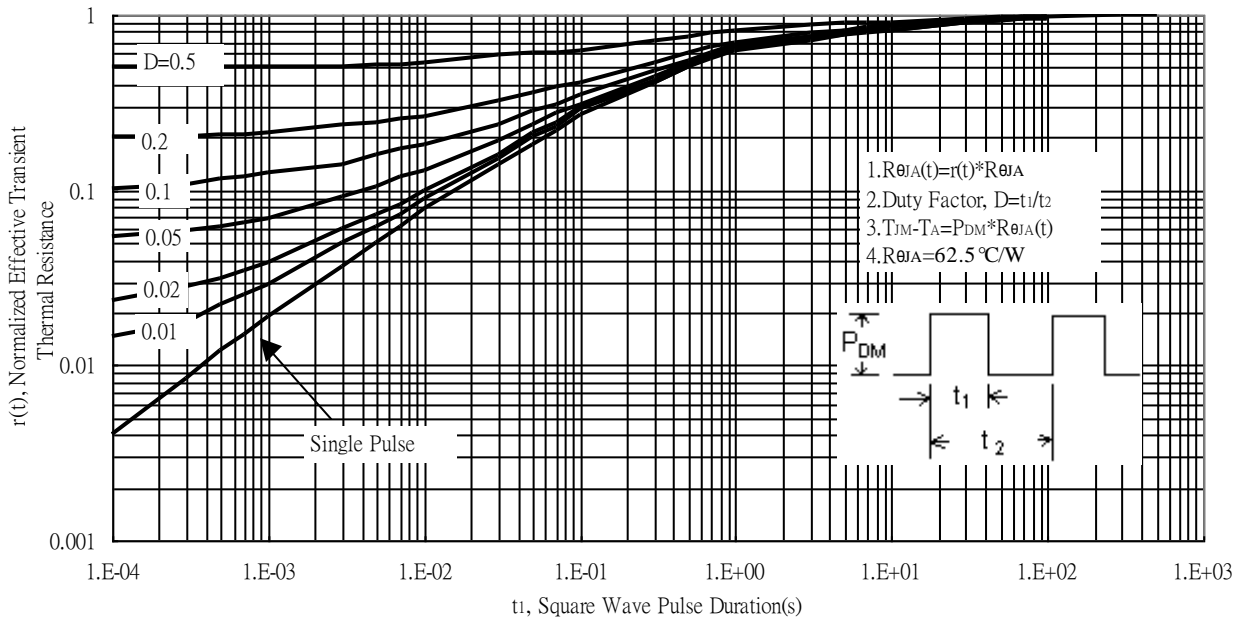
Typical Transfer Characteristics



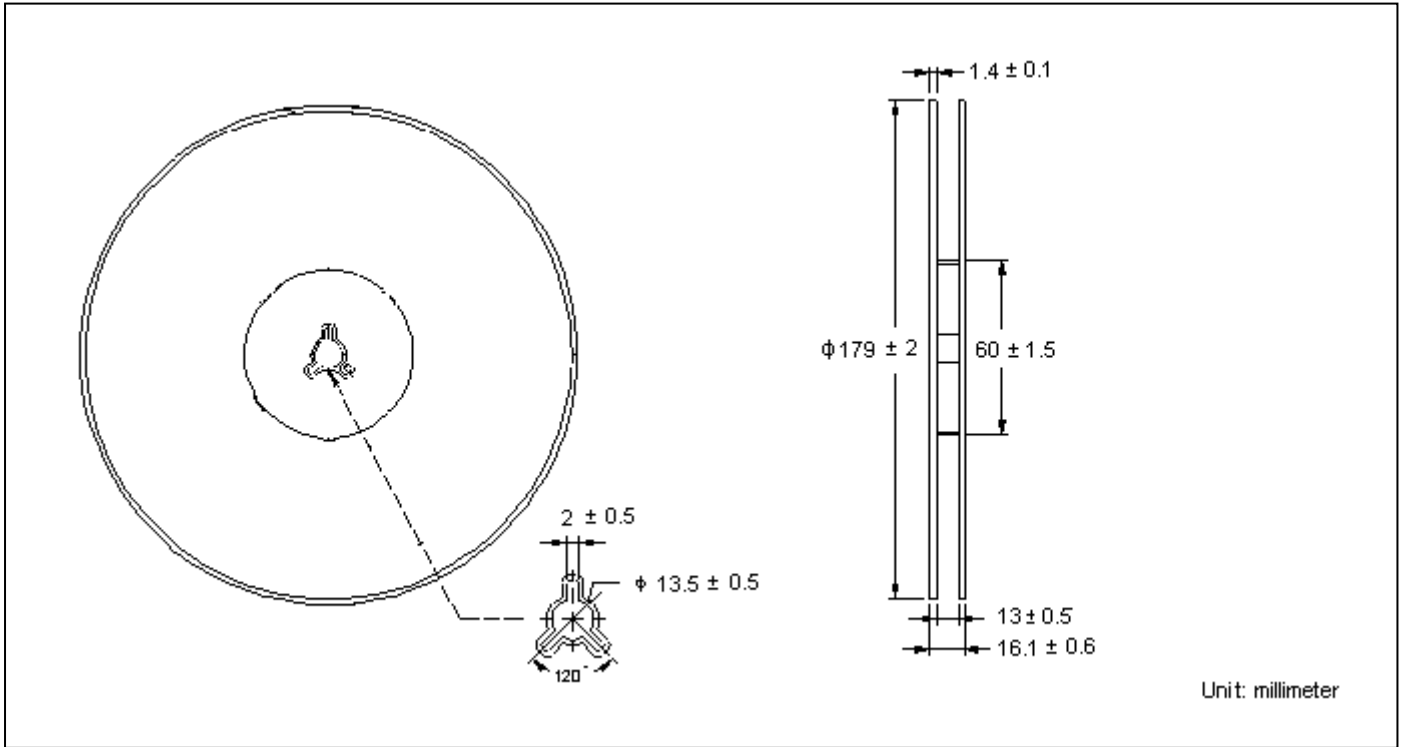
Single Pulse Maximum Power Dissipation



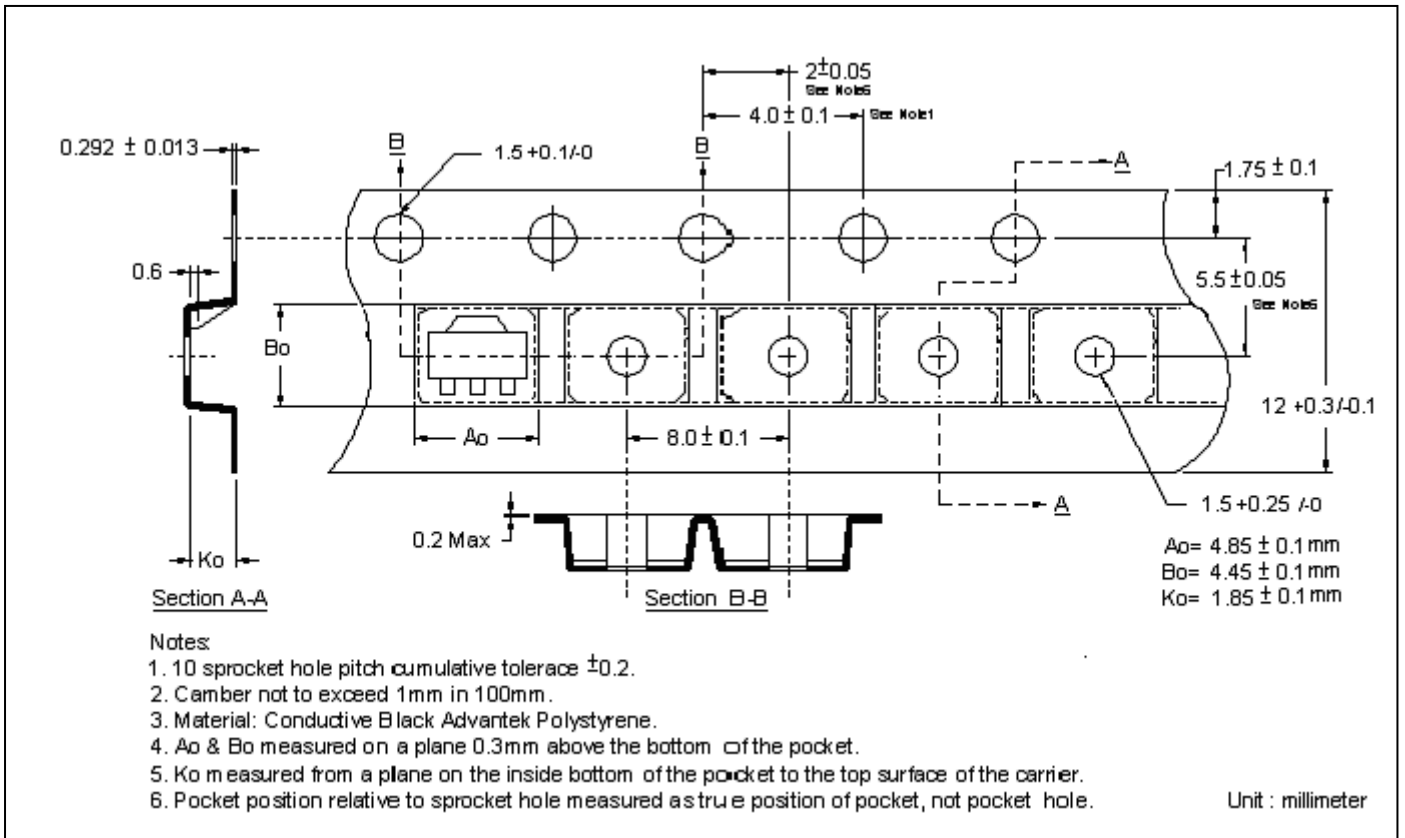
Transient Thermal Response Curves



**Reel Dimension**



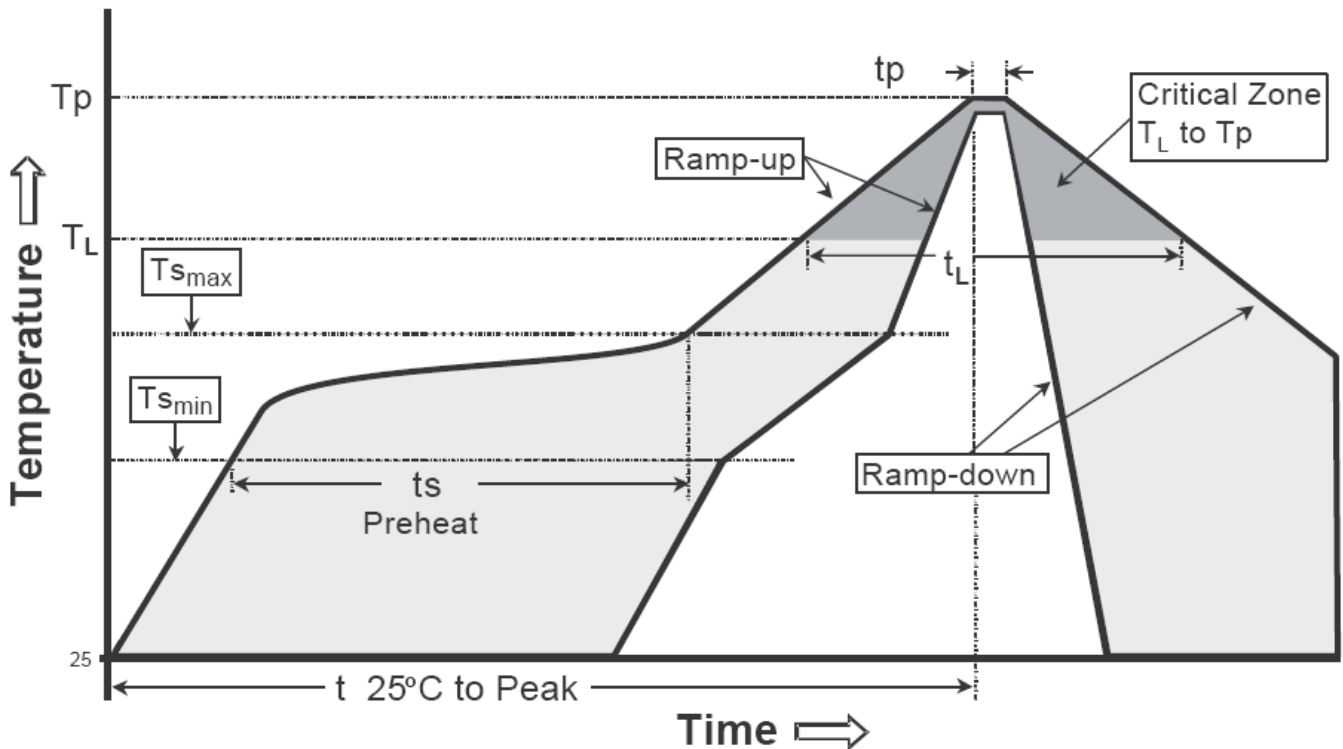
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

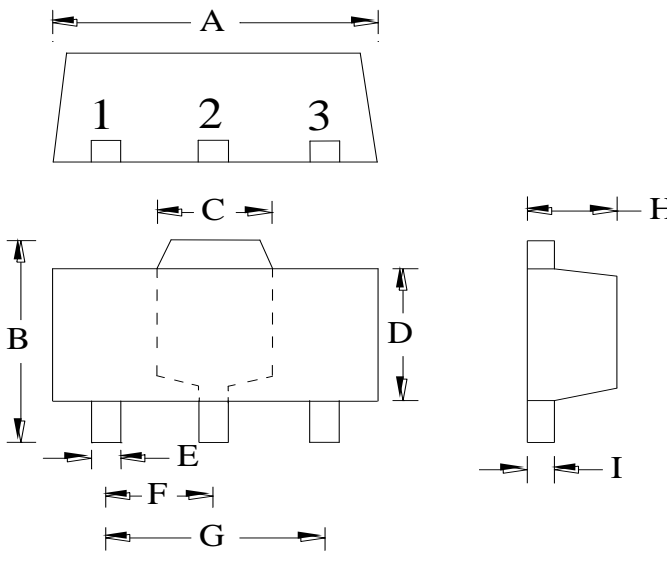


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

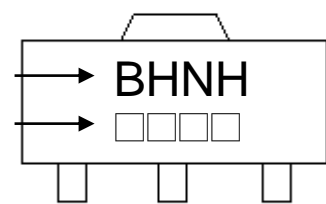


**SOT-89 Dimension**



The diagram shows three views of the SOT-89 package: a top view with dimensions A, C, and lead positions 1, 2, 3; a front view with dimensions B, D, E, F, G; and a side view with dimensions H and I.

**Marking:**



Device Name → **BH NH**  
 Date Code → □ □ □ □

Style: Pin 1. Gate 2. Drain 3. Source

3-Lead SOT-89 Plastic  
 Surface Mounted Package  
 CYStek Package Code: M3

Date Code : (from left to right)

1. 1st code : year code, the last digit of Christian year
2. 2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K, Nov→L, Dec→M
3. 3rd and 4th codes, serial number of production lot, 01~99

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1732	0.1811	4.40	4.60	F	0.0591	TYP	1.50	TYP
B	0.1551	0.1673	3.94	4.25	G	0.1181	TYP	3.00	TYP
C	0.0610 REF		1.55 REF		H	0.0551	0.0630	1.40	1.60
D	0.0906	0.1024	2.30	2.60	I	0.0138	0.0173	0.35	0.44
E	0.0126	0.0205	0.32	0.52					

**Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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