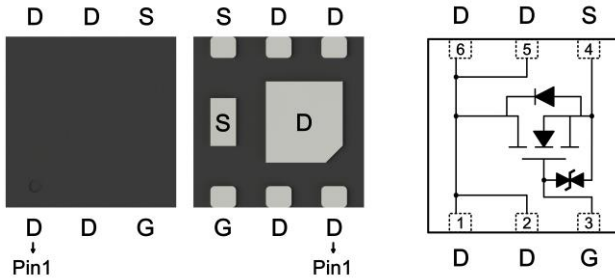


## Product Summary

$BV_{DSS}$	100	V
$R_{DS(ON)}$ typ. @ $V_{GS}=10V, I_D=2A$	62	mΩ
$R_{DS(ON)}$ typ. @ $V_{GS}=4.5V, I_D=2A$	77	
$I_D$ @ $V_{GS}=10V, T_C=25^\circ C$	8.5	A
$I_D$ @ $V_{GS}=10V, T_A=25^\circ C$	3.2	

## DFNWB2×2-6L-J



## Features

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free
- ESD protected gate

## Marking



YMXX: Date Code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

A: Jan	B: Feb	C: Mar	D: Apr	E: May	F: Jun
G: Jul	H: Aug	J: Sep	K: Oct	L: Nov	M: Dec

XX: Production Serial Number, 01~99

## Ordering Information

Device	Package	Shipping
MTB060N10KRDFJ6-0-T1-G	DFNWB2×2-6L-J	3000pcs / Tape & Reel

0: Product rank, zero for no rank products.

T1: Packing spec, T1 : 3000pcs / tape & reel, 7" reel

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

## Absolute Maximum Ratings ( $T_A=25^\circ C$ )

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DS}$	100	V	
Gate-Source Voltage	$V_{GS}$	±20		
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$	$I_D$	8.5	A	
Continuous Drain Current @ $V_{GS}=10V, T_C=100^\circ C$		5.4		
Continuous Drain Current @ $V_{GS}=10V, T_A=25^\circ C$		3.2		
Continuous Drain Current @ $V_{GS}=10V, T_A=70^\circ C$		2.5		
Pulsed Drain Current	$I_{DM}$	28		
Continuous Body Diode Forward Current @ $T_C=25^\circ C$	$I_S$	8.5		
Pulsed Body Diode Forward Current @ $T_C=25^\circ C$	$I_{SM}$	28		
Total Power Dissipation	$P_D$	$T_C=25^\circ C$	11.4	W
		$T_C=100^\circ C$	4.6	
		$T_A=25^\circ C$	1.6	
		$T_A=70^\circ C$	1	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	°C	
Steady State Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	11	°C/W	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	79		



Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1	-	2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	4	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =2A
I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
R <sub>DS(ON)</sub>	-	62	81	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =2A
	-	77	108		V <sub>GS</sub> =4.5V, I <sub>D</sub> =2A
<b>Dynamic</b>					
C <sub>iss</sub>	-	317	-	pF	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	40	-		
C <sub>rss</sub>	-	19	-		
R <sub>g</sub>	-	6.6	-	Ω	f=1MHz
Q <sub>g</sub> *d,e	-	3.7	-	nC	V <sub>DS</sub> =50V, I <sub>D</sub> =2A, V <sub>GS</sub> =4.5V
Q <sub>g</sub> *d,e	-	7.1	-		
Q <sub>gs</sub> *d,e	-	1.1	-		
Q <sub>gd</sub> *d,e	-	1.6	-		
t <sub>d(ON)</sub> *d,e	-	5.2	-	ns	V <sub>DS</sub> =50V, I <sub>D</sub> =2A, V <sub>GS</sub> =10V
t <sub>r</sub> *d,e	-	17	-		
t <sub>d(OFF)</sub> *d,e	-	20	-		
t <sub>f</sub> *d,e	-	7.3	-		
<b>Source-Drain Diode</b>					
V <sub>SD</sub> *d	-	0.82	1.2	V	I <sub>S</sub> =2A, V <sub>GS</sub> =0V
t <sub>rr</sub>	-	20	-	ns	I <sub>F</sub> =2A, di/dt=100A/μs
Q <sub>rr</sub>	-	12	-	μC	

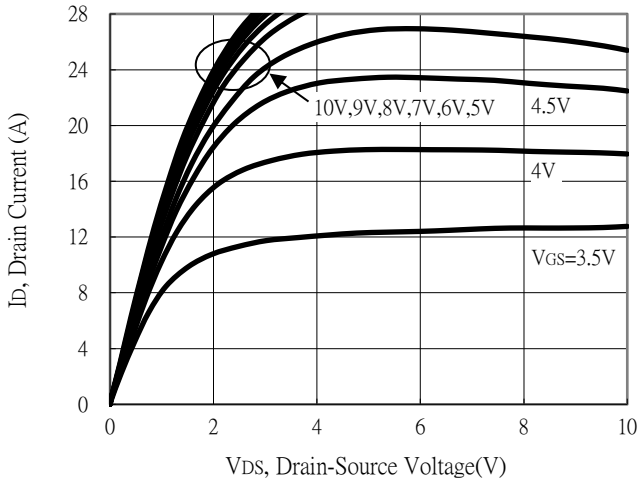
Note:

- \*a. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- \*b. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.
- \*d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- \*e. Independent of operating temperature.

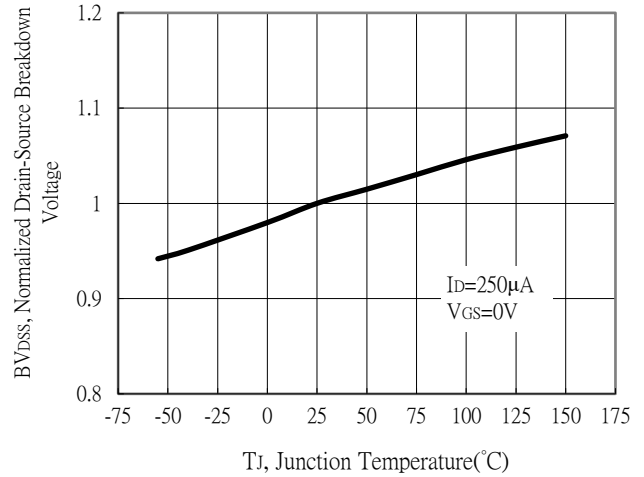


## Typical Characteristics

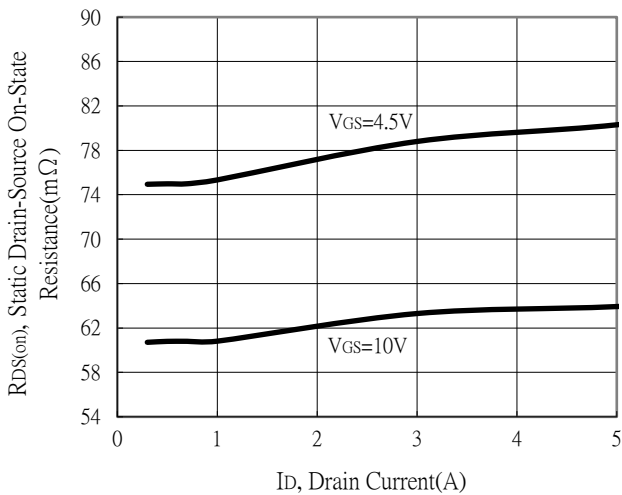
Typical Output Characteristics



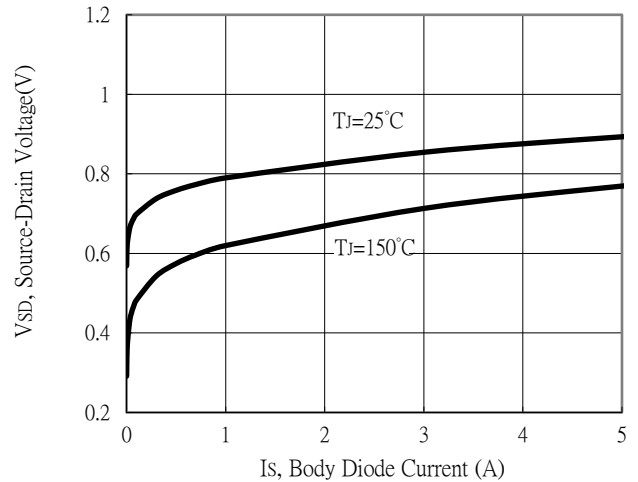
Breakdown Voltage vs Ambient Temperature



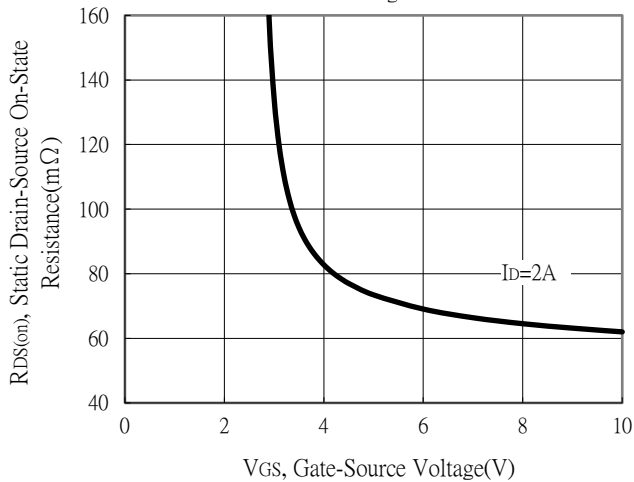
Static Drain-Source On-State resistance vs Drain Current



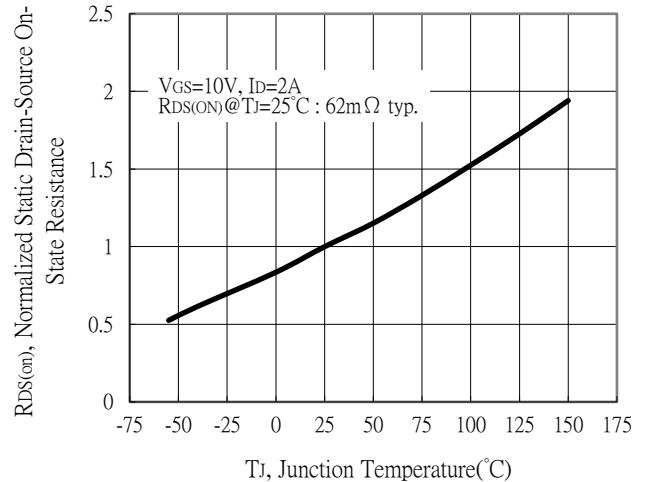
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



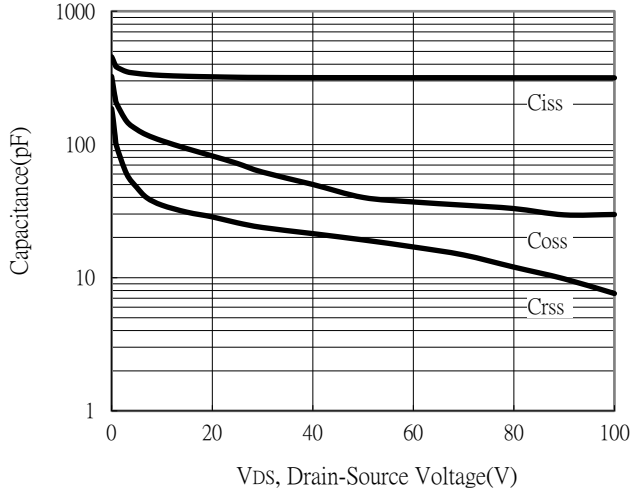
Drain-Source On-State Resistance vs Junction Temperature



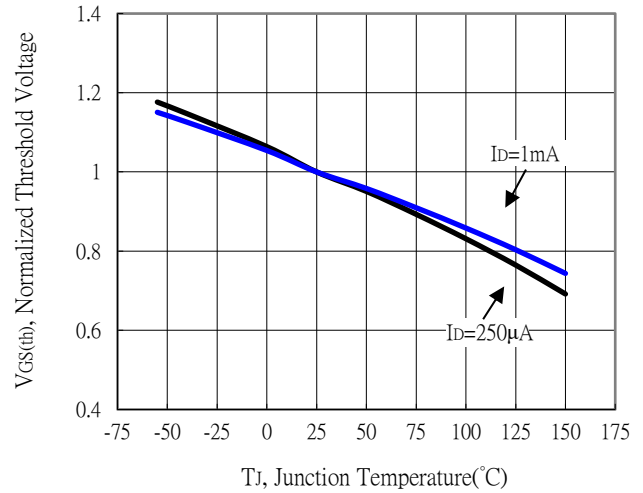


## Typical Characteristics

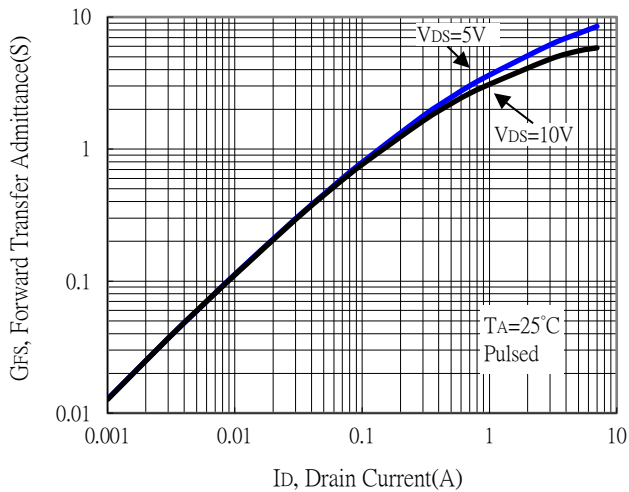
Capacitance vs Drain-to-Source Voltage



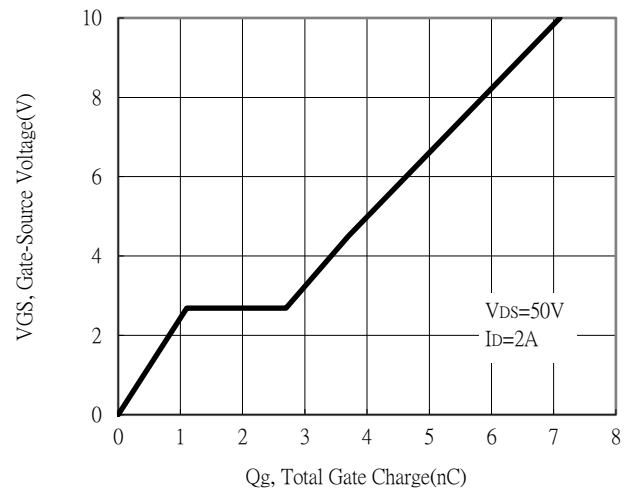
Threshold Voltage vs Junction Temperature



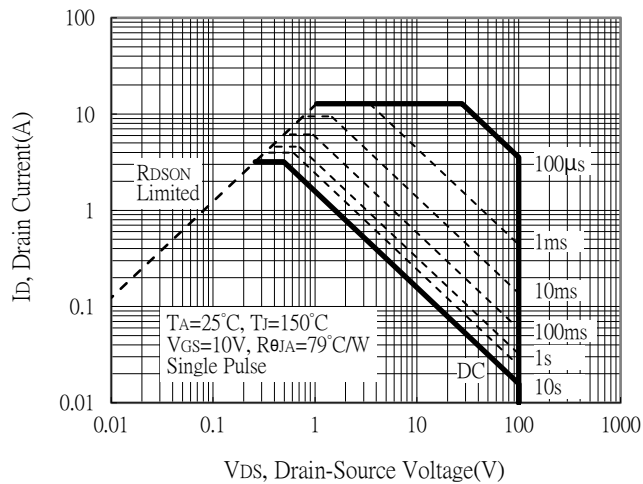
Forward Transfer Admittance vs Drain Current



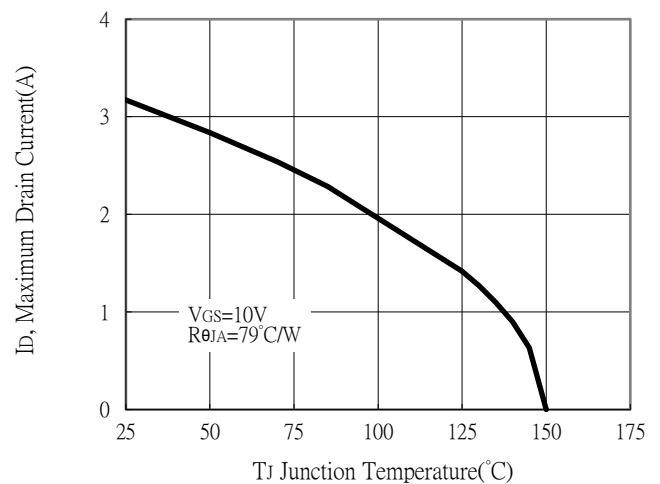
Gate Charge Characteristics



Maximum Safe Operating Area



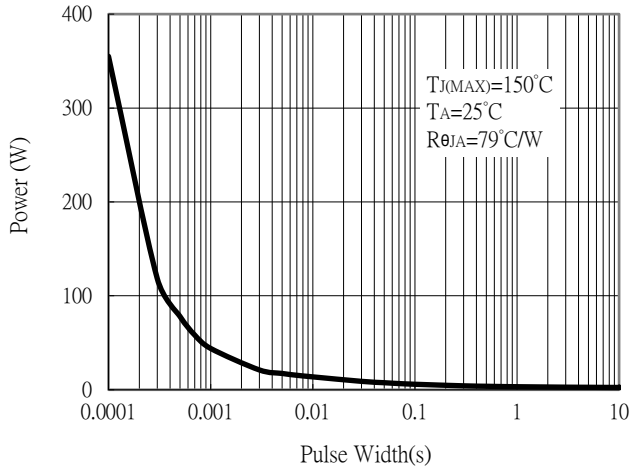
Maximum Drain Current vs Junction Temperature



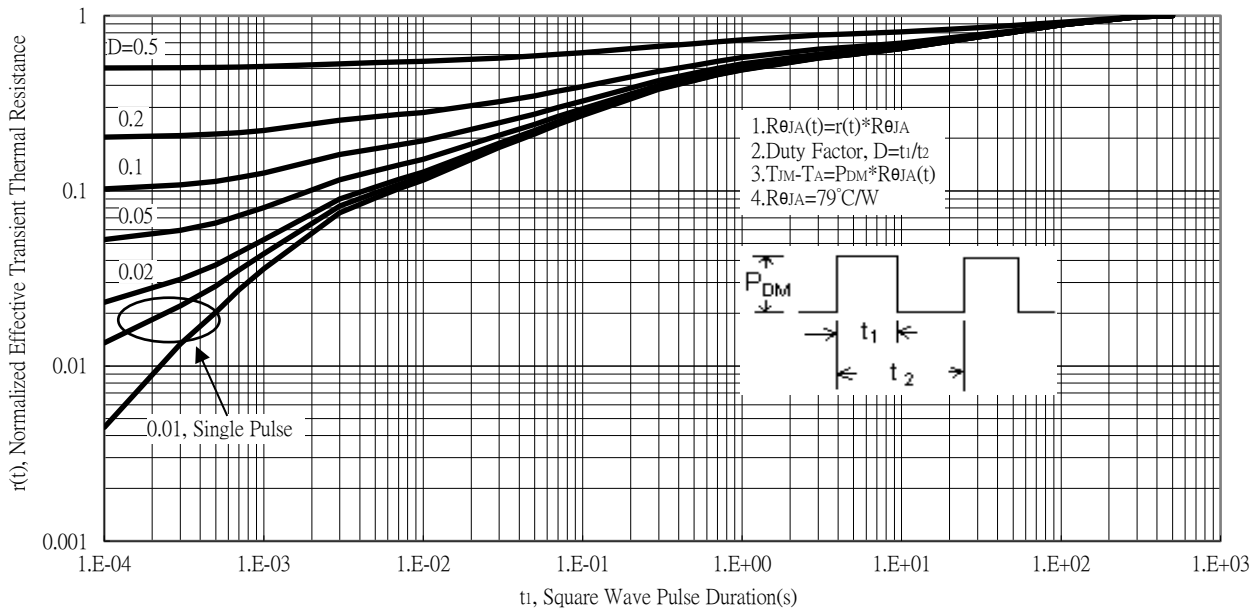


### Typical Characteristics

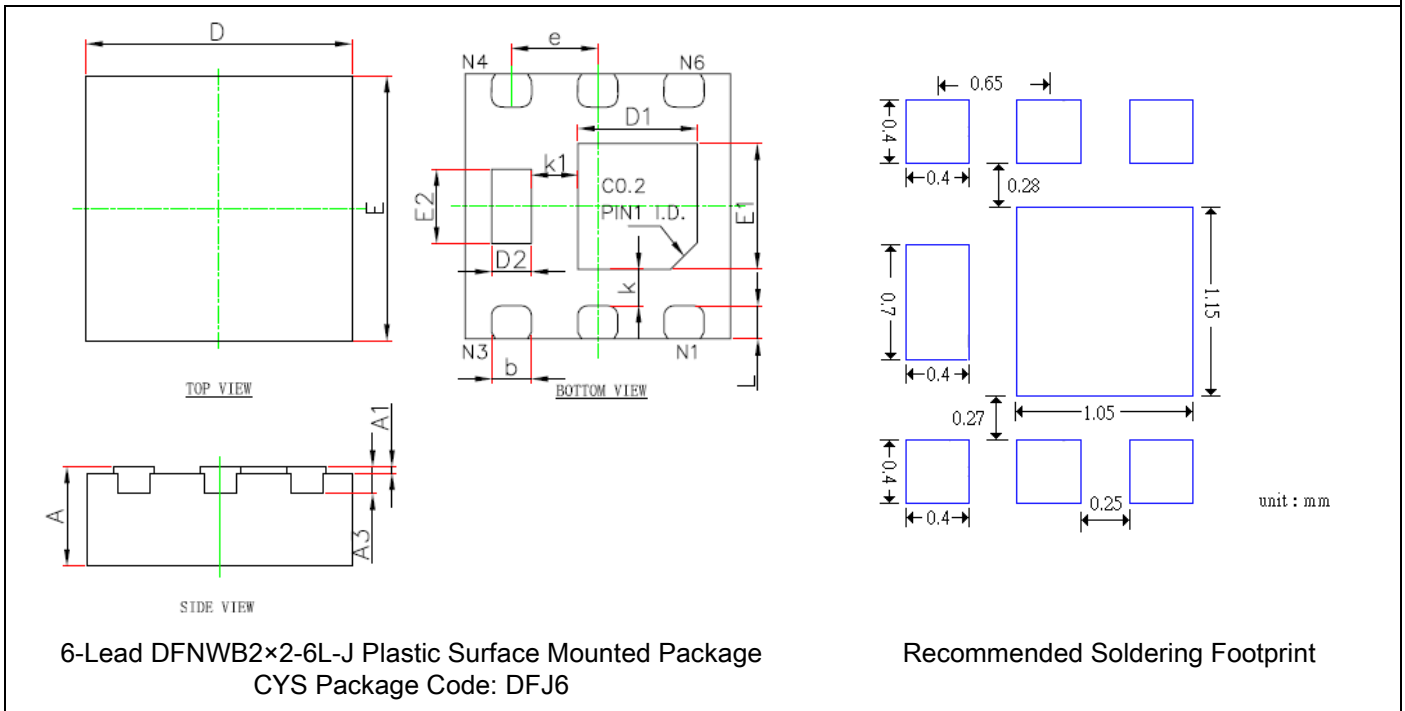
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



## DFNWB2×2-6L-J Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031	D2	0.200	0.400	0.008	0.016
A1	0.000	0.050	0.000	0.002	E2	0.460	0.660	0.018	0.026
A3	0.203	REF	0.008	REF	b	0.250	0.350	0.010	0.014
D	1.900	2.100	0.075	0.083	e	0.650	BSC	0.026	BSC
E	1.900	2.100	0.075	0.083	k	0.275	REF	0.011	REF
D1	0.800	1.000	0.031	0.039	k1	0.350	REF	0.014	REF
E1	0.850	1.050	0.033	0.041	L	0.174	0.326	0.007	0.013

**Note:**

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

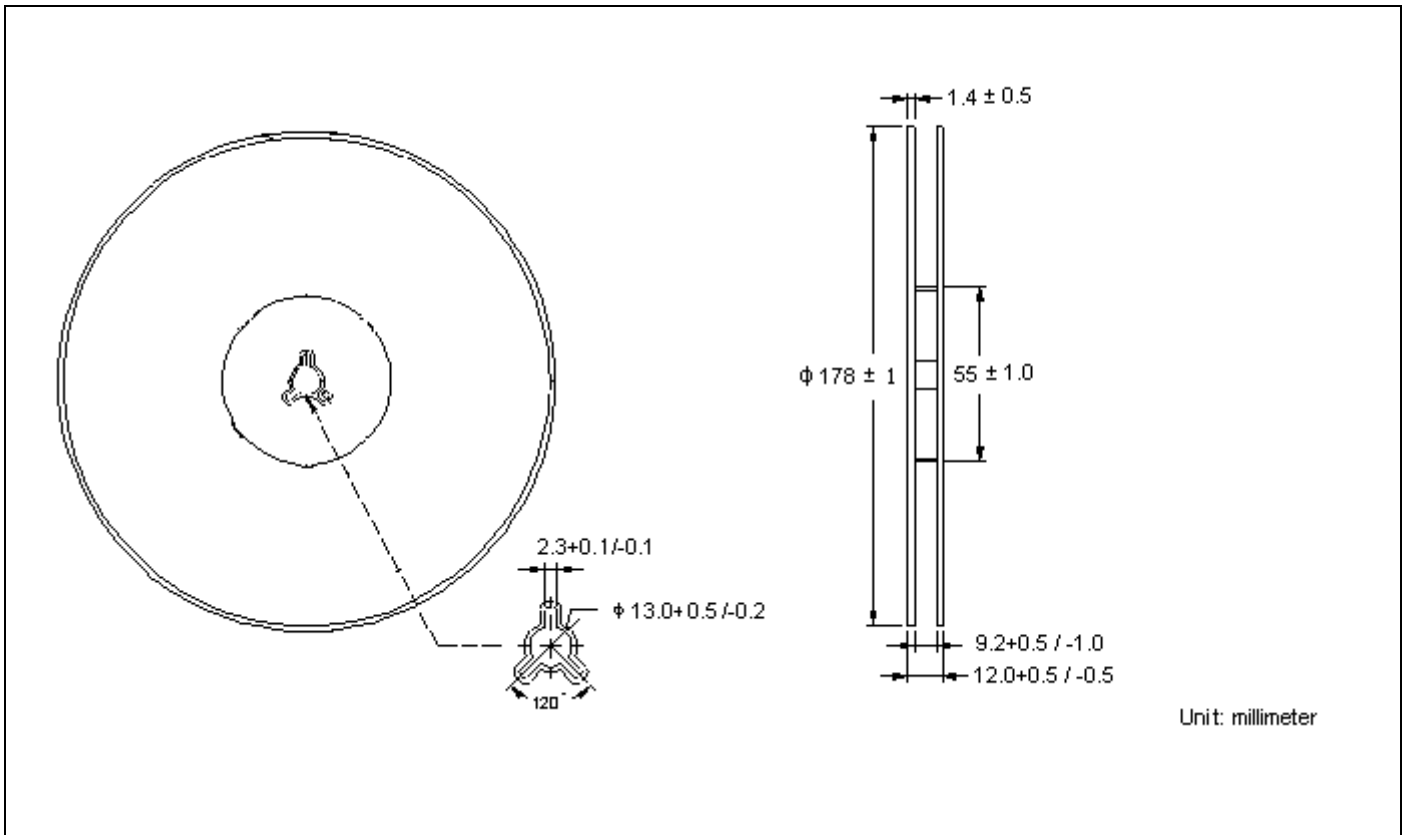
**Material:**

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

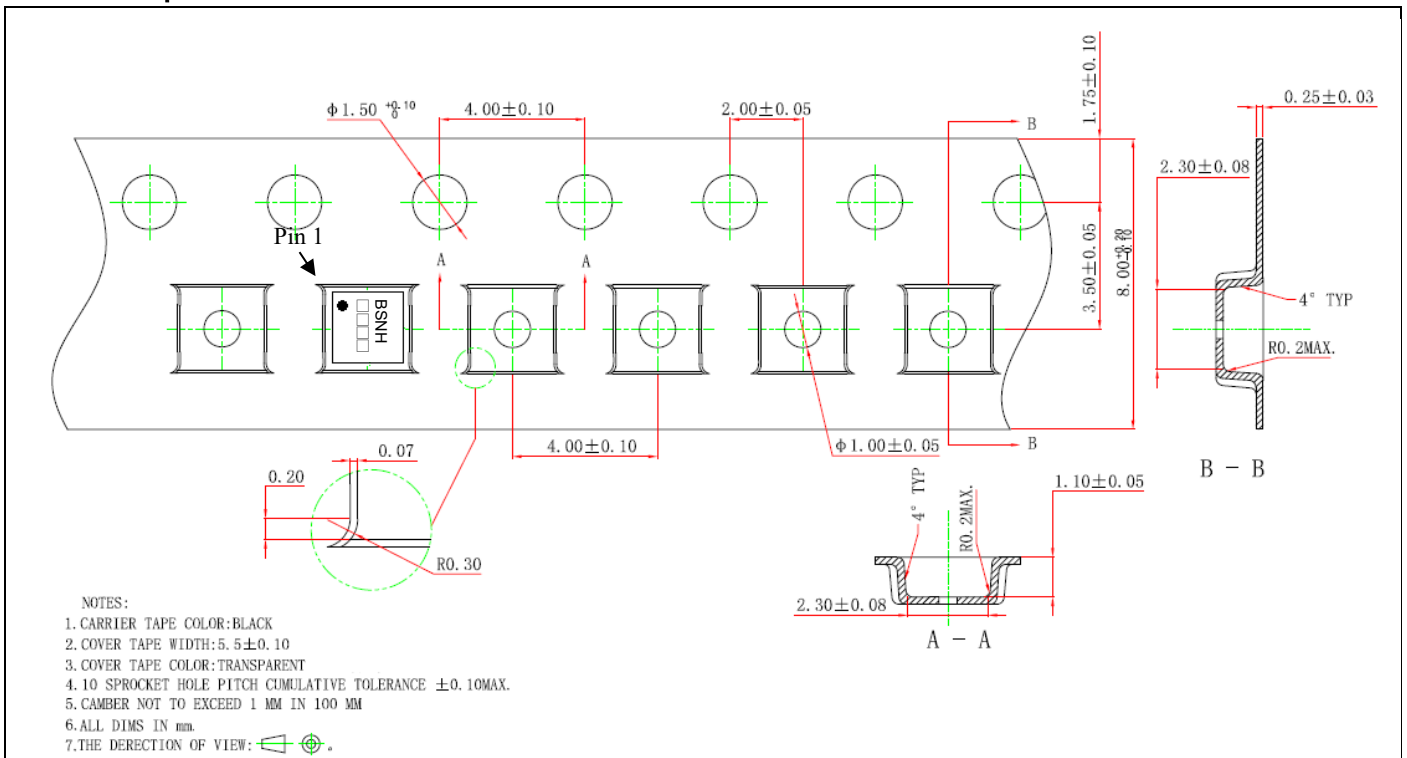
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## Reel Dimension



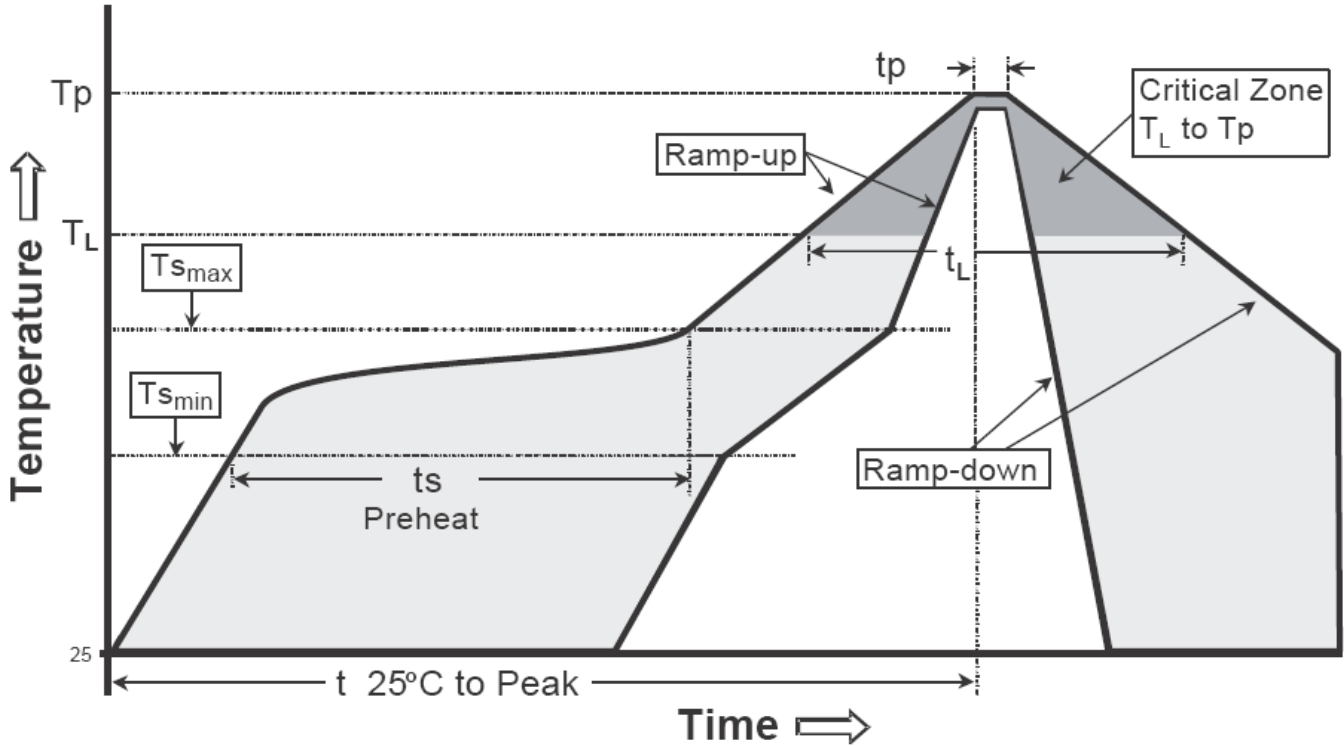
## Carrier Tape Dimension



### Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

### Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>S</sub> max to T <sub>P</sub> )	3°C/second max.	3°C/second max.
Preheat -Temperature Min (T <sub>S</sub> min) -Temperature Max (T <sub>S</sub> max) -Time (t <sub>S</sub> min to t <sub>S</sub> max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T <sub>L</sub> ) -Time (t <sub>L</sub> )	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (t <sub>P</sub> )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

**Note:**

- All temperatures refer to topside of the package, measured on the package body surface.