

**Dual P-Channel Enhancement Mode Power MOSFET**

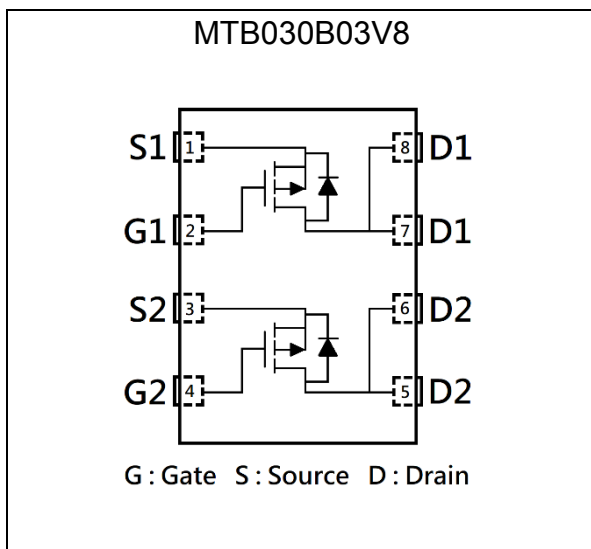
# MTB030B03V8

**Features**

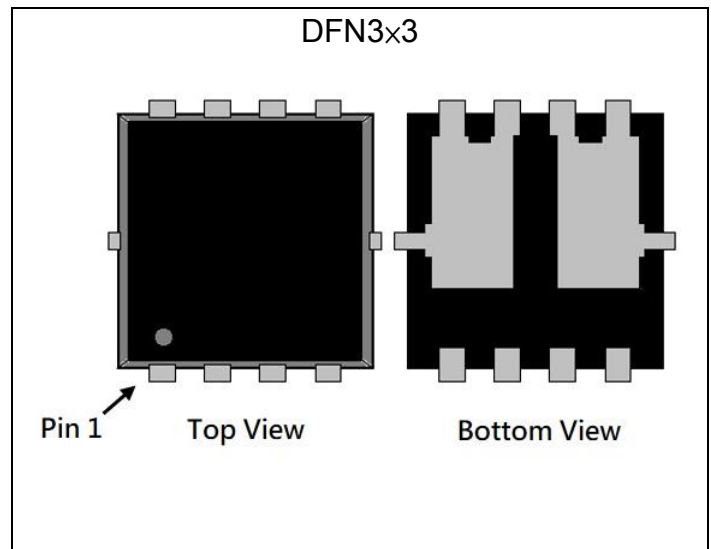
- Low Gate Charge
- Fast Switching Characteristic

|   |       |
|---|-------|
| $BV_{DSS}$                              | -30V  |
| $I_D@V_{GS}=-10V, T_C=25^\circ C$       | -10A  |
| $I_D@V_{GS}=-10V, T_A=25^\circ C$       | -5.3A |
| $R_{DS(ON) typ.}@V_{GS}=-10V, I_D=-5A$  | 26mΩ  |
| $R_{DS(ON) typ.}@V_{GS}=-4.5V, I_D=-4A$ | 37mΩ  |

**Equivalent Circuit**

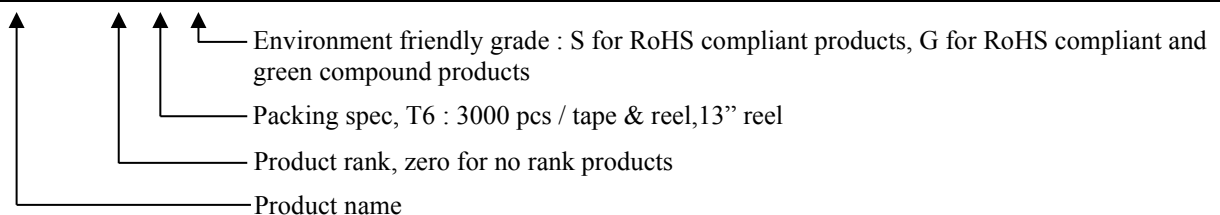


**Outline**



**Ordering Information**

| Device             | Package   | Shipping               |
|--------------------|---|------------------------|
| MTB030B03V8-0-T6-G | DFN3×3<br>(Pb-free lead plating and halogen-free package) | 3000 pcs / Tape & Reel |



**Absolute Maximum Ratings (T<sub>A</sub>=25°C)**

| Parameter   | Symbol                            | Limits          | Unit |
|---|-----------------------------------|-----------------|------|
| Drain-Source Voltage  | V <sub>DS</sub>                   | -30             | V    |
| Gate-Source Voltage   | V <sub>GS</sub>                   | ±20             |      |
| Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>C</sub> =25°C (silicon limit) *a | I <sub>D</sub>                    | -15             | A    |
| Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>C</sub> =25°C (package limit) *a |                                   | -10             |      |
| Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>C</sub> =100°C *a                |                                   | -9.5            |      |
| Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =25°C *b                 |                                   | -5.3            |      |
| Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =70°C *b                 |                                   | -4.3            |      |
| Pulsed Drain Current *c   |                                   | I <sub>DM</sub> |      |
| Continuous Body Diode Forward Current @ T <sub>C</sub> =25°C *a                           | I <sub>S</sub>                    | -10             | mJ   |
| Pulsed Body Diode Forward Current @ T <sub>C</sub> =25°C *a                               | I <sub>SM</sub>                   | -40             |      |
| Avalanche Current @ L=0.1mH   | I <sub>AS</sub>                   | -15             |      |
| Avalanche Energy @ L=0.5mH  | E <sub>AS</sub>                   | 16              | W    |
| Total Power Dissipation   | T <sub>C</sub> =25°C *a           | 13              |      |
|   | T <sub>C</sub> =100°C *a          | 5.2             |      |
|   | T <sub>A</sub> =25°C *b           | 1.6             |      |
|   | T <sub>A</sub> =70°C *b           | 1               |      |
| Operating Junction and Storage Temperature Range  | T <sub>J</sub> , T <sub>stg</sub> | -55~+150        | °C   |

**Thermal Data**

| Parameter                                  | Symbol           | Steady State | Unit |
|--|------------------|--------------|------|
| Thermal Resistance, Junction-to-case       | R <sub>θJC</sub> | 10           | °C/W |
| Thermal Resistance, Junction-to-ambient *b | R <sub>θJA</sub> | 76           |      |

Note:

- \*a. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
- \*b. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The power dissipation P<sub>D</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- \*c. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.



**Electrical Characteristics (T<sub>A</sub>=25°C, unless otherwise specified)**

| Symbol                    | Min. | Typ.  | Max. | Unit | Test Conditions   |
|---------------------------|------|-------|------|------|---|
| <b>Static</b>             |      |       |      |      |   |
| BV <sub>DSS</sub>         | -30  | -     | -    | V    | V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA   |
| V <sub>GS(th)</sub>       | -1   | -     | -2.5 |      | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA                               |
| G <sub>FS</sub>           | -    | 5.7   | -    | S    | V <sub>DS</sub> =-10V, I <sub>D</sub> =-5A  |
| I <sub>GSS</sub>          | -    | -     | ±100 | nA   | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V  |
| I <sub>DSS</sub>          | -    | -     | -1   | μA   | V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V  |
| R <sub>DS(ON)</sub>       | -    | 26    | 34   | mΩ   | V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A  |
|                           | -    | 37    | 52   |      | V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A   |
| <b>Dynamic</b>            |      |       |      |      |   |
| C <sub>iss</sub>          | -    | 1000  | -    | pF   | V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz                                      |
| C <sub>oss</sub>          | -    | 110   | -    |      |   |
| C <sub>rss</sub>          | -    | 95    | -    |      |   |
| R <sub>g</sub>            | -    | 22    | -    | Ω    | f=1MHz  |
| Q <sub>g</sub> *1, 2      | -    | 20    | -    | nC   | V <sub>DS</sub> =-15V, I <sub>D</sub> =-5A, V <sub>GS</sub> =-10V                       |
| Q <sub>gs</sub> *1, 2     | -    | 3.8   | -    |      |   |
| Q <sub>gd</sub> *1, 2     | -    | 4     | -    |      |   |
| t <sub>d(ON)</sub> *1, 2  | -    | 8.2   | -    | ns   | V <sub>DS</sub> =-15V, I <sub>D</sub> =-5A, V <sub>GS</sub> =-10V, R <sub>GS</sub> =10Ω |
| t <sub>r</sub> *1, 2      | -    | 18    | -    |      |   |
| t <sub>d(OFF)</sub> *1, 2 | -    | 65    | -    |      |   |
| t <sub>f</sub> *1, 2      | -    | 28    | -    |      |   |
| <b>Source-Drain Diode</b> |      |       |      |      |   |
| V <sub>SD</sub> *1        | -    | -0.85 | -1.2 | V    | I <sub>S</sub> =-5A, V <sub>GS</sub> =0V  |
| t <sub>rr</sub>           | -    | 11    | -    | ns   | I <sub>F</sub> =-5A, dI <sub>F</sub> /dt=100A/μs  |
| Q <sub>rr</sub>           | -    | 6     | -    | nC   |   |

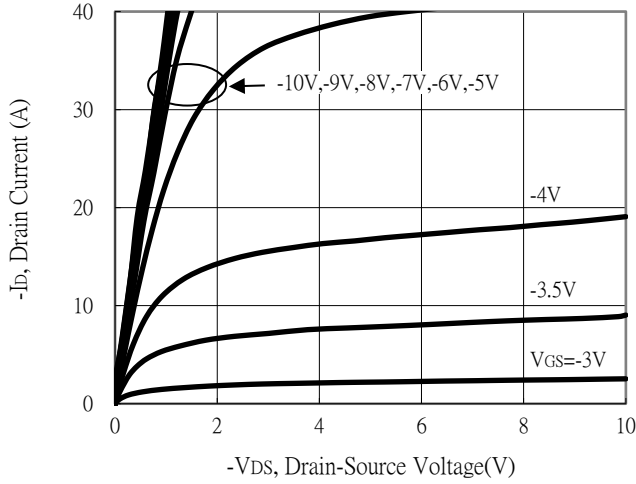
Note:

\*1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

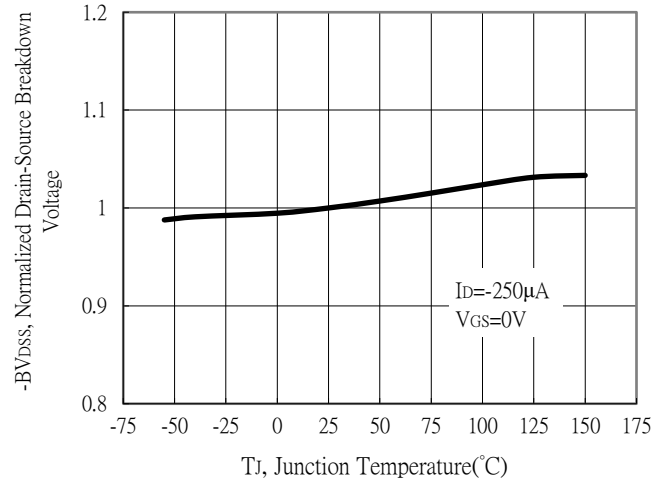
\*2. Independent of operating temperature

## Typical Characteristics

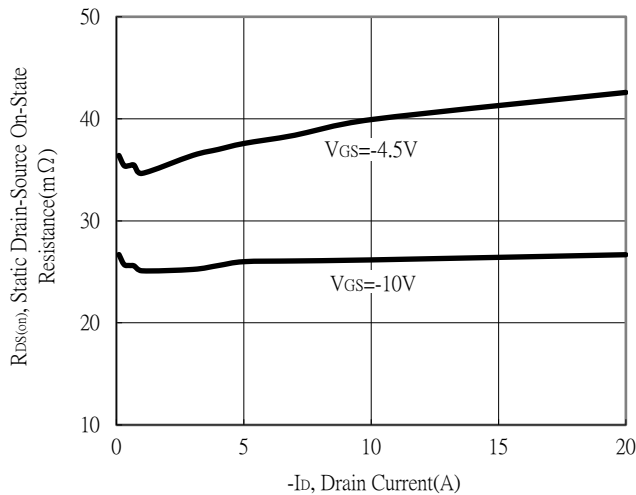
Typical Output Characteristics



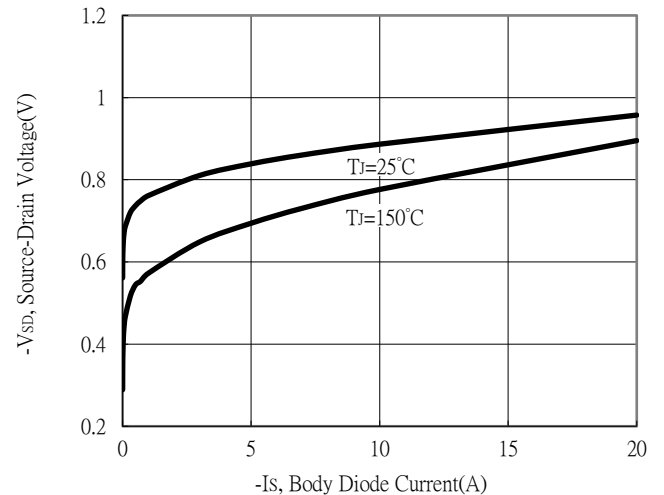
Breakdown Voltage vs Ambient Temperature



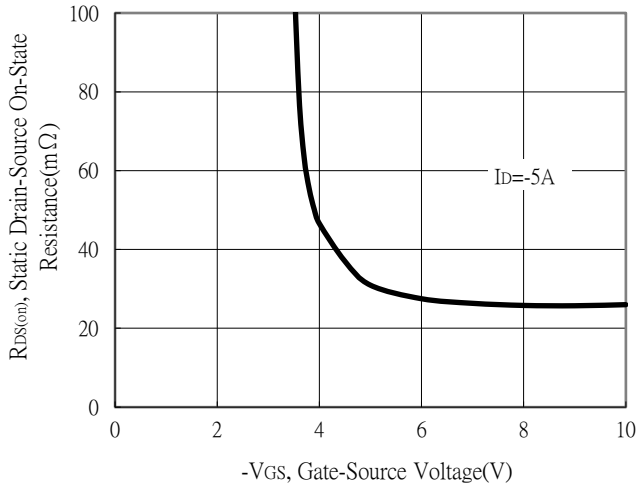
Static Drain-Source On-State resistance vs Drain Current



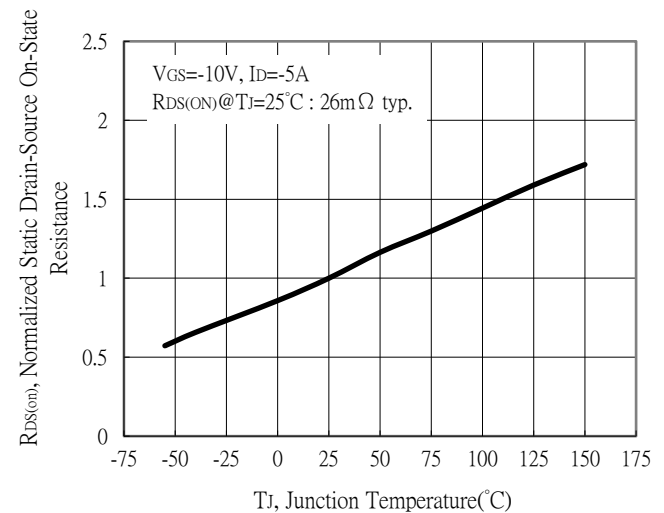
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

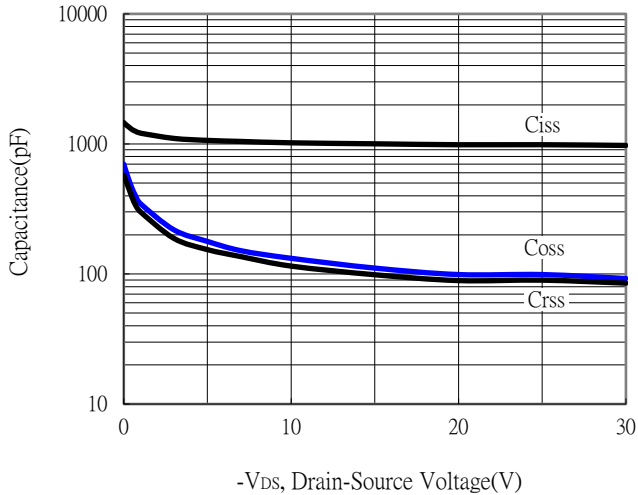


Drain-Source On-State Resistance vs Junction Temperature

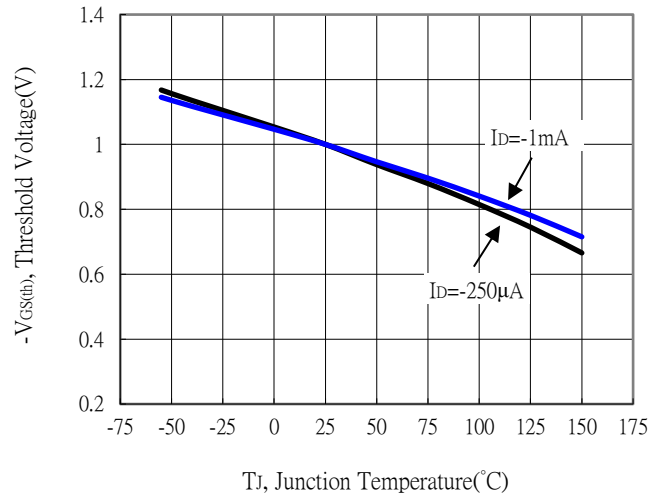


## Typical Characteristics (Cont.)

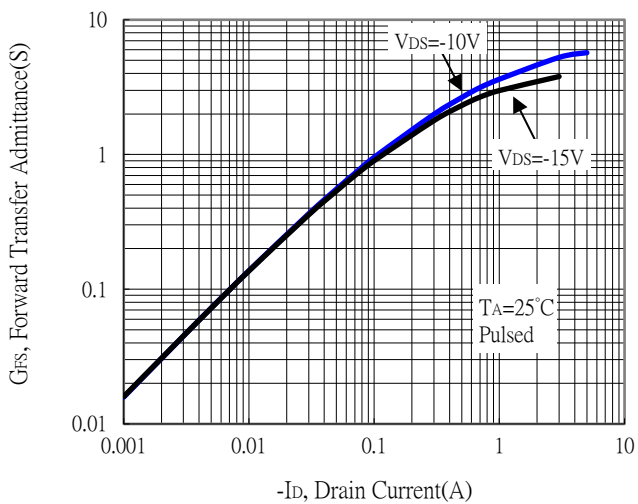
Capacitance vs Drain-to-Source Voltage



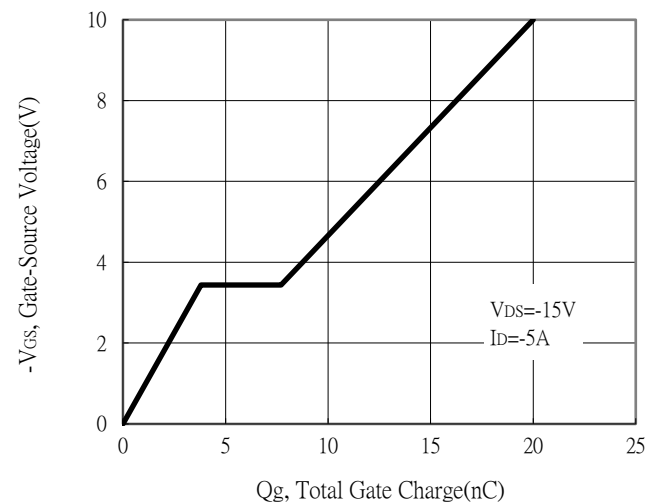
Threshold Voltage vs Junction Temperature



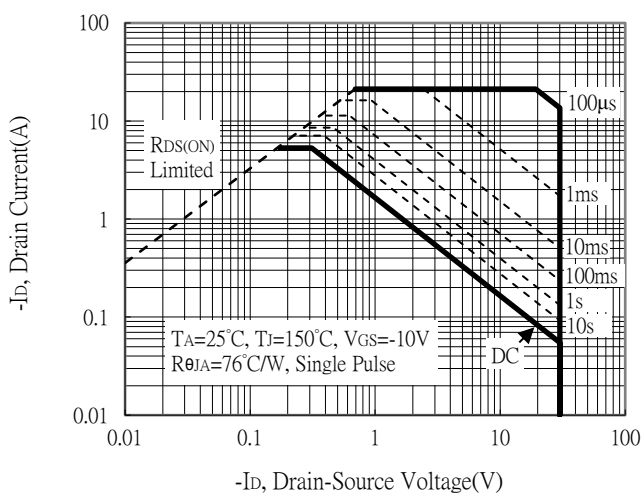
Forward Transfer Admittance vs Drain Current



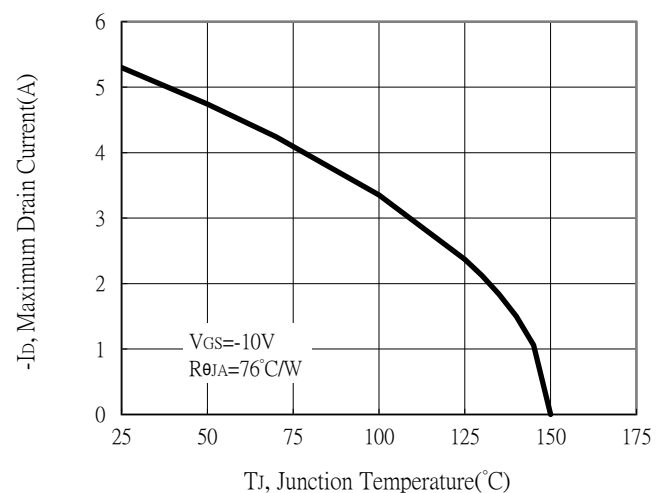
Gate Charge Characteristics



Maximum Safe Operating Area

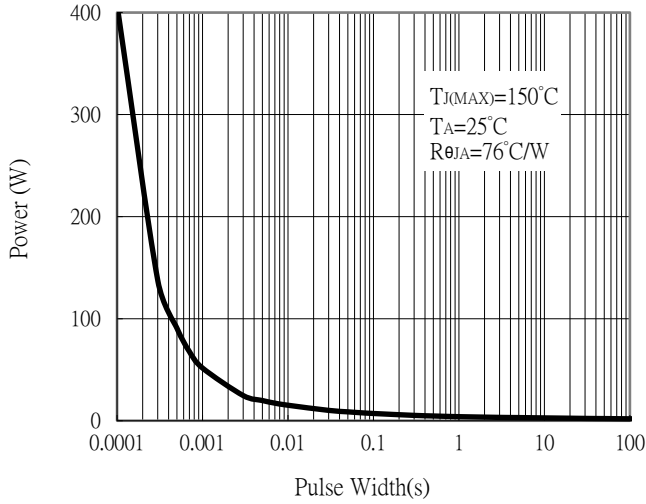


Maximum Drain Current vs Junction Temperature

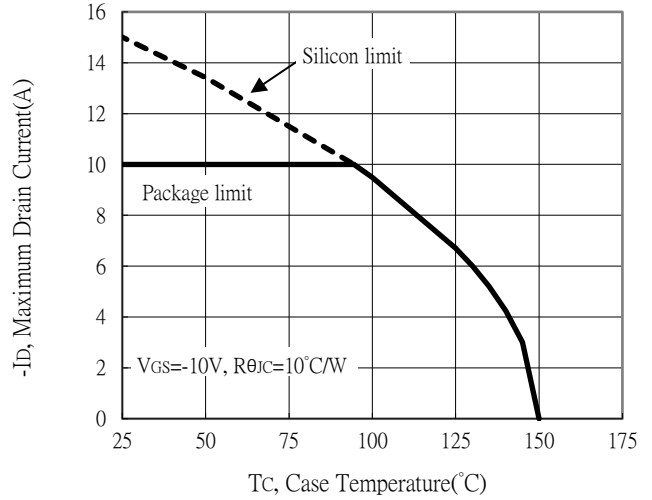


**Typical Characteristics (Cont.)**

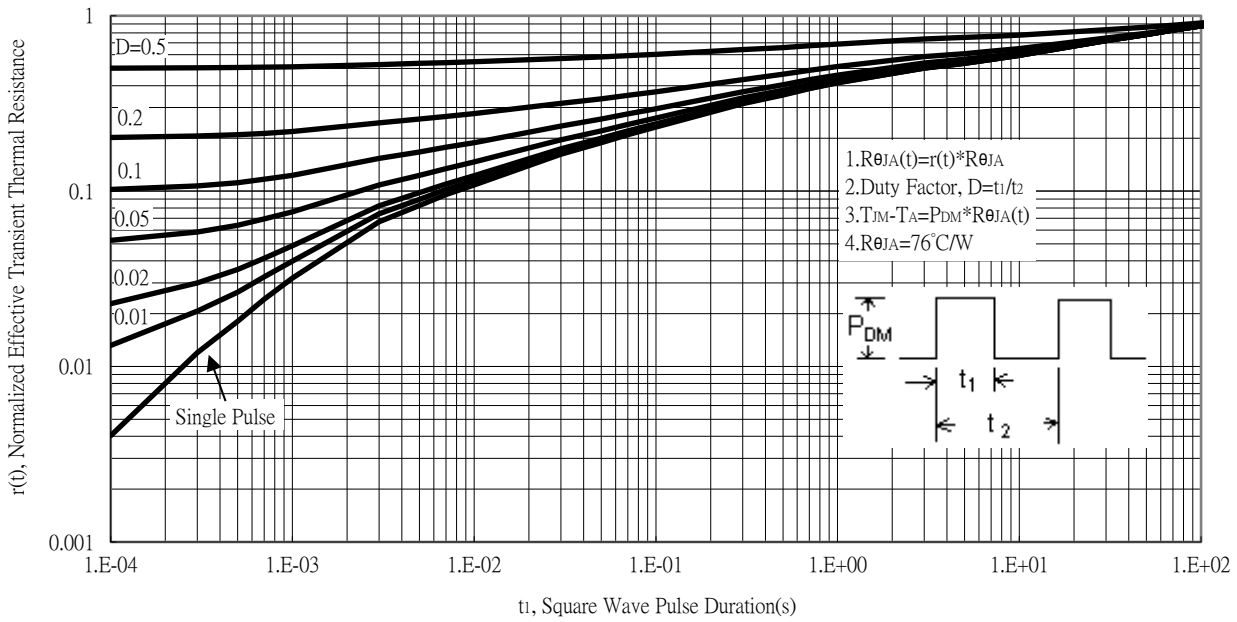
Single Pulse Power Rating, Junction to Ambient



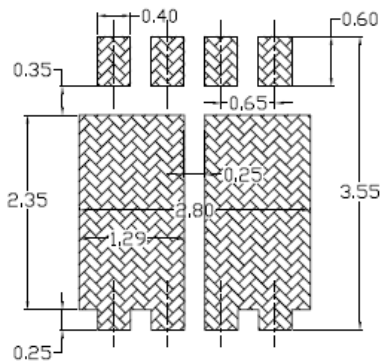
Maximum Drain Current vs Case Temperature



Transient Thermal Response Curves

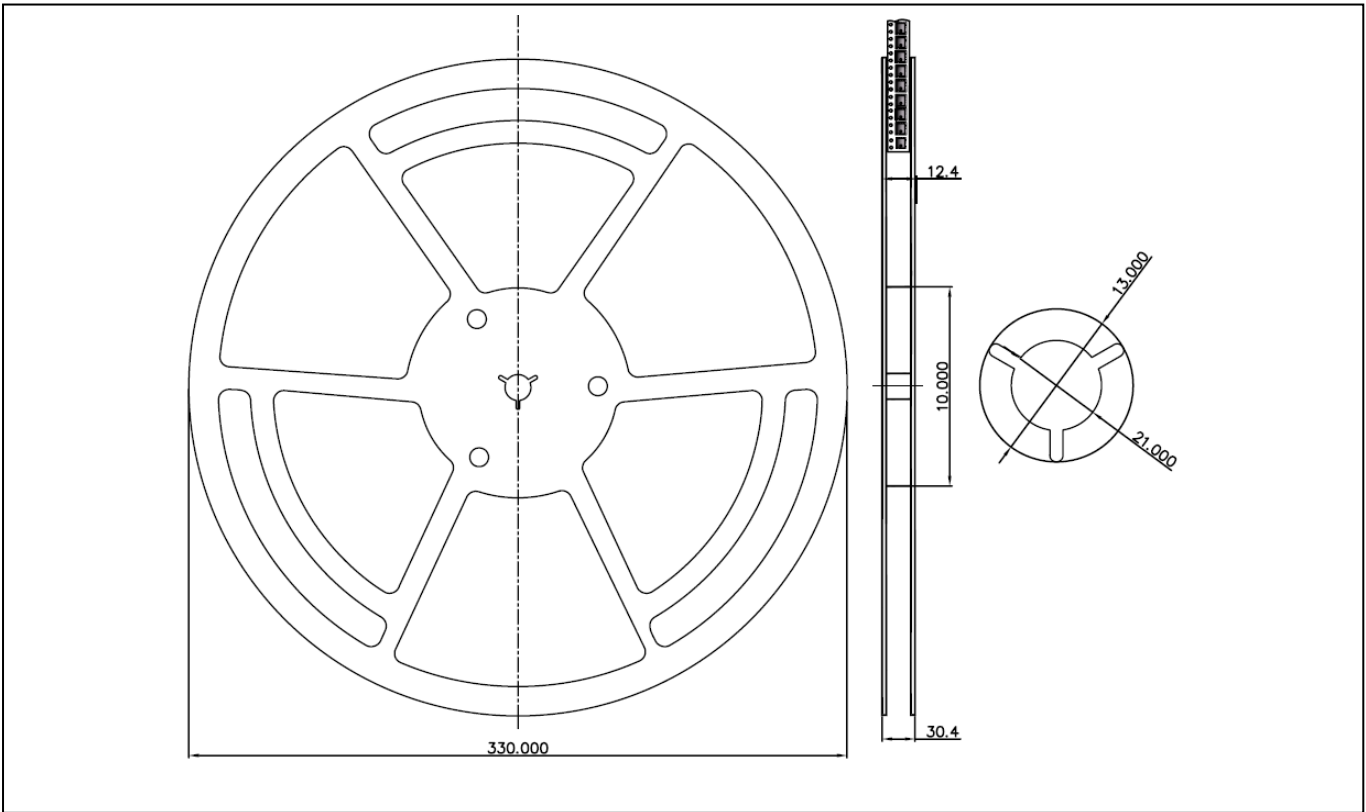


**Recommended Soldering Footprint**

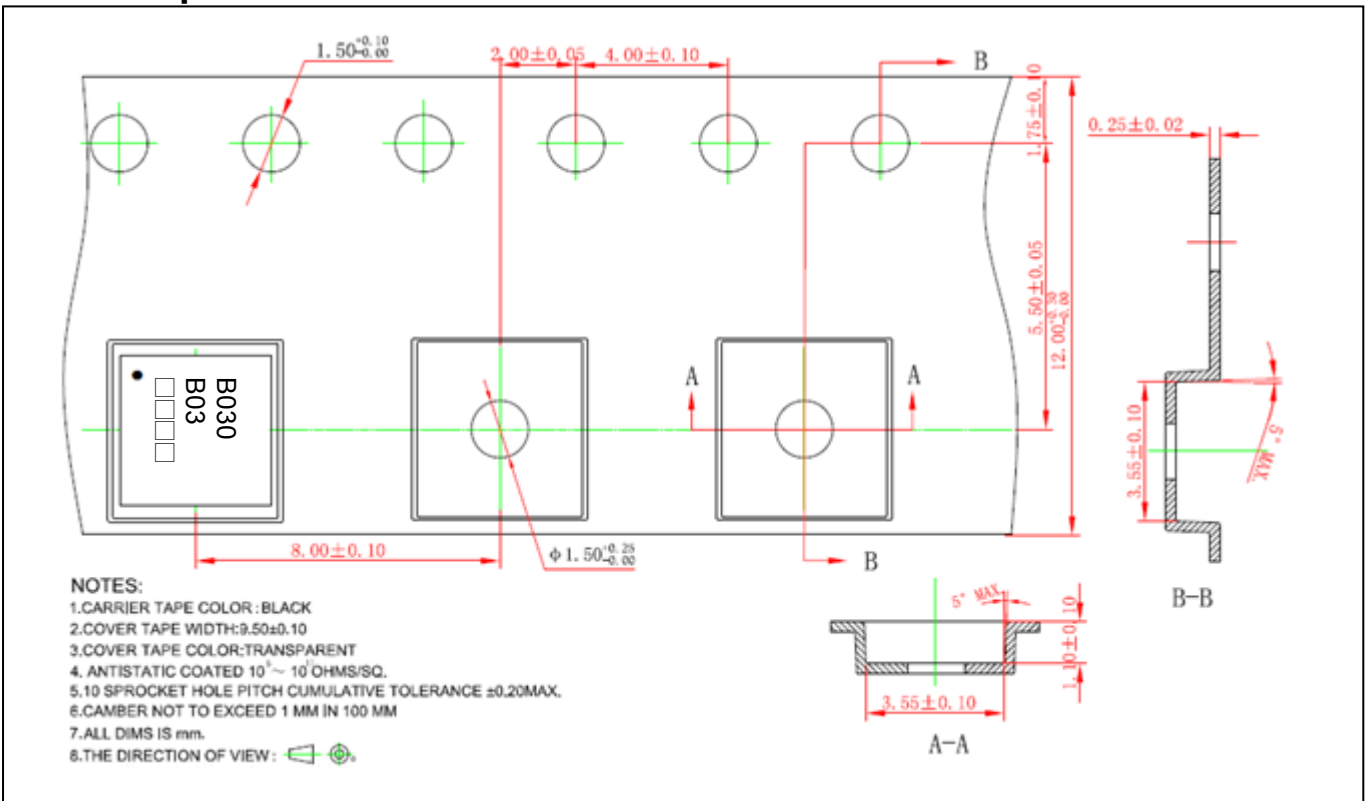


Unit : mm

**Reel Dimension**



**Carrier Tape Dimension**



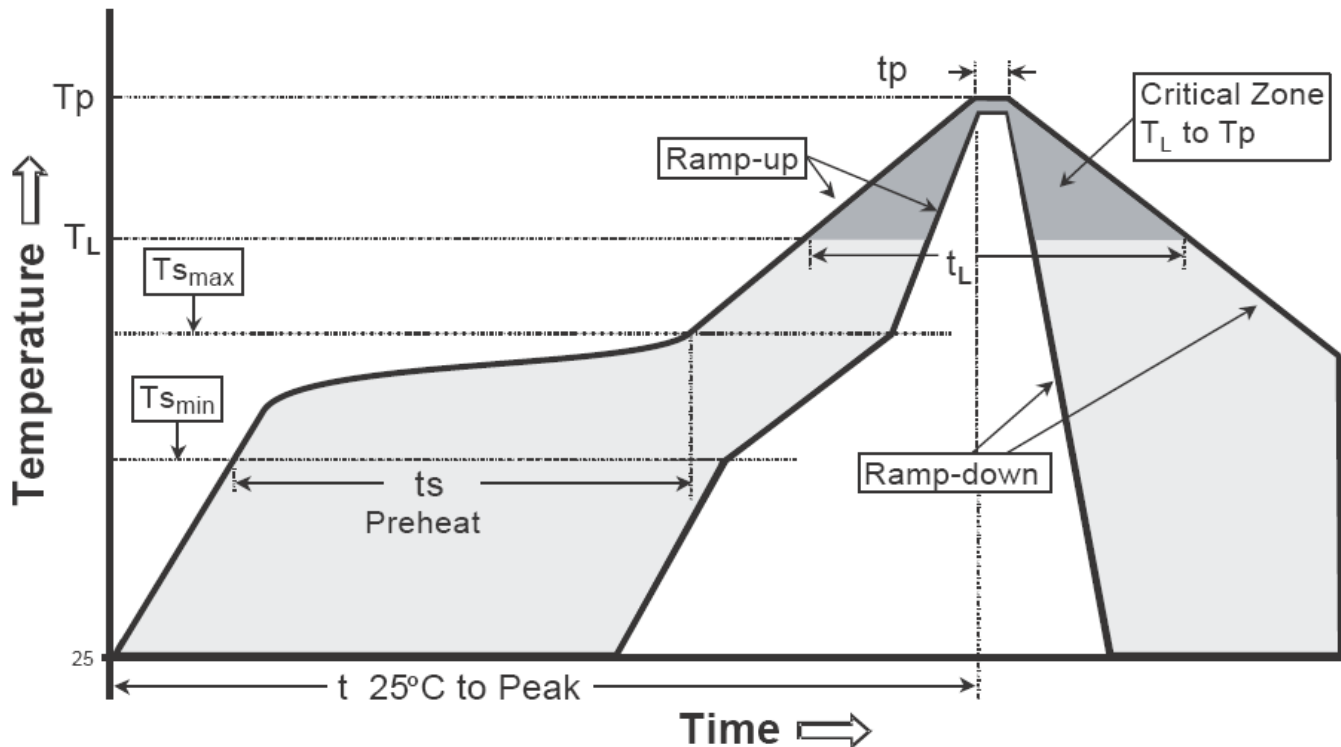
**NOTES:**

1. CARRIER TAPE COLOR : BLACK
2. COVER TAPE WIDTH :  $9.50 \pm 0.10$
3. COVER TAPE COLOR : TRANSPARENT
4. ANTISTATIC COATED  $10^5 \sim 10^7$  OHMS/SQ.
5. 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE  $\pm 0.20$  MAX.
6. CAMBER NOT TO EXCEED 1 MM IN 100 MM
7. ALL DIMS IS mm.
8. THE DIRECTION OF VIEW :

**Recommended wave soldering condition**

|                 |                  |                 |
|-----------------|------------------|-----------------|
| Product         | Peak Temperature | Soldering Time  |
| Pb-free devices | 260 +0/-5 °C     | 5 +1/-1 seconds |

**Recommended temperature profile for IR reflow**



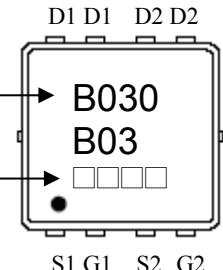
| Profile feature   | Sn-Pb eutectic Assembly | Pb-free Assembly |
|---|-------------------------|------------------|
| Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> ) | 3°C/second max.         | 3°C/second max.  |
| Preheat   |                         |                  |
| -Temperature Min(T <sub>s min</sub> )                       | 100°C                   | 150°C            |
| -Temperature Max(T <sub>s max</sub> )                       | 150°C                   | 200°C            |
| -Time(t <sub>s min</sub> to t <sub>s max</sub> )            | 60-120 seconds          | 60-180 seconds   |
| Time maintained above:                                      |                         |                  |
| -Temperature (T <sub>L</sub> )                              | 183°C                   | 217°C            |
| - Time (t <sub>L</sub> )                                    | 60-150 seconds          | 60-150 seconds   |
| Peak Temperature(T <sub>P</sub> )                           | 240 +0/-5 °C            | 260 +0/-5 °C     |
| Time within 5°C of actual peak temperature(t <sub>p</sub> ) | 10-30 seconds           | 20-40 seconds    |
| Ramp down rate  | 6°C/second max.         | 6°C/second max.  |
| Time 25 °C to peak temperature                              | 6 minutes max.          | 8 minutes max.   |

Note : All temperatures refer to topside of the package, measured on the package body surface.



**DFN3x3 Dimension (C forming)**

**Marking:**



Device Code → **B030**  
 Date Code → **B03**  
 S1 G1 S2 G2

**8-Lead DFN3x3 Plastic Package**  
 CYStek Package Code: V8

Date Code(counting from left to right) :  
 1<sup>st</sup> code: year code, the last digit of Christian year  
 2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C,  
 Apr→D, May→E, Jun→F, Jul→G, Aug→H,  
 Sep→J, Oct→K, Nov→L, Dec→M  
 3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

| DIM | Millimeters |       | Inches |       | DIM | Millimeters |       | Inches |       |
|-----|-------------|-------|--------|-------|-----|-------------|-------|--------|-------|
|     | Min.        | Max.  | Min.   | Max.  |     | Min.        | Max.  | Min.   | Max.  |
| A   | 0.650       | 0.850 | 0.026  | 0.033 | b   | 0.200       | 0.400 | 0.008  | 0.016 |
| A1  | 0.152       | REF   | 0.006  | REF   | e   | 0.550       | 0.750 | 0.022  | 0.030 |
| A2  | 0.000       | 0.050 | 0.000  | 0.002 | L   | 0.300       | 0.500 | 0.012  | 0.020 |
| D   | 2.900       | 3.100 | 0.114  | 0.122 | L1  | 0.180       | 0.480 | 0.007  | 0.019 |
| D1  | 0.935       | 1.135 | 0.037  | 0.045 | L2  | 0.100       | TYP   | 0.004  | TYP   |
| D2  | 0.280       | 0.480 | 0.011  | 0.019 | L3  | 0.100       | TYP   | 0.004  | TYP   |
| E   | 2.900       | 3.100 | 0.114  | 0.122 | H   | 0.315       | 0.515 | 0.012  | 0.020 |
| E1  | 3.150       | 3.450 | 0.124  | 0.136 | θ   | 9°          | 13°   | 9°     | 13°   |
| E2  | 1.535       | 1.935 | 0.060  | 0.076 |     |             |       |        |       |

- Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

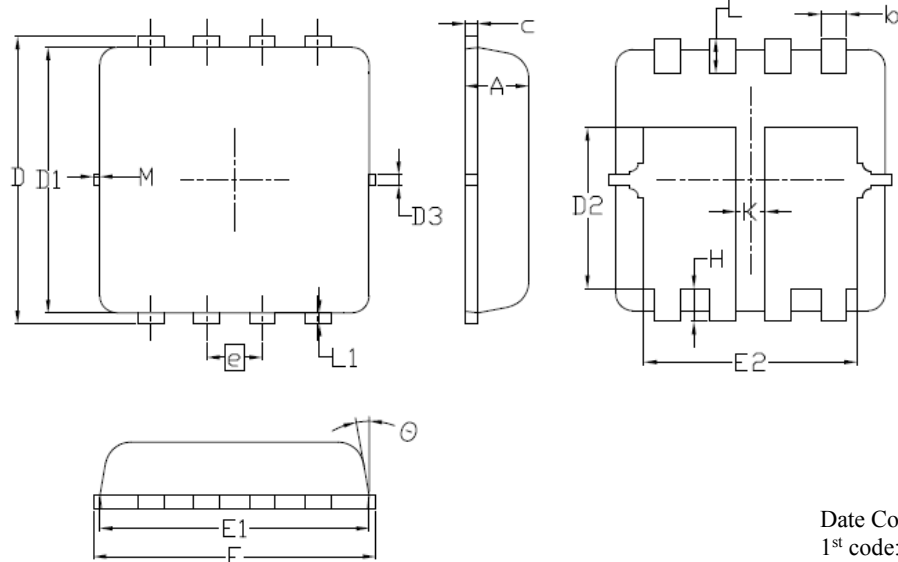
**Material:**

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

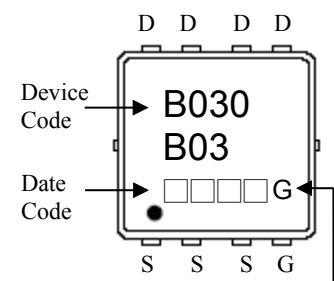
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**DFN3x3 Dimension (G forming)**



**Marking:**



Device Code → B030  
 B03

Date Code →     G

Assembly site code :  
 Blank → site 1  
 G → site 2

**8-Lead DFN3x3 Plastic Package**  
 CYStek Package Code: V8

Date Code(counting from left to right) :  
 1<sup>st</sup> code: year code, the last digit of Christian year  
 2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C,  
 Apr→D, May→E, Jun→F, Jul→G, Aug→H,  
 Sep→J, Oct→K, Nov→L, Dec→M  
 3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

| DIM | Millimeters |      | Inches |       | DIM | Millimeters |      | Inches |       |
|-----|-------------|------|--------|-------|-----|-------------|------|--------|-------|
|     | Min.        | Max. | Min.   | Max.  |     | Min.        | Max. | Min.   | Max.  |
| A   | 0.70        | 0.80 | 0.028  | 0.031 | E2  | 2.39        | 2.59 | 0.094  | 0.102 |
| b   | 0.25        | 0.35 | 0.010  | 0.014 | e   | 0.65        | BSC  | 0.026  | BSC   |
| c   | 0.10        | 0.25 | 0.004  | 0.010 | H   | 0.30        | 0.50 | 0.012  | 0.020 |
| D   | 3.25        | 3.45 | 0.128  | 0.136 | L   | 0.30        | 0.50 | 0.012  | 0.020 |
| D1  | 3.00        | 3.20 | 0.118  | 0.126 | L1  | 0.13        | TYP  | 0.005  | TYP   |
| D2  | 1.78        | 1.98 | 0.070  | 0.077 | K   | 0.30        | -    | 0.012  | -     |
| D3  | 0.13        | TYP  | 0.005  | TYP   | θ   | -           | 12°  | -      | 12°   |
| E   | 3.00        | 3.40 | 0.118  | 0.134 | M   | -           | 0.15 | -      | 0.006 |
| E1  | 3.00        | 3.20 | 0.118  | 0.126 |     |             |      |        |       |

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 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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