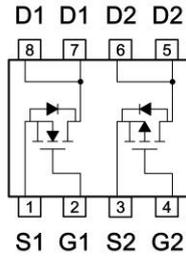
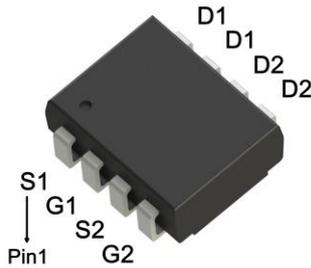


Product Summary

	N-CH	P-CH	
BV_{DSS}	30	-30	V
$R_{DS(ON)}$ typ. @ $V_{GS}=(-)10V$	14	40	mΩ
$R_{DS(ON)}$ typ. @ $V_{GS}=(-)4.5V$	22	68	
I_D @ $V_{GS}=(-)10V, T_C=25^\circ C$	9.9	-6.2	A
I_D @ $V_{GS}=(-)10V, T_A=25^\circ C$	5.9	-3.6	

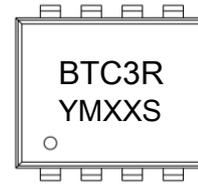
2928-8J



Features

- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free

Marking



← Device Code
← Date Code, Assembly site code

YM: Date Code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

A: Jan	B: Feb	C: Mar	D: Apr	E: May	F: Jun
G: Jul	H: Aug	J: Sep	K: Oct	L: Nov	M: Dec

XX: Production Serial Number, 01~99

S: Assembly site code, Site 1: G

Ordering Information

Device	Package	Shipping
MTB018C03RN8J-0-T1-G	2928-8J	3000pcs / Tape & Reel

0: Product rank, zero for no rank products.

T1: Packing spec, T1 : 3000pcs / tape & reel, 7" reel

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

Absolute Maximum Ratings ($T_A=25^\circ C$)

Parameter	Symbol	Value		Unit	
		N-CH	P-CH		
Drain-Source Voltage	V_{DS}	30	-30	V	
Gate-Source Voltage	V_{GS}	± 20	± 20		
Continuous Drain Current @ $V_{GS}=(-)10V, T_C=25^\circ C$	I_D	9.9	-6.2	A	
Continuous Drain Current @ $V_{GS}=(-)10V, T_C=100^\circ C$		6.3	-3.9		
Continuous Drain Current @ $V_{GS}=(-)10V, T_A=25^\circ C$		5.9	-3.6		
Continuous Drain Current @ $V_{GS}=(-)10V, T_A=70^\circ C$		4.7	-2.9		
Pulsed Drain Current	I_{DM}	39	-25		
Continuous Body Diode Forward Current @ $T_C=25^\circ C$	I_S	2.7	-3		
Pulsed Body Diode Forward Current @ $T_C=25^\circ C$	I_{SM}	10	-12		
Avalanche Current @ $L=0.1mH$	I_{AS}	5	-10		
Avalanche Energy @ $L=0.5mH$	E_{AS}	4	9	mJ	
Total Power Dissipation	P_D	$T_C=25^\circ C$	3.3	3.6	W
		$T_C=100^\circ C$	1.3	1.4	
		$T_A=25^\circ C$	1.2	1.2	
		$T_A=70^\circ C$	0.8	0.8	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150		$^\circ C$	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JC}$	38	35	$^\circ C/W$	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	108	101		



N-Channel Electrical Characteristics (T_A=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	30	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1.2	-	2.5		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	4.2	-	S	V _{DS} =10V, I _D =4.5A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =24V, V _{GS} =0V
R _{DS(ON)}	-	14	19	mΩ	V _{GS} =10V, I _D =4.5A
	-	22	31		V _{GS} =4.5V, I _D =2A
Dynamic					
C _{iSS}	-	270	-	pF	V _{DS} =15V, V _{GS} =0V, f=1MHz
C _{oss}	-	200	-		
C _{rSS}	-	34	-		
R _g	-	0.4	-	Ω	f=1MHz
Q _g *d,e	-	3	-	nC	V _{DS} =15V, I _D =4.5A, V _{GS} =4.5V
Q _g *d,e	-	6	-		
Q _{gs} *d,e	-	1.2	-		
Q _{gd} *d,e	-	1.2	-		
t _{d(ON)} *d,e	-	4.6	-	ns	V _{DS} =15V, I _D =3A, V _{GS} =10V, R _{GS} =10Ω
t _r *d,e	-	16	-		
t _{d(OFF)} *d,e	-	13.5	-		
t _f *d,e	-	4.3	-		
Source-Drain Diode					
V _{SD} *d	-	0.83	1.2	V	I _S =4.5A, V _{GS} =0V
t _{rr}	-	11	-	ns	I _F =4A, di/dt=100A/μs
Q _{rr}	-	3	-	nC	

Note:

- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- *b. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to
- *d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- *e. Independent of operating temperature.



P-Channel Electrical Characteristics (T_A=25°C, unless otherwise specified)

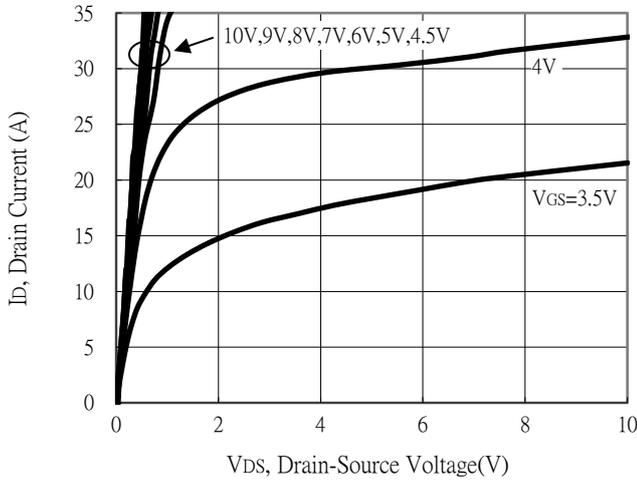
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-30	-	-	V	V _{GS} =0V, I _D =-250μA
V _{GS(th)}	-1.2	-	-2.5		V _{DS} =V _{GS} , I _D =-250μA
G _{FS}	-	5.1	-	S	V _{DS} =-10V, I _D =-3A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-24V, V _{GS} =0V
R _{DS(ON)}	-	40	54	mΩ	V _{GS} =-10V, I _D =-3A
	-	68	98		V _{GS} =-4.5V, I _D =-1.5A
Dynamic					
C _{iSS}	-	470	-	pF	V _{DS} =-15V, V _{GS} =0V, f=1MHz
C _{oss}	-	65	-		
C _{rSS}	-	60	-		
R _g	-	10	-	Ω	f=1MHz
Q _g *d,e	-	5.3	-	nC	V _{DS} =-15V, I _D =-3A, V _{GS} =-4.5V
Q _g *d,e	-	11	-		
Q _{gs} *d,e	-	1.6	-		
Q _{gd} *d,e	-	2.2	-		
t _{d(ON)} *d,e	-	6	-	ns	V _{DS} =-15V, I _D =-3A, V _{GS} =-10V, R _{GS} =1Ω
t _r *d,e	-	16	-		
t _{d(OFF)} *d,e	-	30	-		
t _f *d,e	-	7	-		
Source-Drain Diode					
V _{SD} *d	-	-0.88	-1.2	V	I _S =-3A, V _{GS} =0V
t _{rr}	-	8.5	-	ns	I _F =-3A, di/dt=100A/μs
Q _{rr}	-	4	-	nC	

Note:

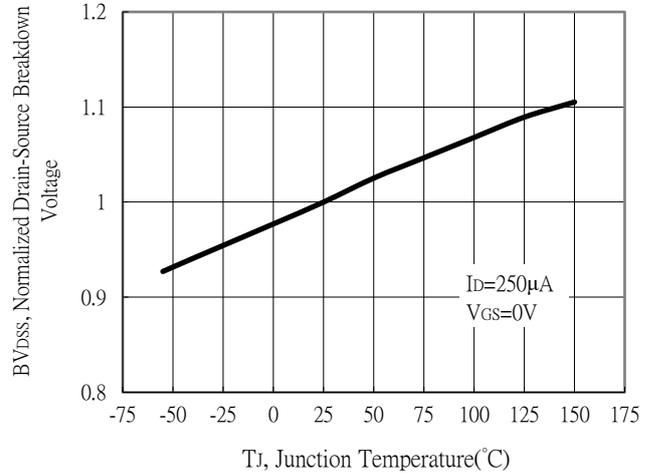
- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- *b. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to
- *d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- *e. Independent of operating temperature.

N-Channel Typical Characteristics

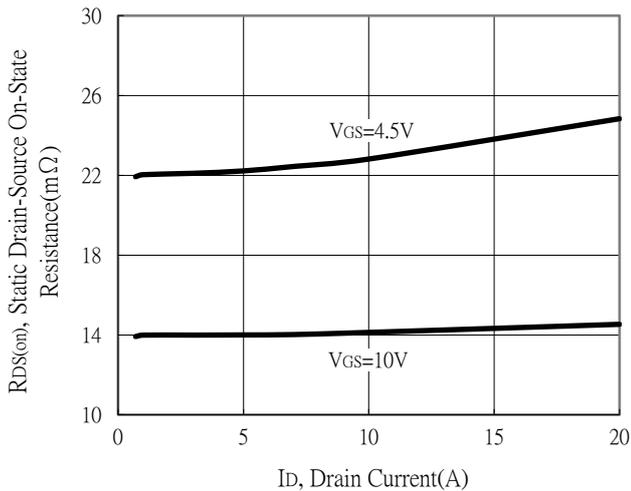
Typical Output Characteristics



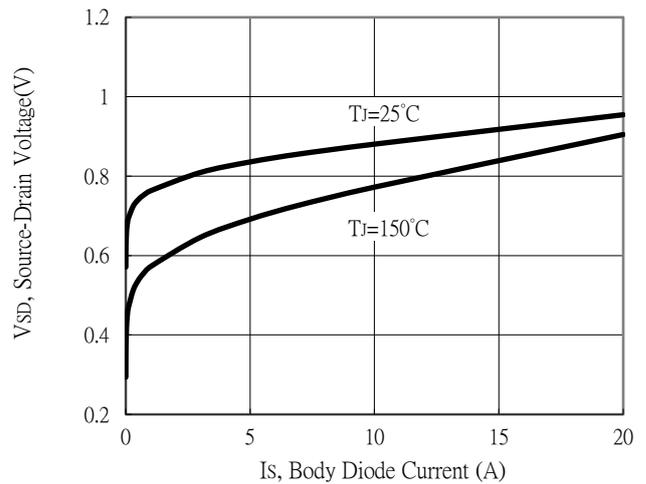
Breakdown Voltage vs Ambient Temperature



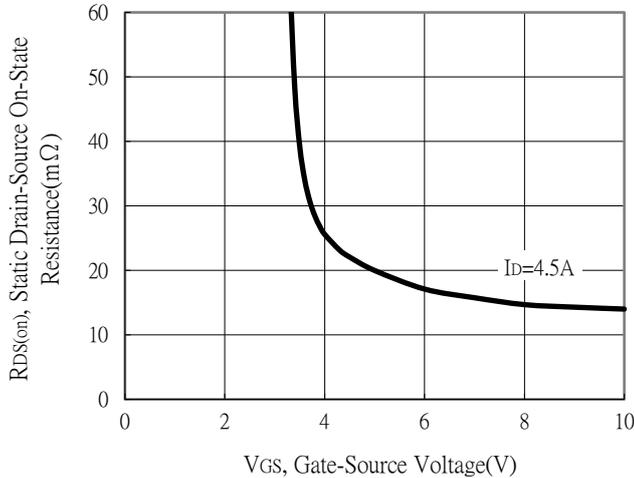
Static Drain-Source On-State resistance vs Drain Current



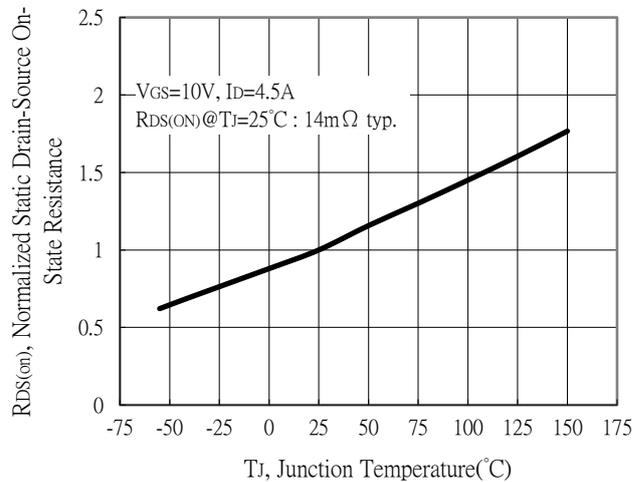
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

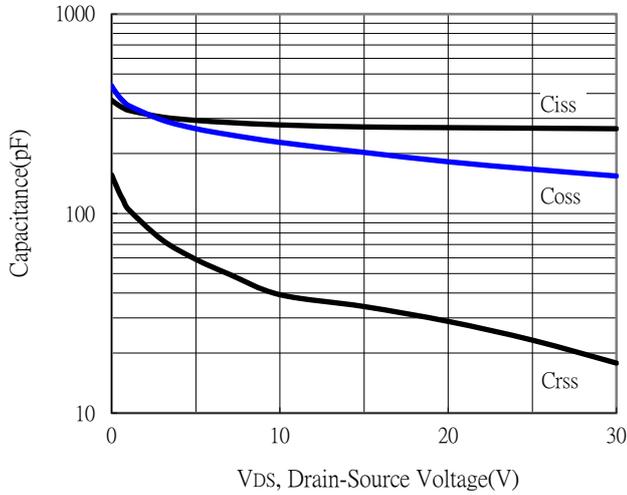


Drain-Source On-State Resistance vs Junction Temperature

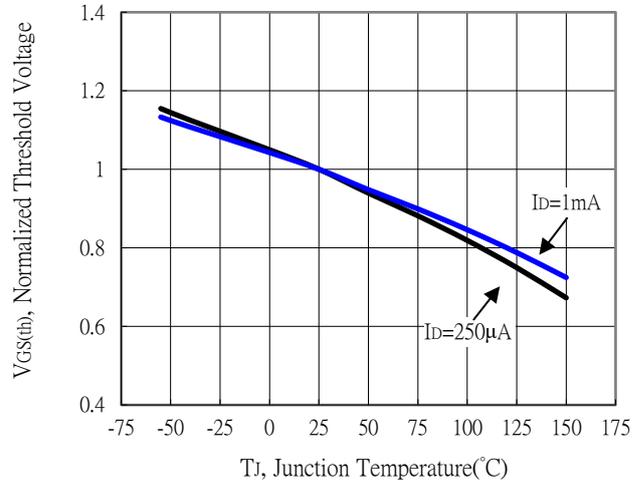


N-Channel Typical Characteristics

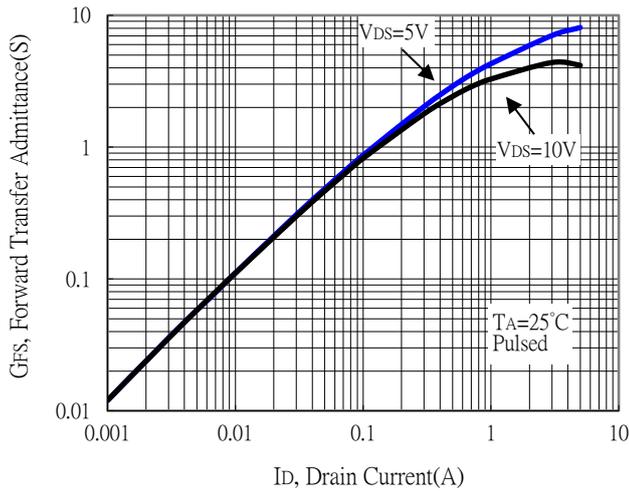
Capacitance vs Drain-to-Source Voltage



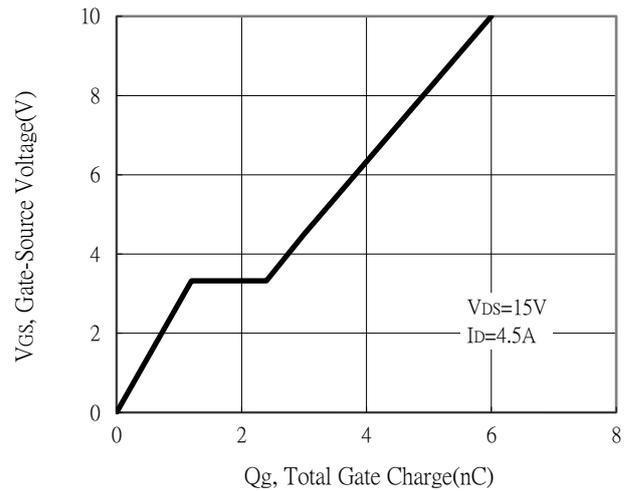
Threshold Voltage vs Junction Temperature



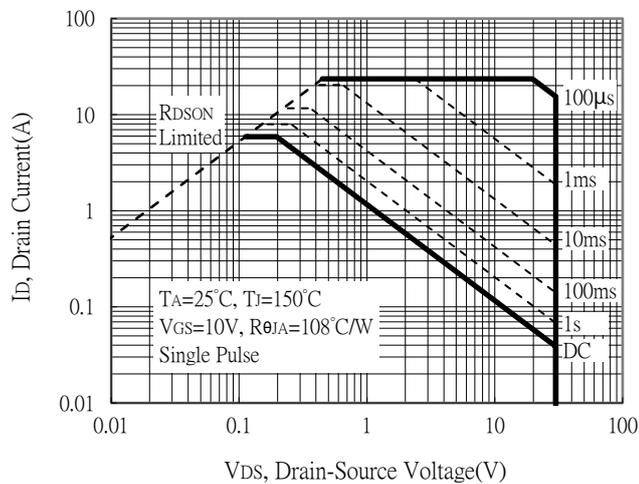
Forward Transfer Admittance vs Drain Current



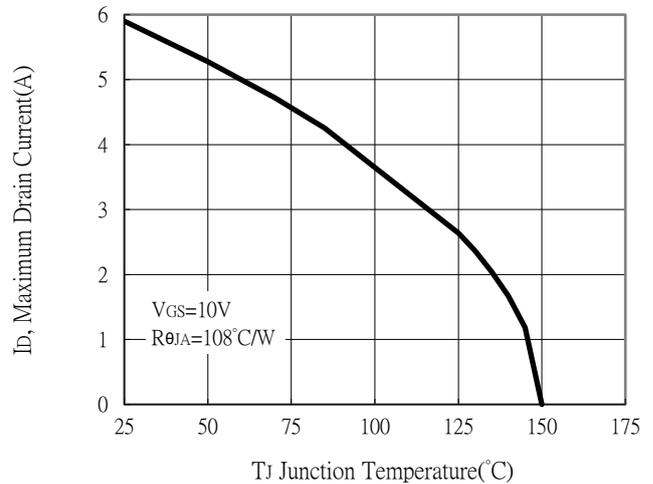
Gate Charge Characteristics



Maximum Safe Operating Area

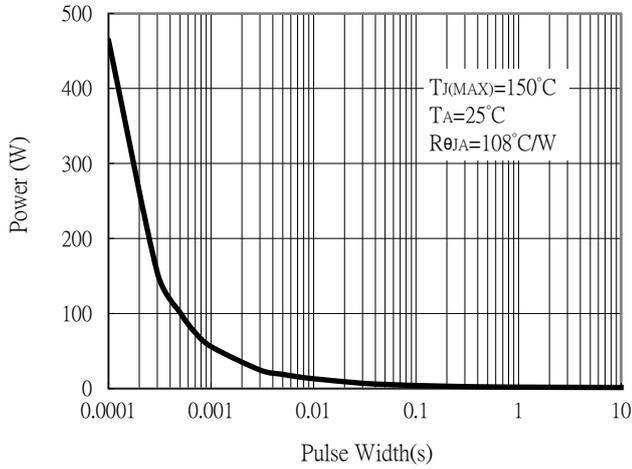


Maximum Drain Current vs Junction Temperature

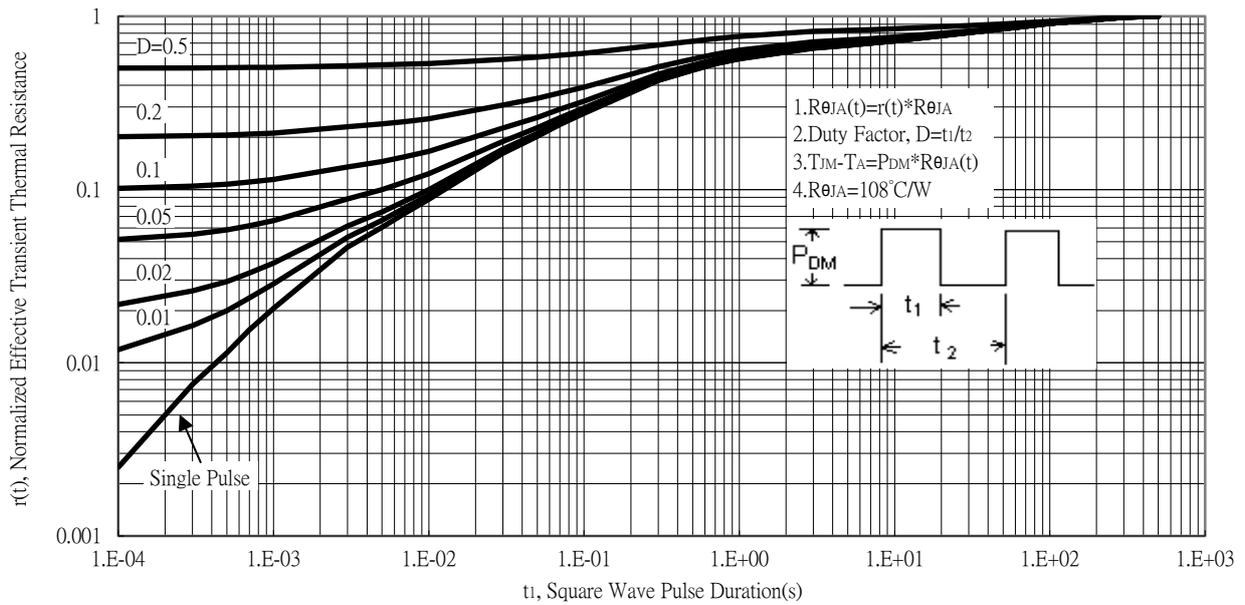


N-Channel Typical Characteristics

Single Pulse Power Rating, Junction to Ambient

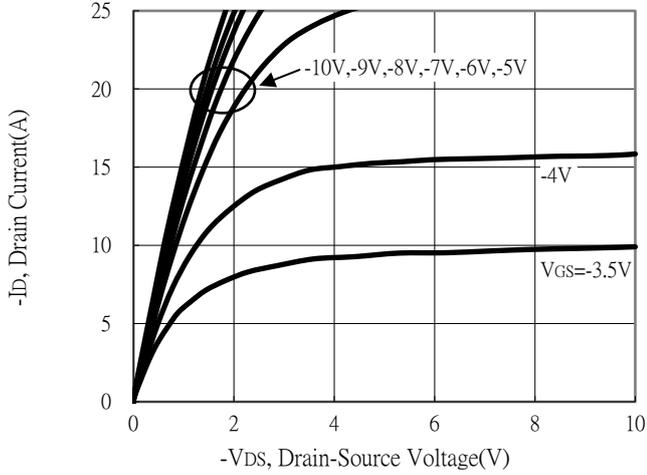


Transient Thermal Response Curves

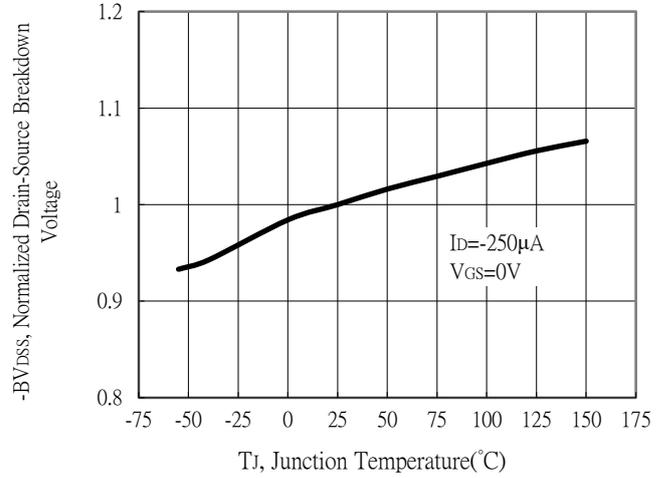


P-Channel Typical Characteristics

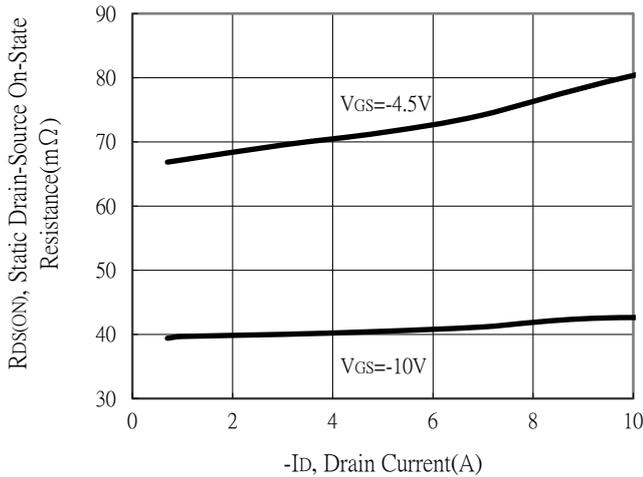
Typical Output Characteristics



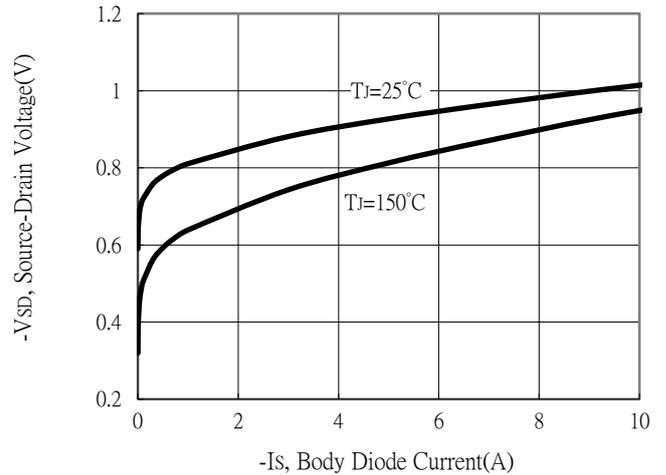
Breakdown Voltage vs Ambient Temperature



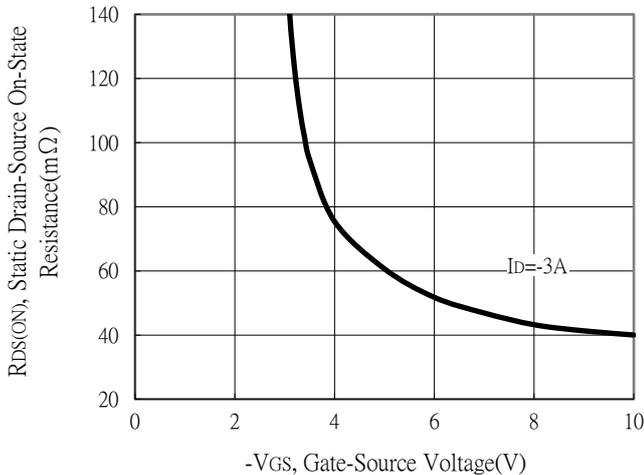
Static Drain-Source On-State resistance vs Drain Current



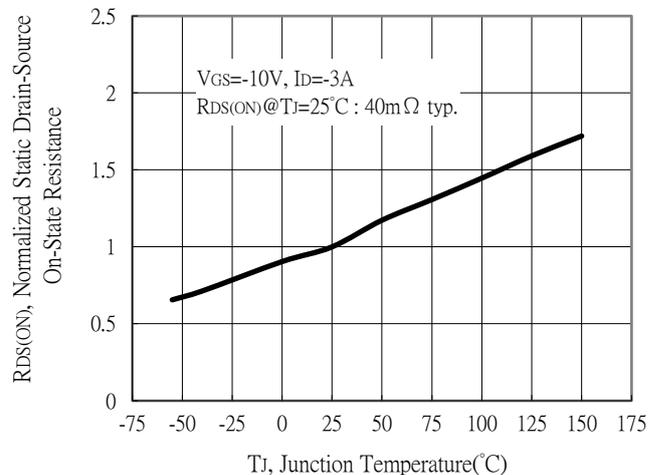
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

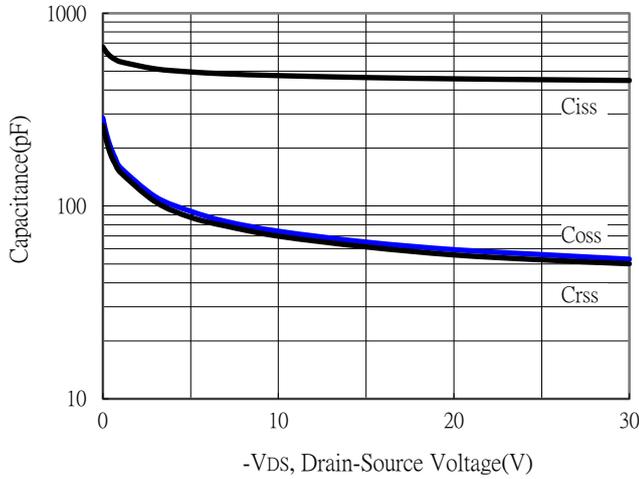


Drain-Source On-State Resistance vs Junction Temperature

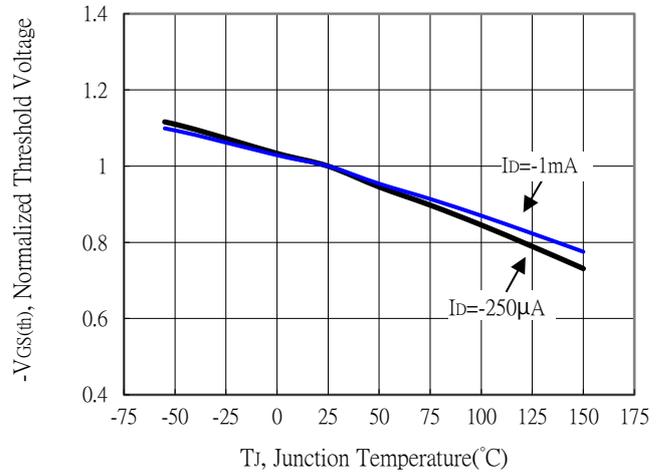


P-Channel Typical Characteristics

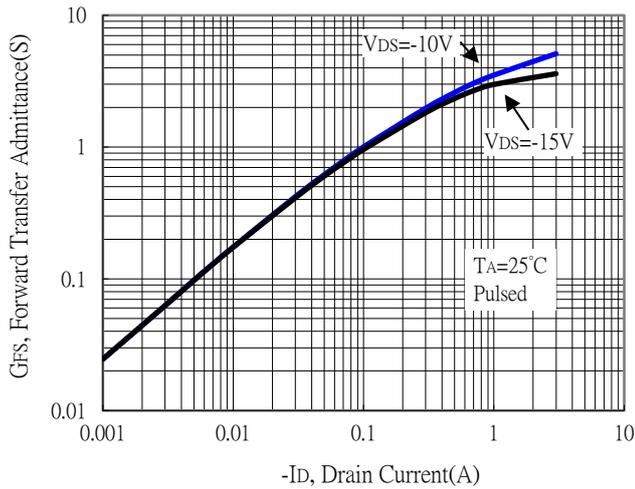
Capacitance vs Drain-to-Source Voltage



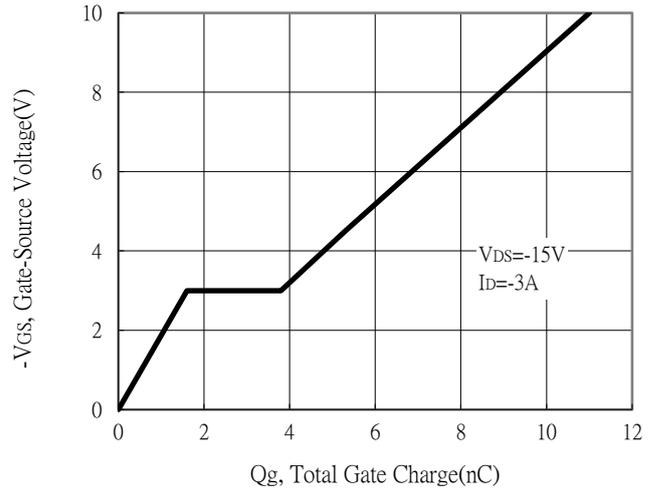
Threshold Voltage vs Junction Temperature



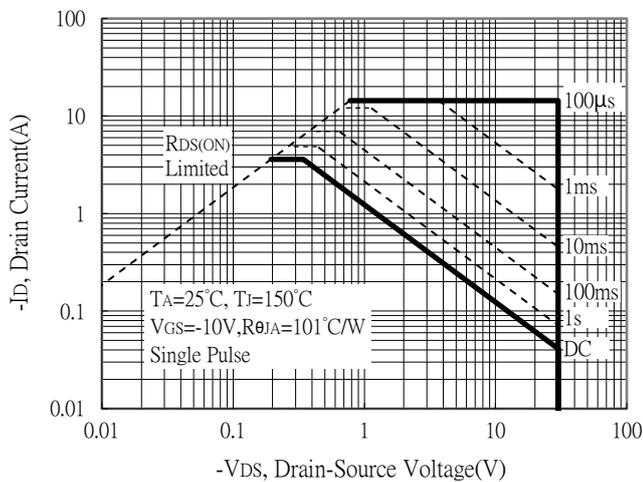
Forward Transfer Admittance vs Drain Current



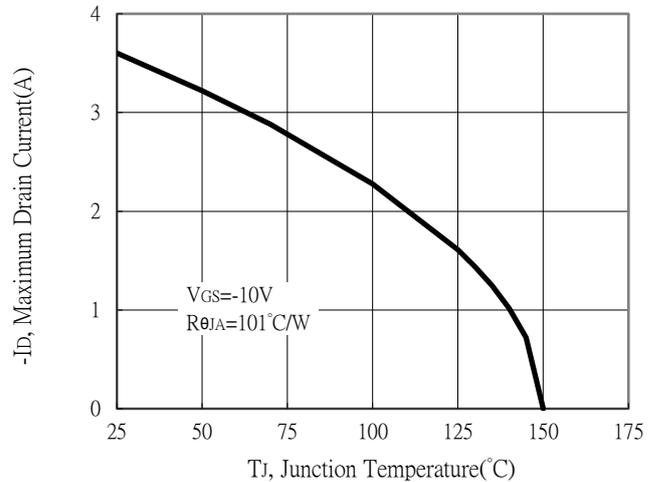
Gate Charge Characteristics



Maximum Safe Operating Area

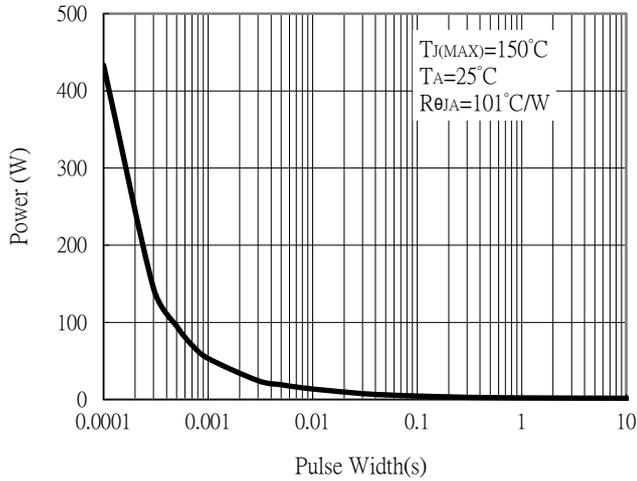


Maximum Drain Current vs Junction Temperature

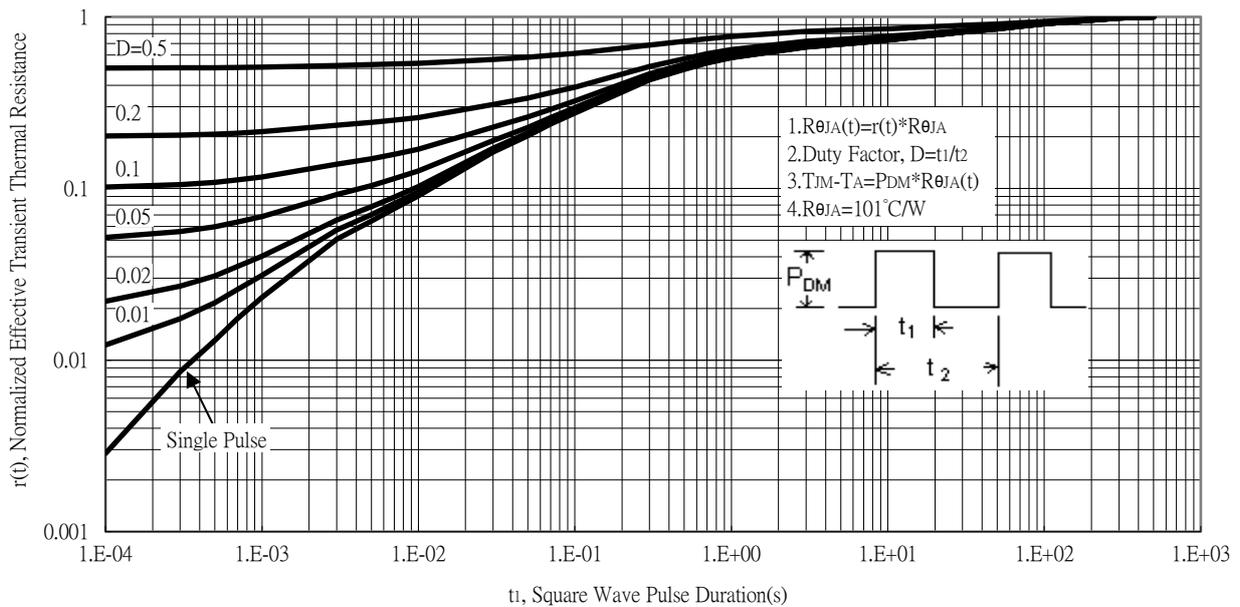


P-Channel Typical Characteristics

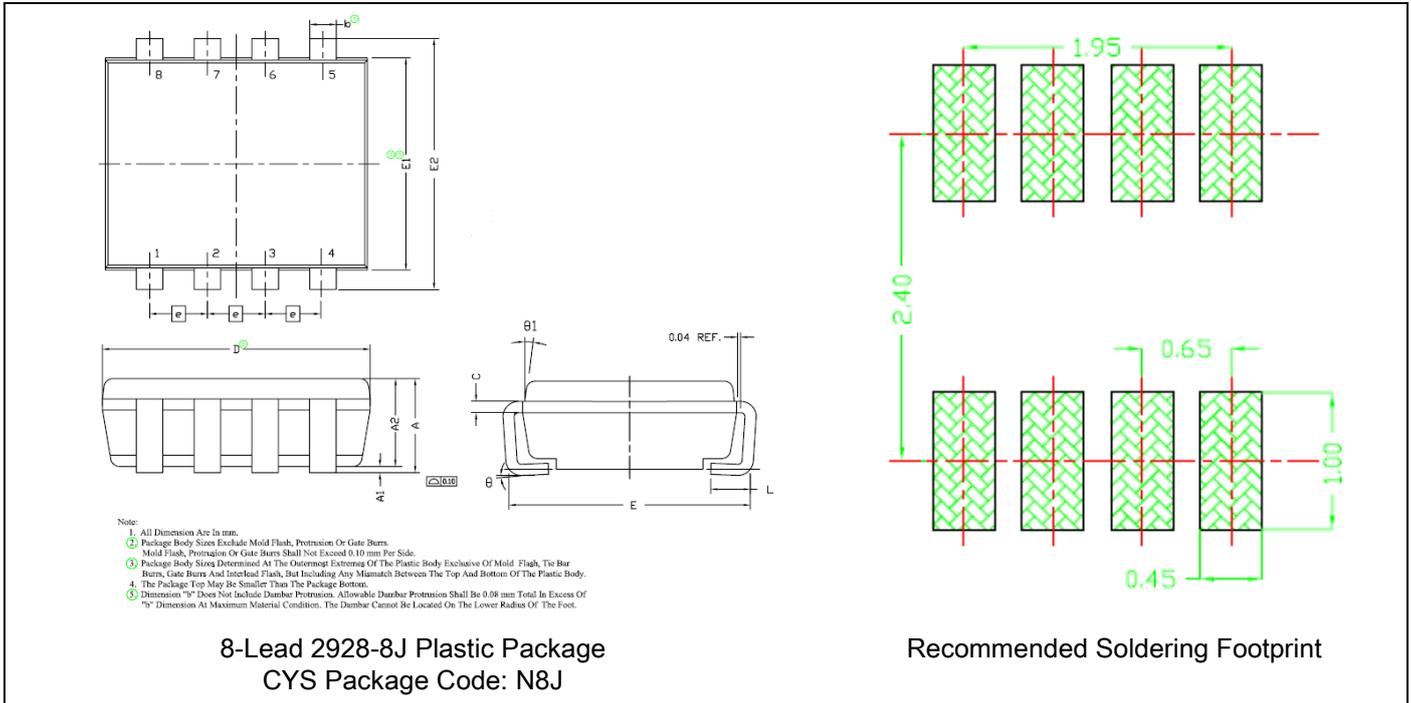
Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



2928-8J Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0368	0.0433	0.935	1.100	E1	0.0906	0.0984	2.300	2.500
A1	0.0004	0.0039	0.010	0.100	E2	0.1043	0.1201	2.650	3.050
A2	0.0364	0.0394	0.925	1.000	e	0.0256	BSC	0.650	BSC
b	0.0098	0.0157	0.250	0.400	L	0.0118	0.0236	0.300	0.600
c	0.0039	0.0079	0.100	0.200	θ	0°	8°	0°	8°
D	0.1161	0.1220	2.950	3.100	θ1	7°	TYP	7°	TYP
E	0.0984	0.1181	2.500	3.000					

Note:

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

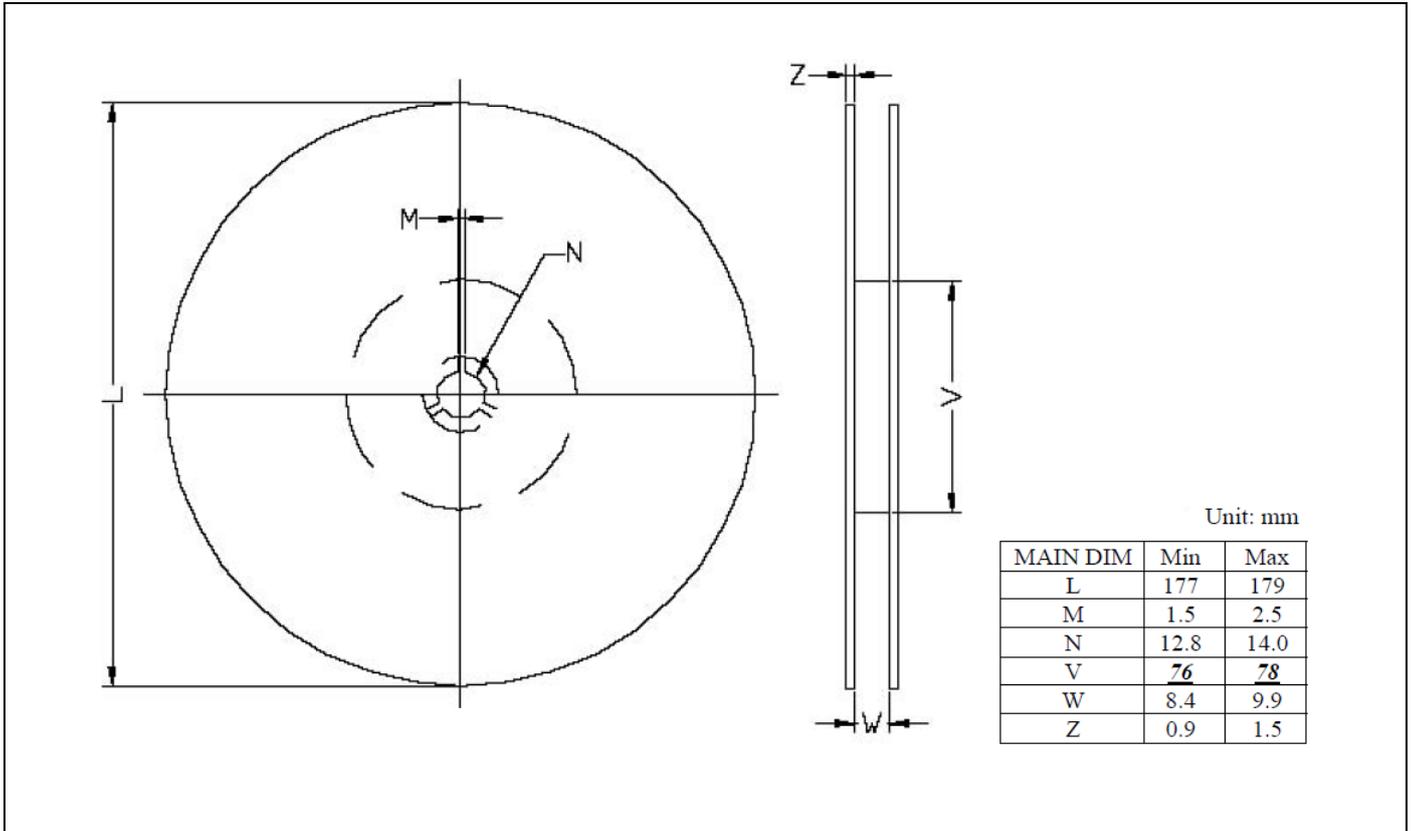
Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

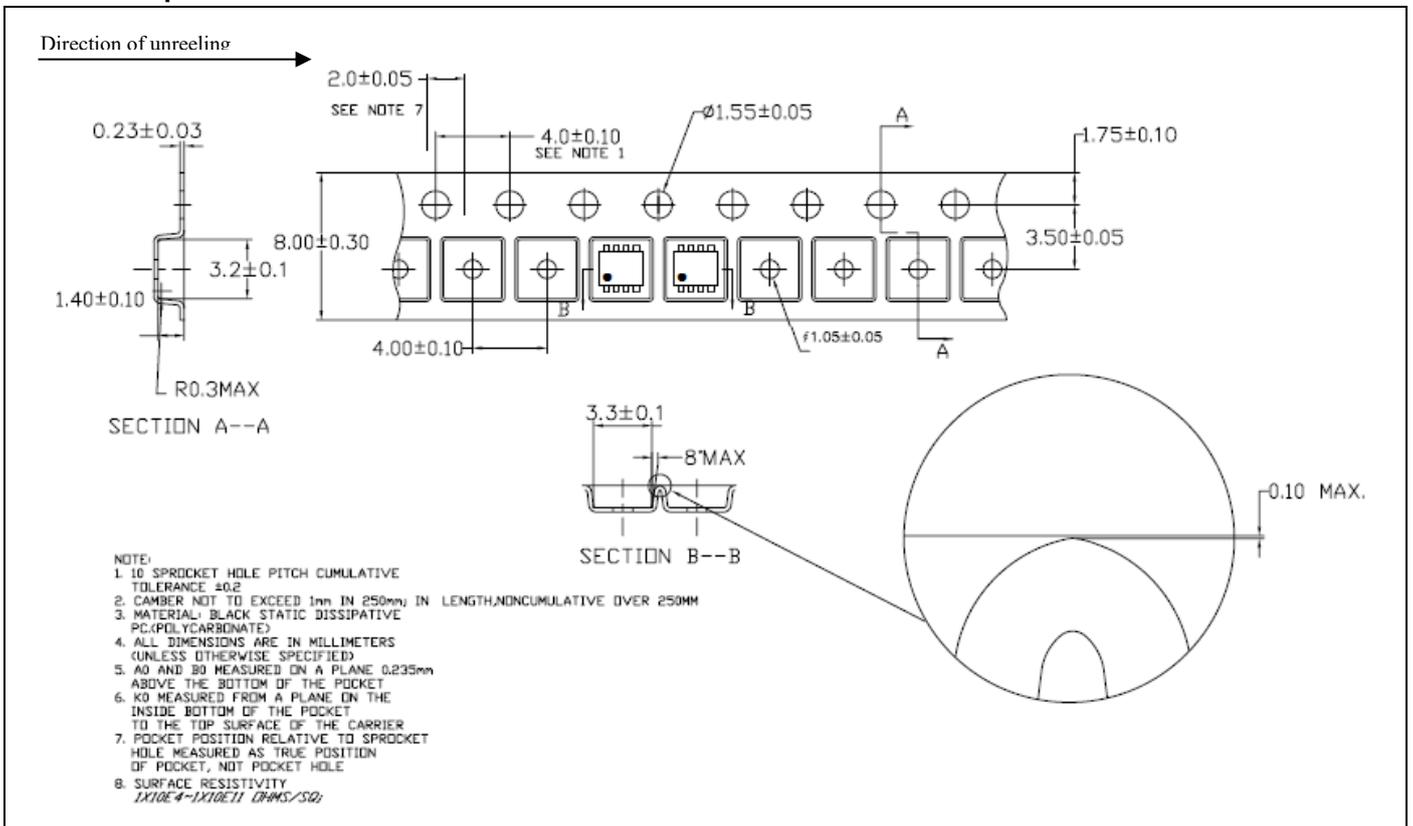
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Reel Dimension



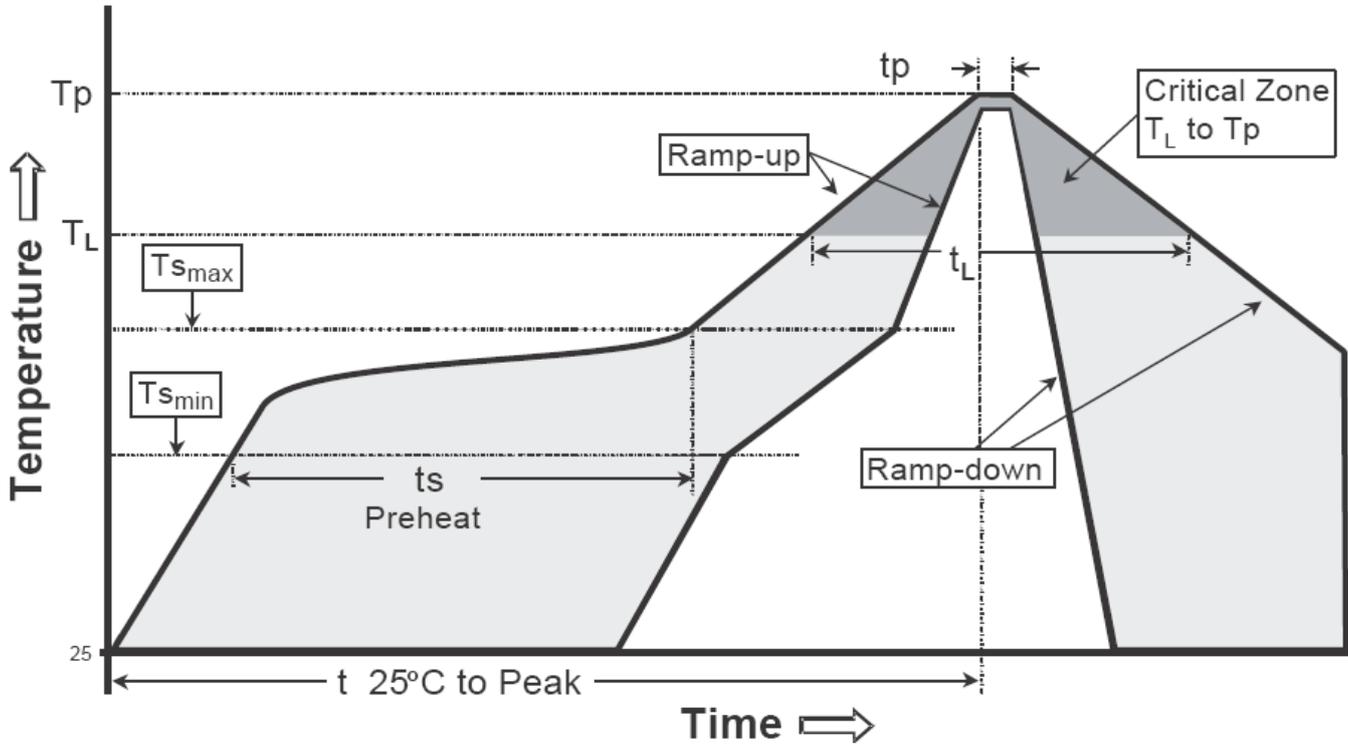
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Ts max to Tp)	3°C/second max.	3°C/second max.
Preheat -Temperature Min (Ts min) -Temperature Max (Ts max) -Time (ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (Tl) -Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note:

- All temperatures refer to topside of the package, measured on the package body surface.