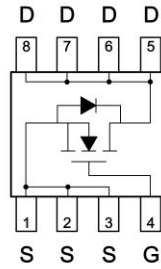
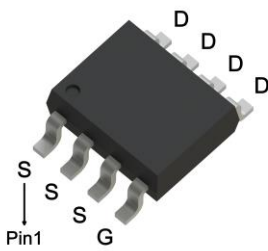


Product Summary

BV_{DSS}	40	V
$R_{DS(ON)}$ typ. @ $V_{GS}=10V, I_D=8A$	12	mΩ
$R_{DS(ON)}$ typ. @ $V_{GS}=4.5V, I_D=4A$	16	
I_D @ $V_{GS}=10V, T_C=25^\circ C$	16	A
I_D @ $V_{GS}=10V, T_A=25^\circ C$	8	

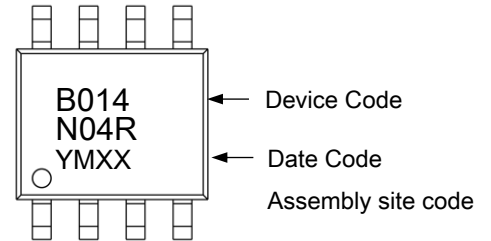
SOP-8



Features

- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free

Marking



YMXXS: Date Code & Assembly site code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

A: Jan	B: Feb	C: Mar	D: Apr	E: May	F: Jun
G: Jul	H: Aug	J: Sep	K: Oct	L: Nov	M: Dec

XX: Production Serial Number, 01~99

Ordering Information

Device	Package	Shipping
MTB014N04RQ8-0-TF-G	SOP-8	4000pcs / Tape & Reel

0: Product rank, zero for no rank products.

TF: Packing spec, TF : 4000pcs / tape & reel, 13" reel

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

Absolute Maximum Ratings ($T_A=25^\circ C$)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V_{DS}	40	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current @ $V_{GS}=10V, T_C=25^\circ C$	I_D	16	A	
Continuous Drain Current @ $V_{GS}=10V, T_C=100^\circ C$		10		
Continuous Drain Current @ $V_{GS}=10V, T_A=25^\circ C$		8		
Continuous Drain Current @ $V_{GS}=10V, T_A=70^\circ C$		6.4		
Pulsed Drain Current	I_{DM}	64		
Continuous Body Diode Forward Current @ $T_C=25^\circ C$	I_S	13		
Pulsed Body Diode Forward Current @ $T_C=25^\circ C$	I_{SM}	52		
Avalanche Current @ $L=0.1mH$	I_{AS}	10		
Avalanche Energy @ $L=0.5mH$	E_{AS}	6		
Total Power Dissipation	P_D	$T_C=25^\circ C$	8.3	W
		$T_C=100^\circ C$	3.3	
		$T_A=25^\circ C$	1.9	
		$T_A=70^\circ C$	1.2	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ C$	
Steady State Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	15	$^\circ C/W$	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	65		



Electrical Characteristics (T_A=25°C, unless otherwise specified)

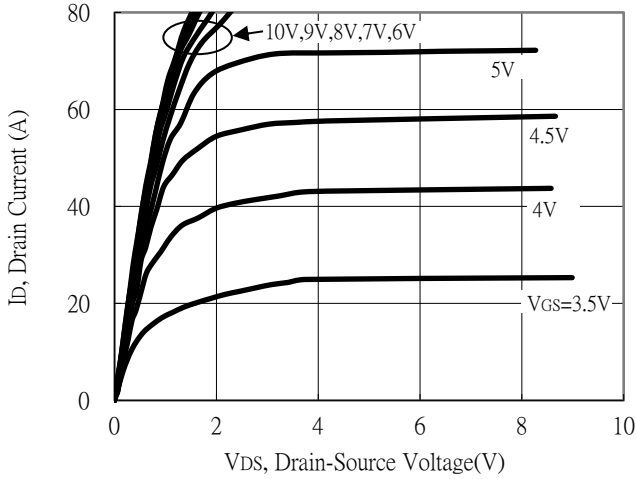
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	40	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	-	2.5		V _{DS} =V _{GS} , I _D =250μA
G _{FS}	-	11	-	S	V _{DS} =5V, I _D =7A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =32V, V _{GS} =0V
R _{DS(ON)}	-	12	16	mΩ	V _{GS} =10V, I _D =8A
	-	16	23		V _{GS} =4.5V, I _D =4A
Dynamic					
C _{iss}	-	400	-	pF	V _{DS} =20V, V _{GS} =0V, f=1MHz
C _{oss}	-	150	-		
C _{rss}	-	25	-		
R _g	-	0.75	-	Ω	f=1MHz
Q _g	^{*d,e}	-	3.8	nC	V _{DS} =20V, I _D =8A, V _{GS} =4.5V
	^{*d,e}	-	7.6		
Q _{gs}	^{*d,e}	-	2		
Q _{gd}	^{*d,e}	-	1.2		
t _{d(ON)}	^{*d,e}	-	5.4	ns	V _{DS} =20V, I _D =8A, V _{GS} =10V, R _{GS} =6Ω
t _r	^{*d,e}	-	13		
t _{d(OFF)}	^{*d,e}	-	17		
t _f	^{*d,e}	-	5.2		
Source-Drain Diode					
V _{SD}	^{*d}	-	0.86	V	I _S =8A, V _{GS} =0V
t _{rr}	-	-	9.5	ns	I _F =8A, di/dt=100A/μs
Q _{rr}	-	-	2.4	nC	

Note:

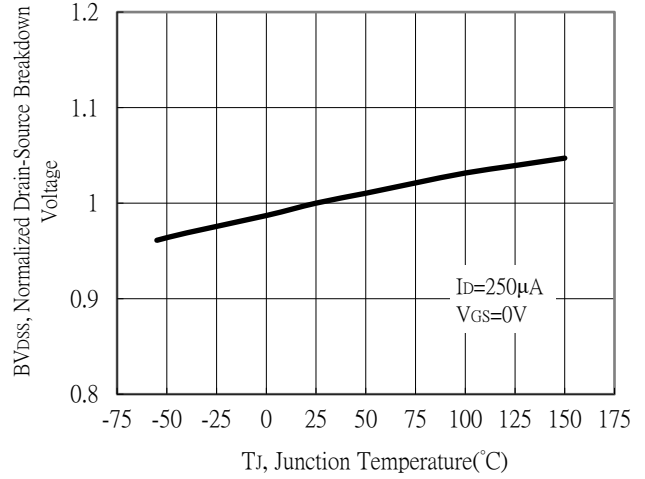
- *a. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper Dissipation.
- *b. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz copper, in a still air environment with T_A=25°C. The power dissipation P_D is based on R_{θJA} and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- *c. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.
- *d. Pulse Test : Pulse Width≤300μs, Duty Cycle≤2%.
- *e. Independent of operating temperature.

Typical Characteristics

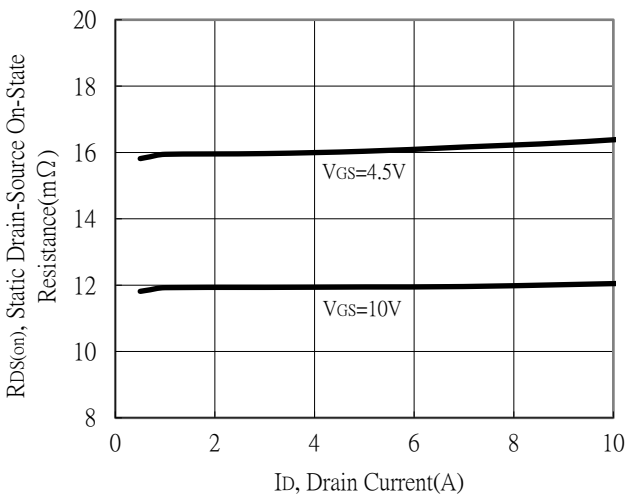
Typical Output Characteristics



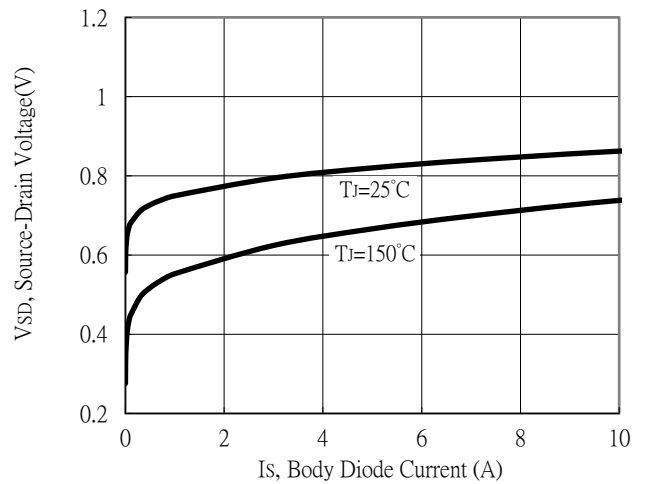
Breakdown Voltage vs Ambient Temperature



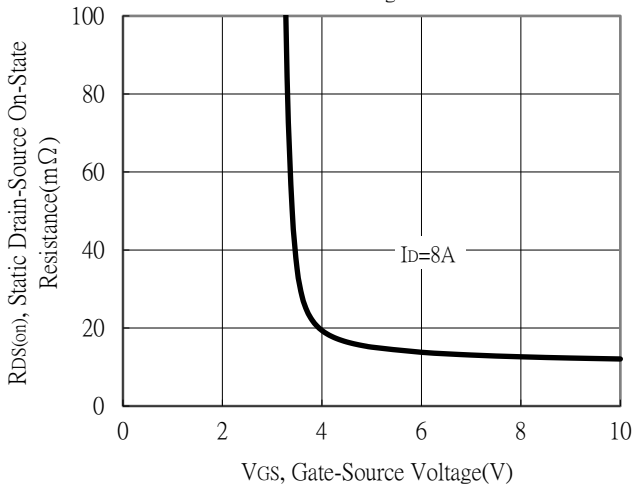
Static Drain-Source On-State resistance vs Drain Current



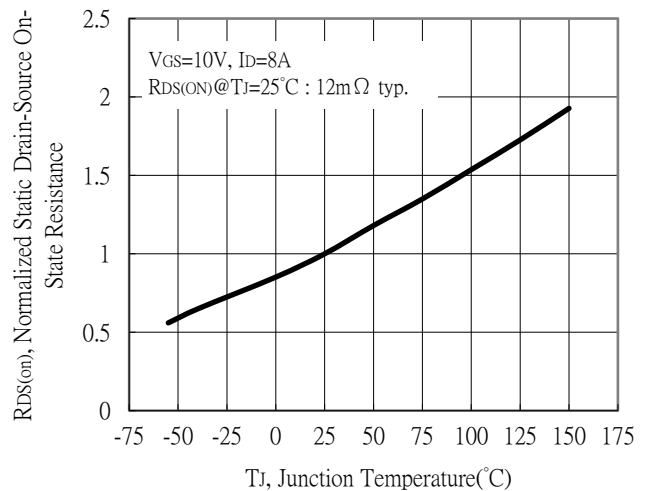
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

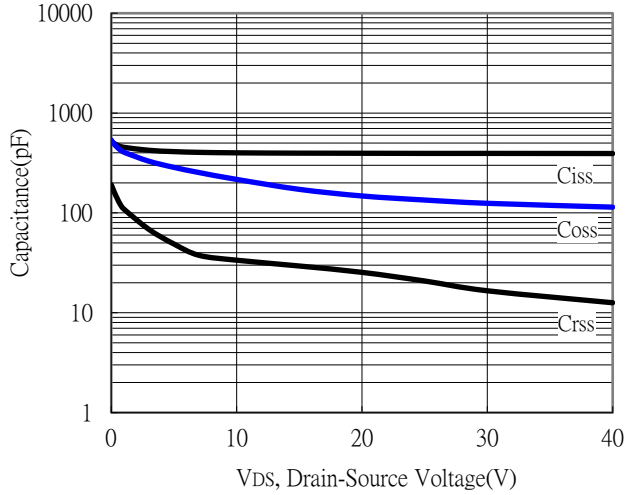


Drain-Source On-State Resistance vs Junction Temperature

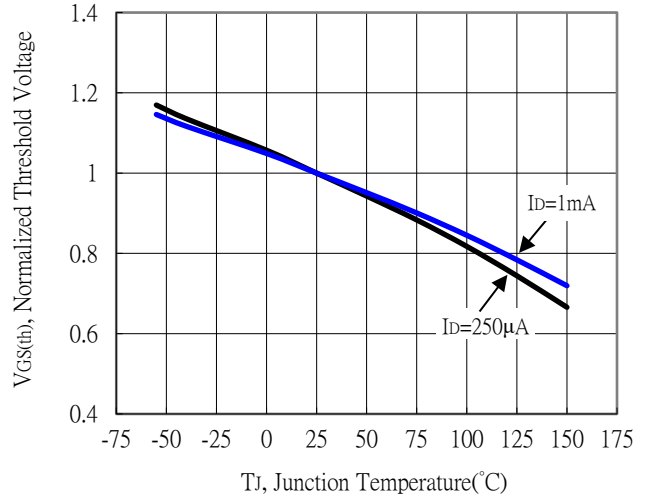


Typical Characteristics

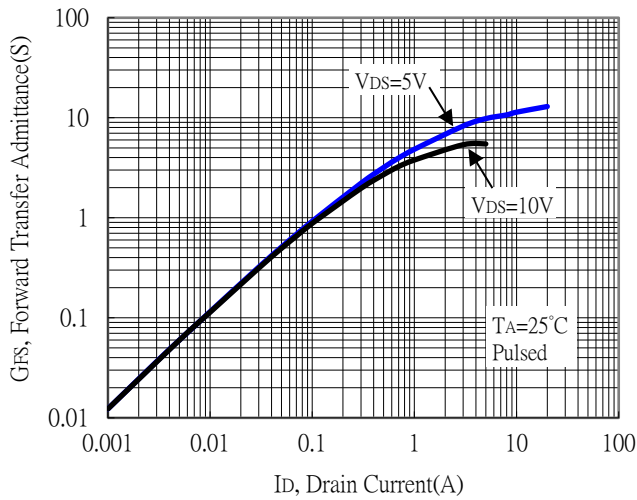
Capacitance vs Drain-to-Source Voltage



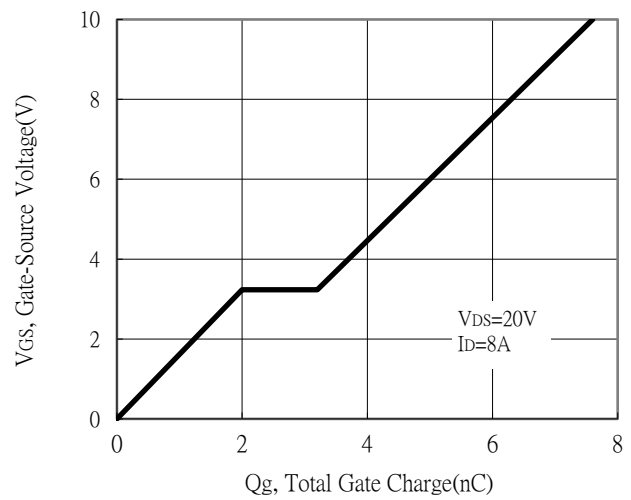
Threshold Voltage vs Junction Temperature



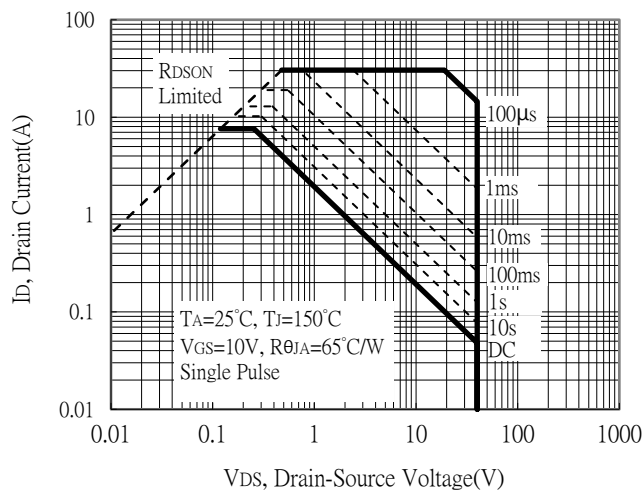
Forward Transfer Admittance vs Drain Current



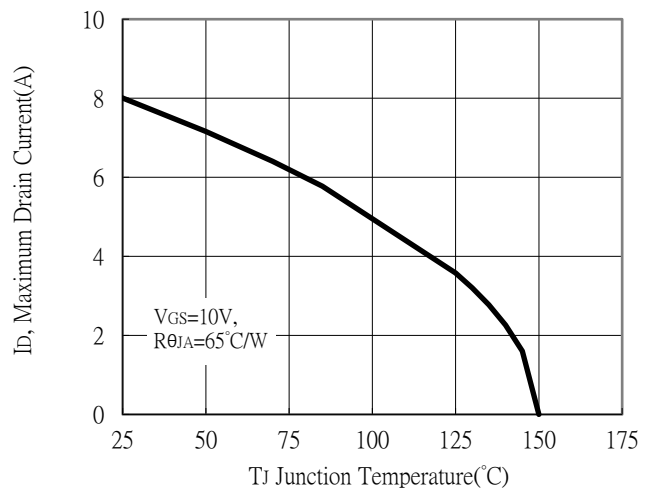
Gate Charge Characteristics



Maximum Safe Operating Area

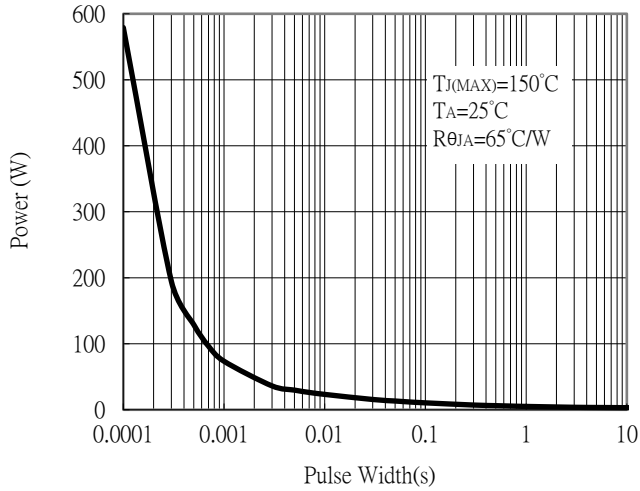


Maximum Drain Current vs Junction Temperature

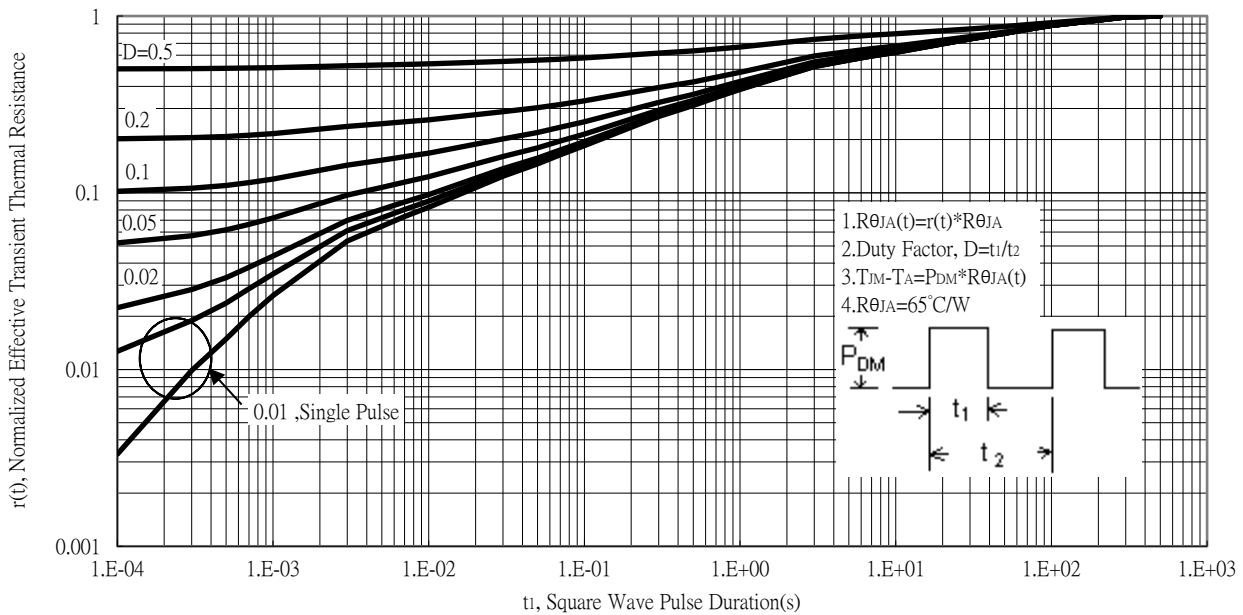


Typical Characteristics

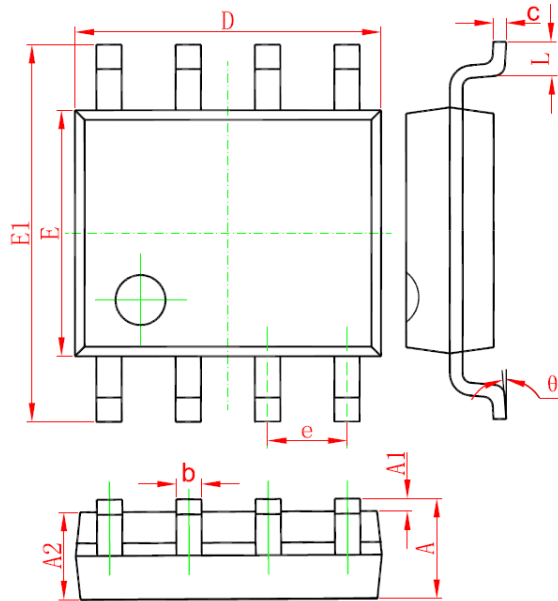
Single Pulse Power Rating, Junction to Ambient



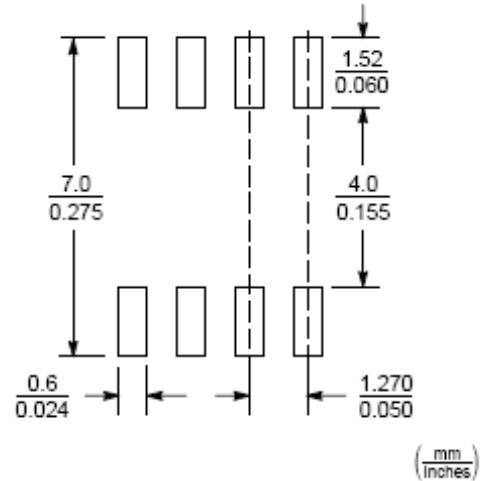
Transient Thermal Response Curves



SOP-8 Dimension



8-Lead SOP-8 Plastic Surface Mount Package
CYS Package Code: Q8



Recommended Soldering Footprint

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.053	0.069	1.350	1.750	E	0.150	0.157	3.800	4.000
A1	0.004	0.010	0.100	0.250	E1	0.228	0.244	5.800	6.200
A2	0.053	0.061	1.350	1.550	e	0.050	TYP.	1.1270	TYP.
b	0.013	0.020	0.330	0.510	L	0.016	0.050	0.400	1.270
c	0.006	0.010	0.170	0.250	θ	0°	8°	0°	8°
D	0.185	0.200	4.700	5.100					

Note:

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

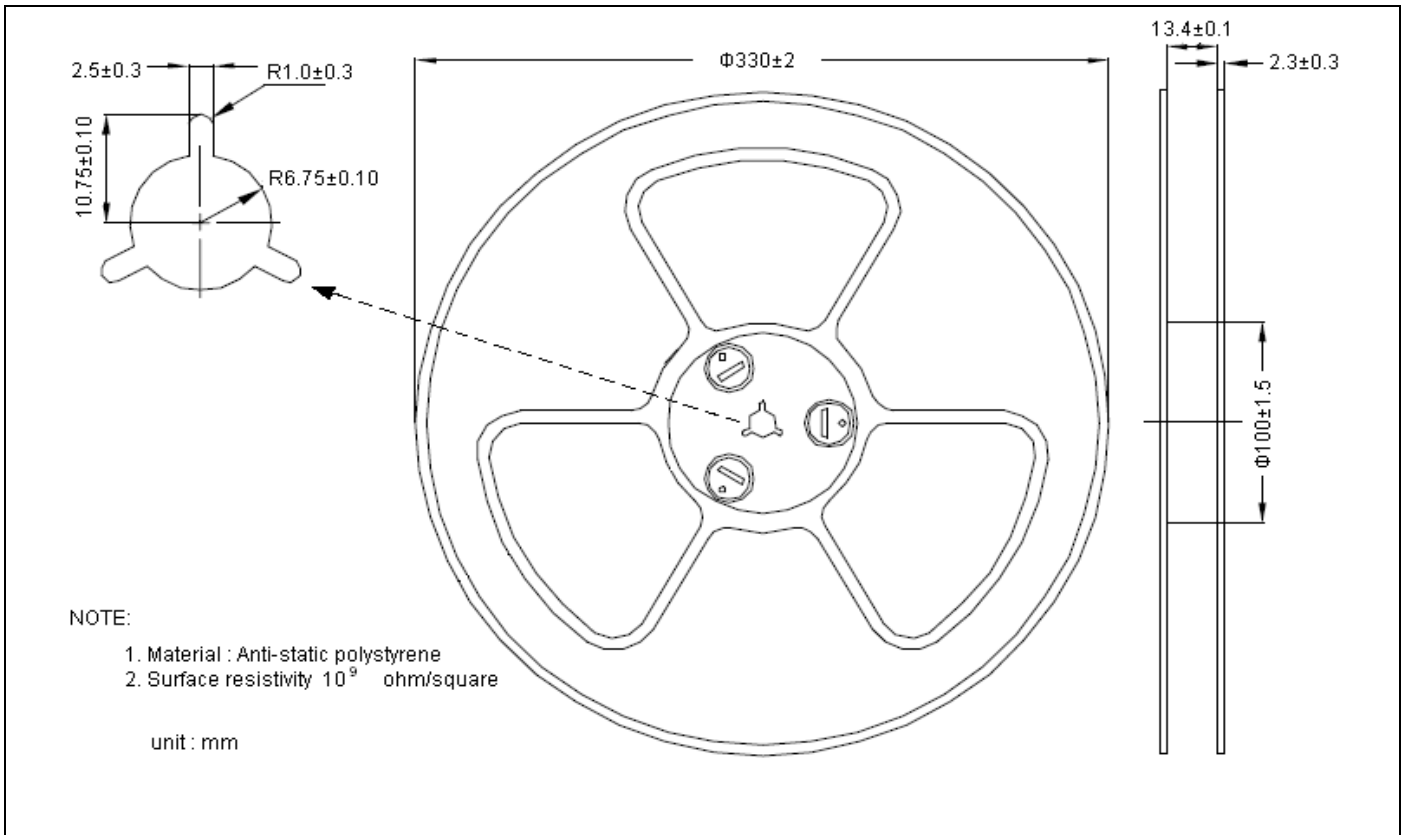
Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

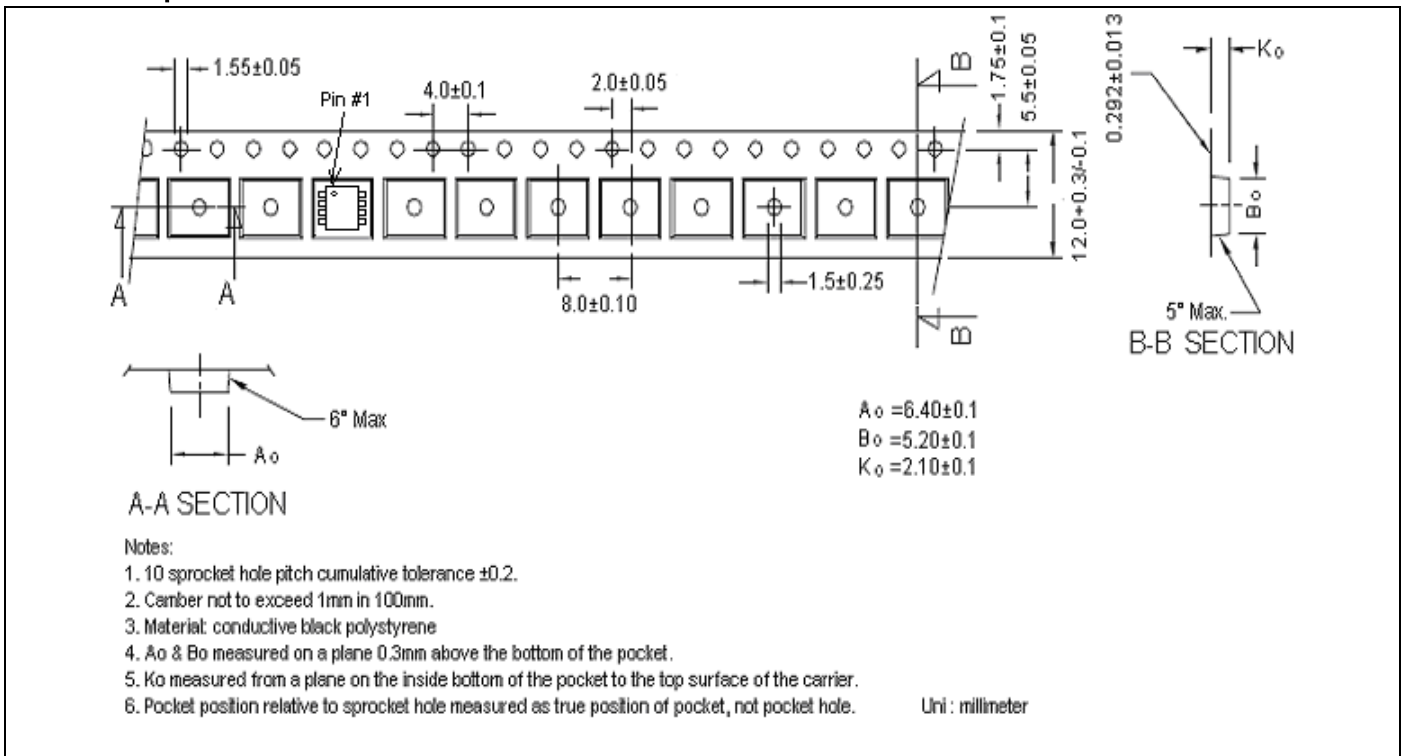
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Reel Dimension



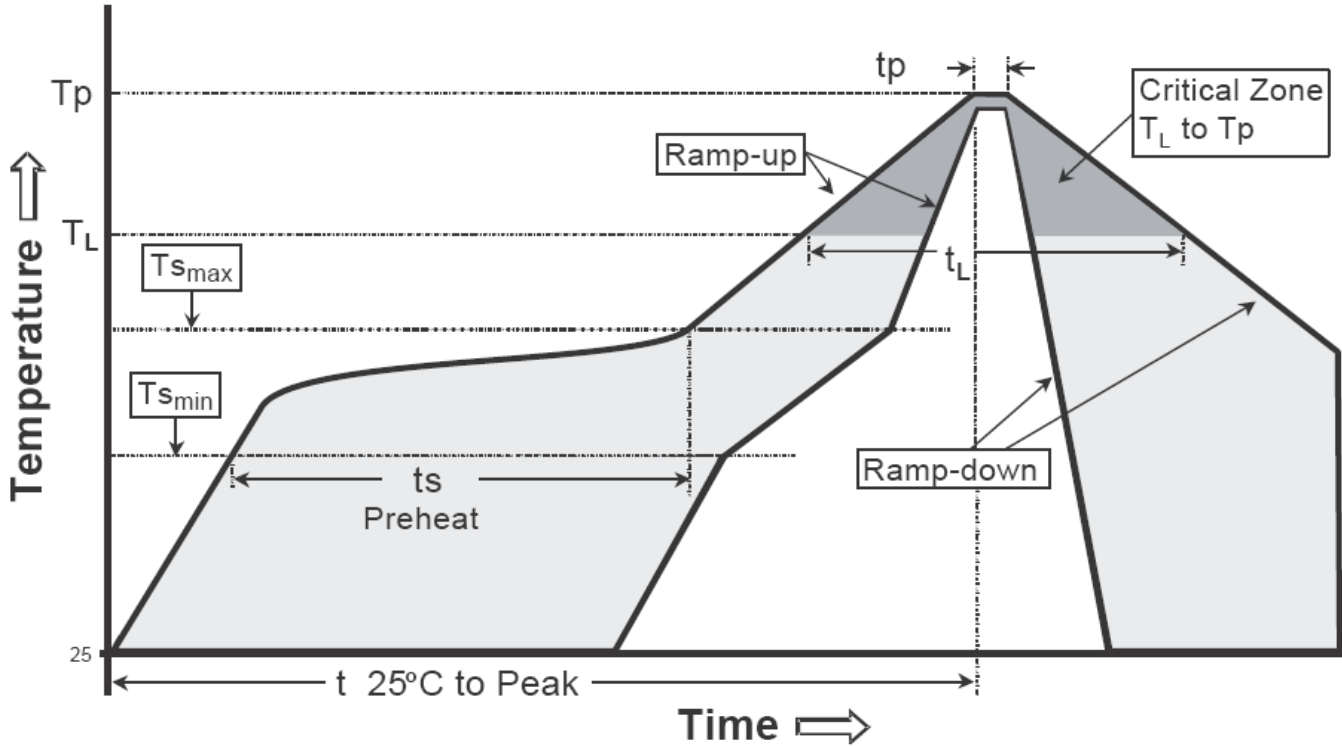
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _S max to T _P)	3°C/second max.	3°C/second max.
Preheat -Temperature Min (T _S min) -Temperature Max (T _S max) -Time (t _S min to t _S max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) -Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (t _P)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note:

- All temperatures refer to topside of the package, measured on the package body surface.