

Common Drain Dual N -Channel Enhancement Mode MOSFET

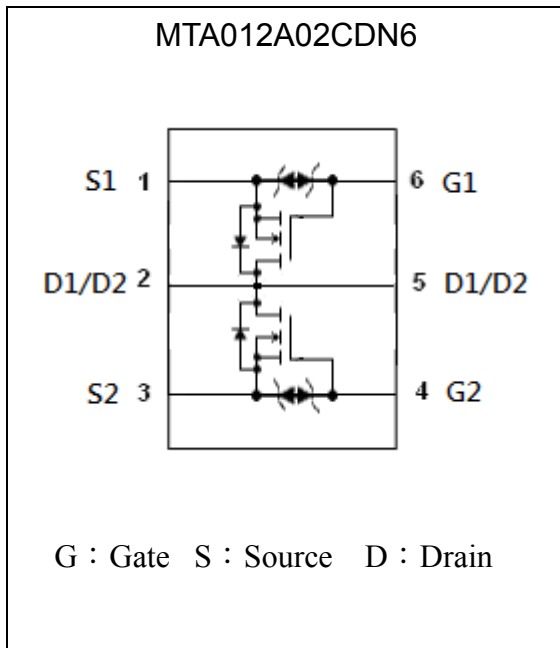
MTA012A02CDN6

BV_{DSS}		20V
I_D	$V_{GS}=4.5V, T_A=25^\circ C$	7A
$R_{DSON} (TYP.)$	$V_{GS}=4.5V, I_D=5A$	15.3 m Ω
	$V_{GS}=2.5V, I_D=2.6A$	18.5 m Ω
	$V_{GS}=1.8V, I_D=1A$	30.5 m Ω

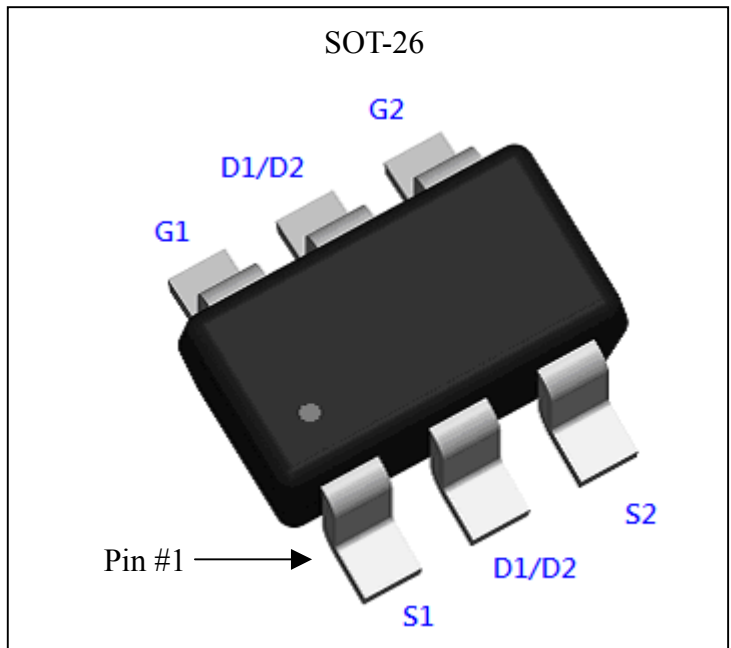
Features

- Simple drive requirement
- Low gate charge
- Low on-resistance
- Fast switching speed
- ESD protected gate
- Pb-free lead plating and halogen-free package

Equivalent Circuit

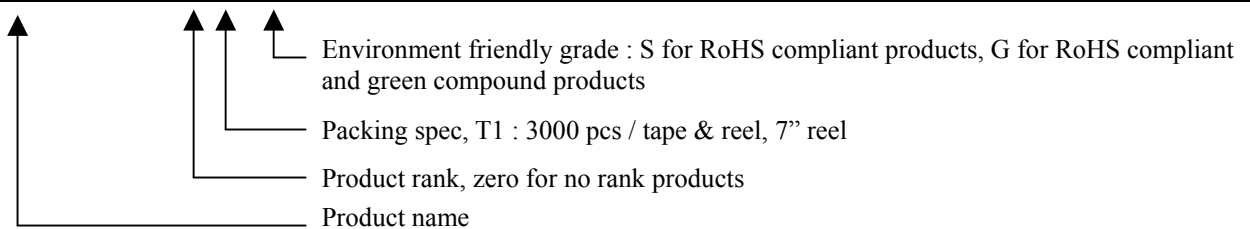


Outline



Ordering Information

Device	Package	Shipping
MTA012A02CDN6-0-T1-G	SOT-26 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C, unless otherwise specified)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±8	
Continuous Drain Current @ V _{GS} =4.5V, T _C =25°C	I _D	12.4	A
Continuous Drain Current @ V _{GS} =4.5V, T _C =100°C		7.8	
Continuous Drain Current @ V _{GS} =4.5V, T _A =25°C		7	
Continuous Drain Current @ V _{GS} =4.5V, T _A =70°C		5.6	
Pulsed Drain Current	I _{DM}	50 *1, 2	
Total Power Dissipation	P _D	T _A =25°C 2 *3	W
		T _A =70°C 1.3 *3	
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	20	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{θJA}	62.5 *3	

Note : 1. Pulse width limited by maximum junction temperature.
 2. Duty cycle≤1%.
 3. Surface mounted on a 1 in² pad of 2oz copper. In practice R_{th,j-a} will be determined by the customer's PCB characteristics.

Electrical Characteristics (Tj=25°C, unless otherwise specified)

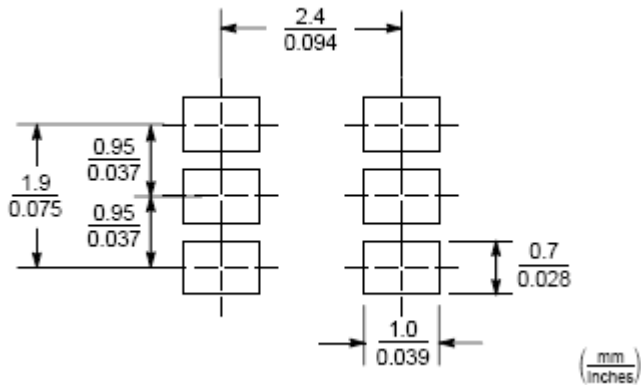
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	V _{GS} =0V, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.02	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	0.4	-	1.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±20	μA	V _{GS} =±8V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =20V, V _{GS} =0V
	-	-	10		V _{DS} =16V, V _{GS} =0V, T _j =70°C
*R _{DS(ON)}	-	15.3	22	mΩ	I _D =5A, V _{GS} =4.5V
	-	18.5	30		I _D =2.6A, V _{GS} =2.5V
	-	30.5	65		I _D =1A, V _{GS} =1.8V
*G _{FS}	-	4.8	-	S	V _{DS} =10V, I _D =1A
Dynamic					
C _{iss}	-	680	-	pF	V _{DS} =10V, V _{GS} =0V, f=1MHz
C _{oss}	-	186	-		
C _{rss}	-	31	-		



* $t_{d(ON)}$	-	82.4	-	ns	$V_{DS}=10V, I_D=5A, V_{GS}=4.5V, R_G=6\Omega$
* t_r	-	46.8	-		
* $t_{d(OFF)}$	-	319.4	-		
* t_f	-	888.8	-		
* Q_g	-	10.5	-	nC	$V_{DS}=16V, I_D=5A, V_{GS}=4.5V$
* Q_{gs}	-	1	-		
* Q_{gd}	-	3.1	-		
Source-Drain Diode					
* I_S	-	-	3	A	
* I_{SM}	-	-	5		
* V_{SD}	-	0.76	1.2	V	$I_S=1.2A, V_{GS}=0V$

*Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

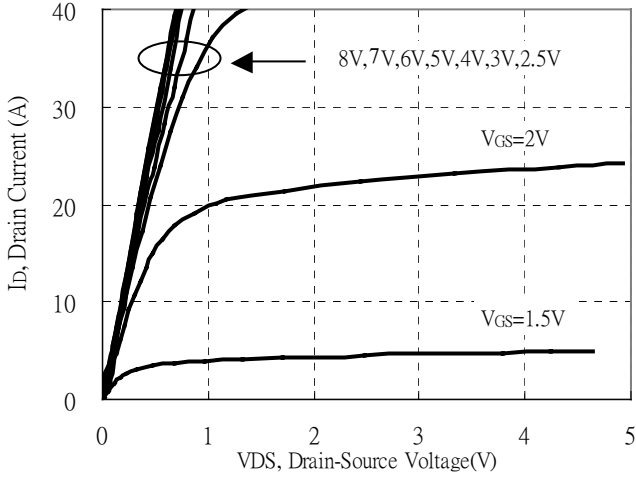
Recommended Soldering Footprint



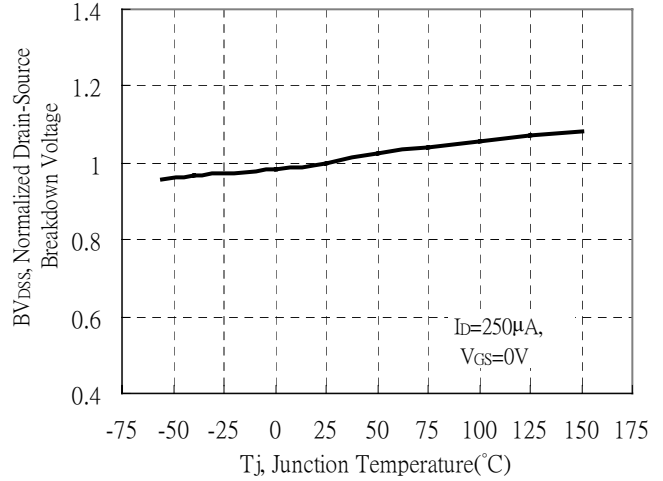


Typical Characteristics

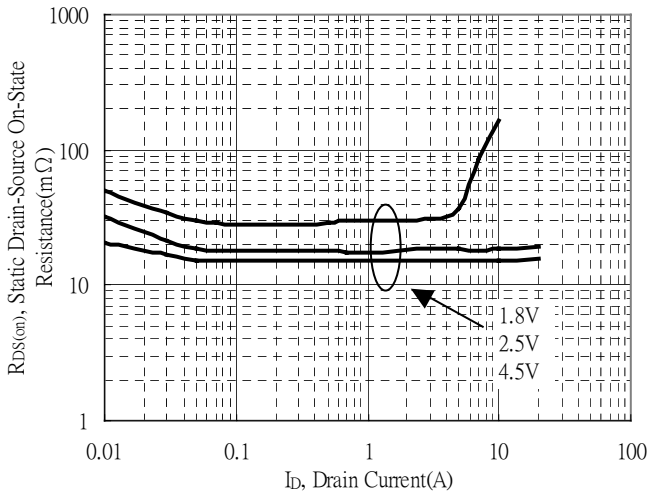
Typical Output Characteristics



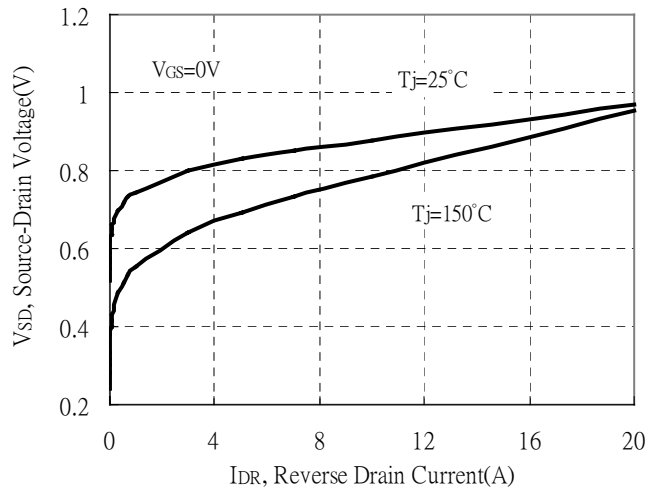
Brekdown Voltage vs Junction Temperature



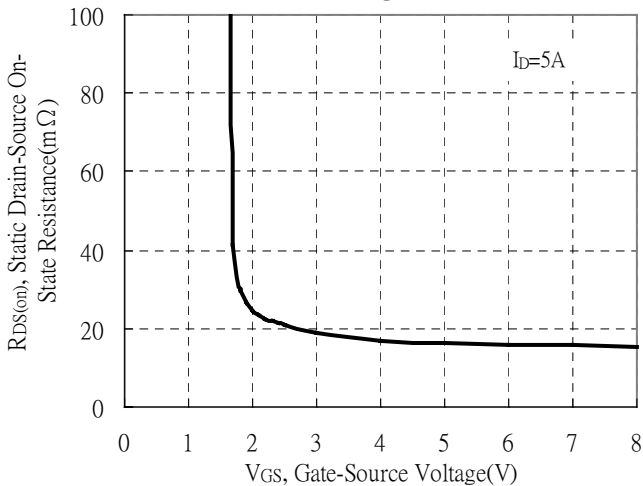
Static Drain-Source On-State resistance vs Drain Current



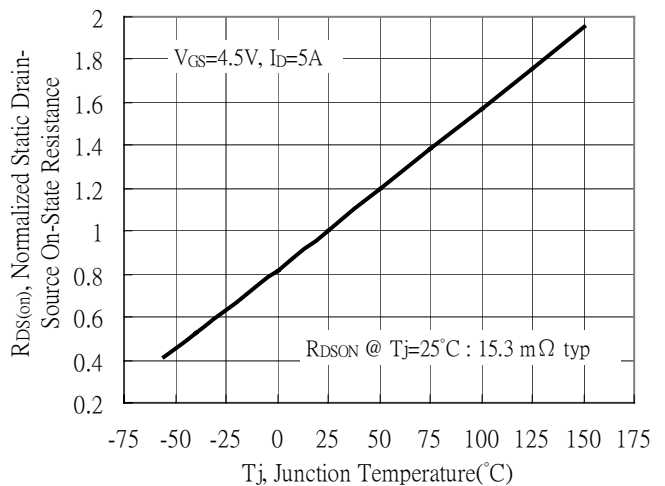
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

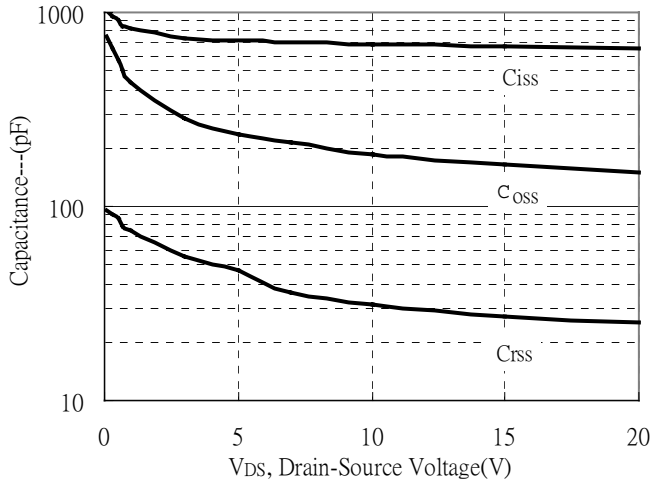


Drain-Source On-State Resistance vs Junction Temperature

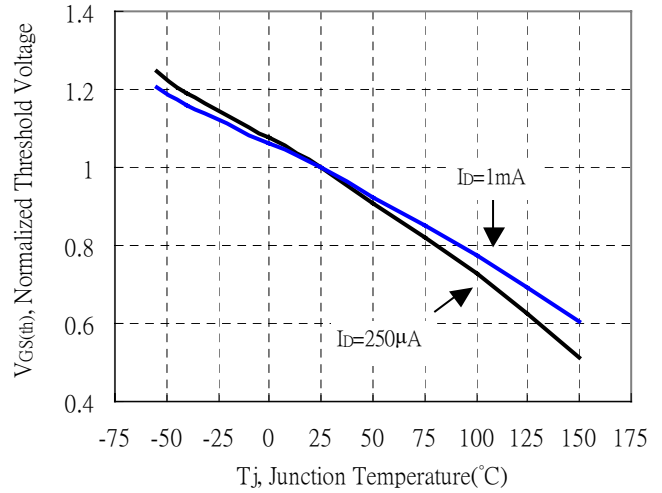


Typical Characteristics(Cont.)

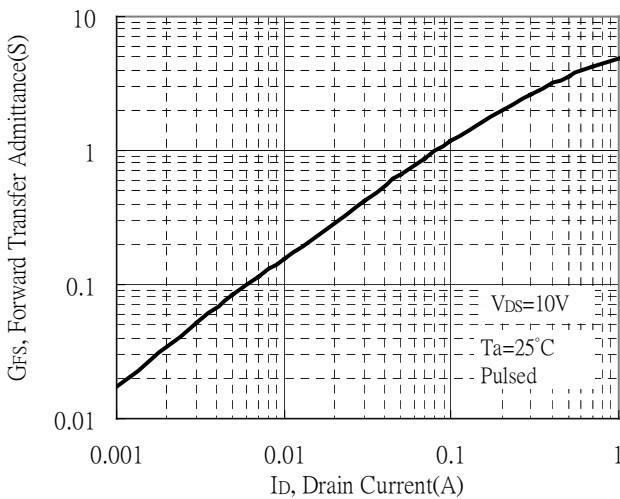
Capacitance vs Drain-to-Source Voltage



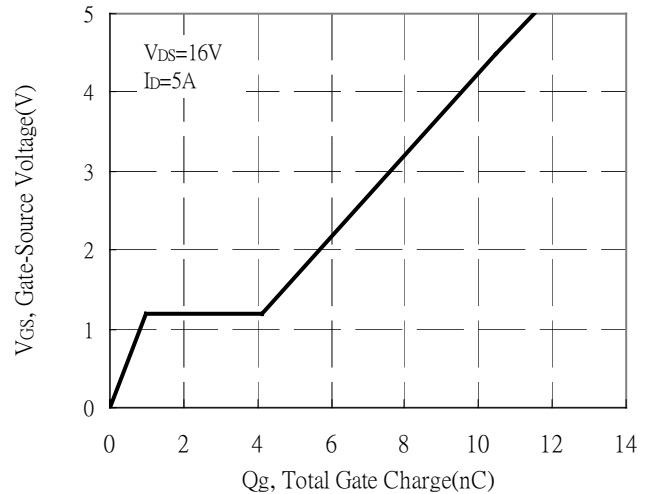
Threshold Voltage vs Junction Temperature



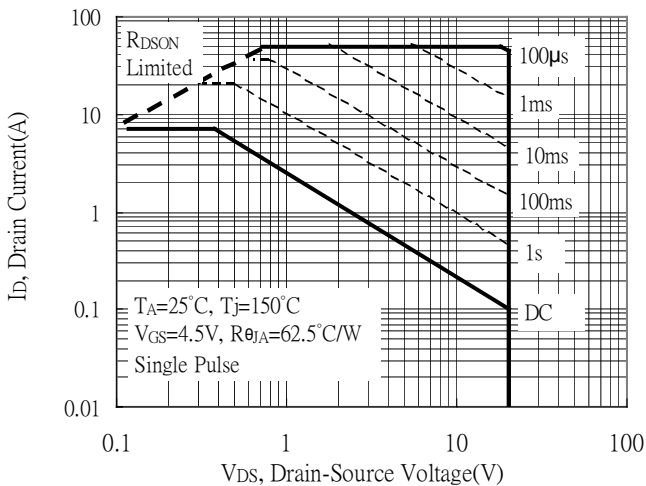
Forward Transfer Admittance vs Drain Current



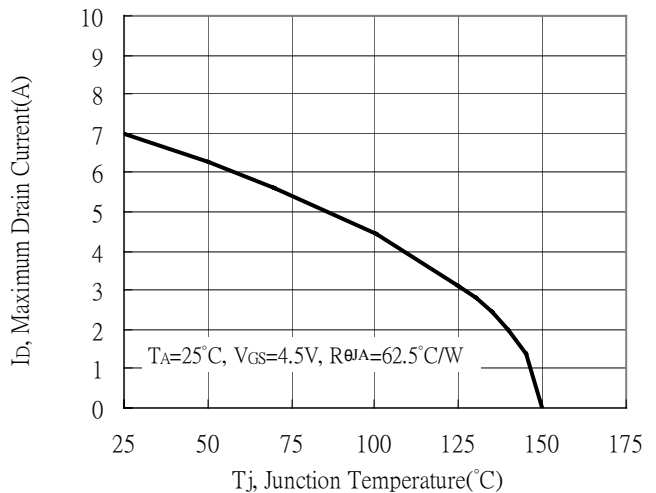
Gate Charge Characteristics



Maximum Safe Operating Area



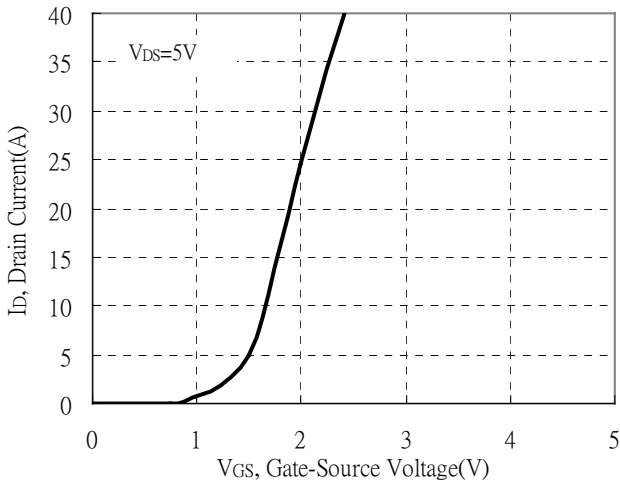
Maximum Drain Current vs Junction Temperature



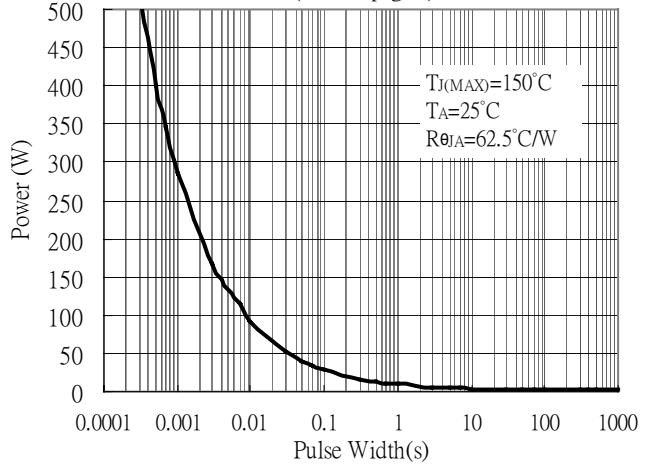


Typical Characteristics(Cont.)

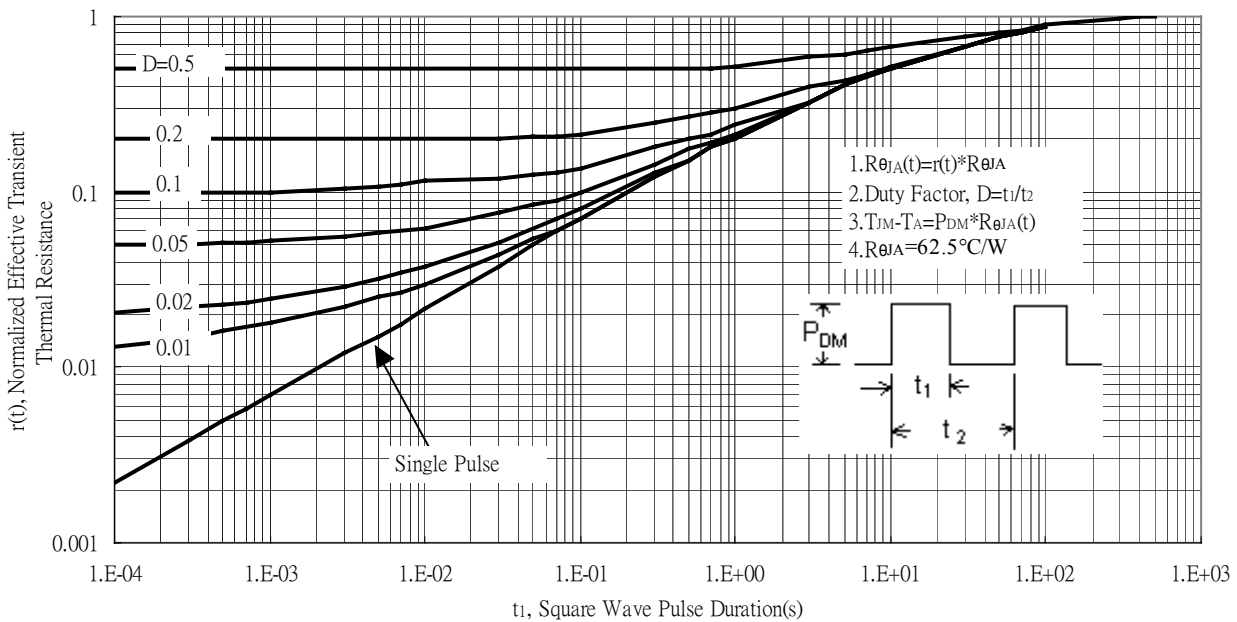
Typical Transfer Characteristics



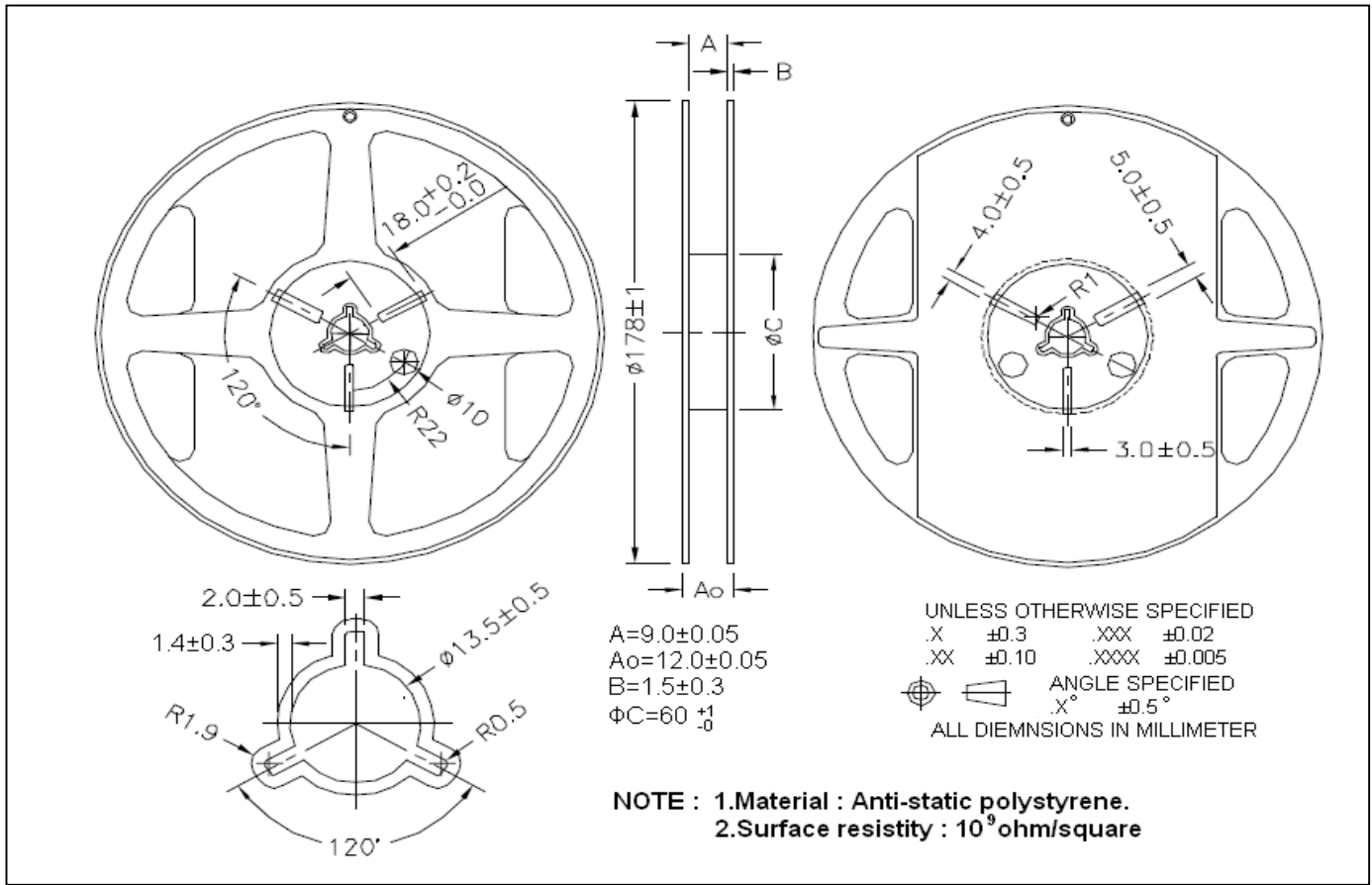
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



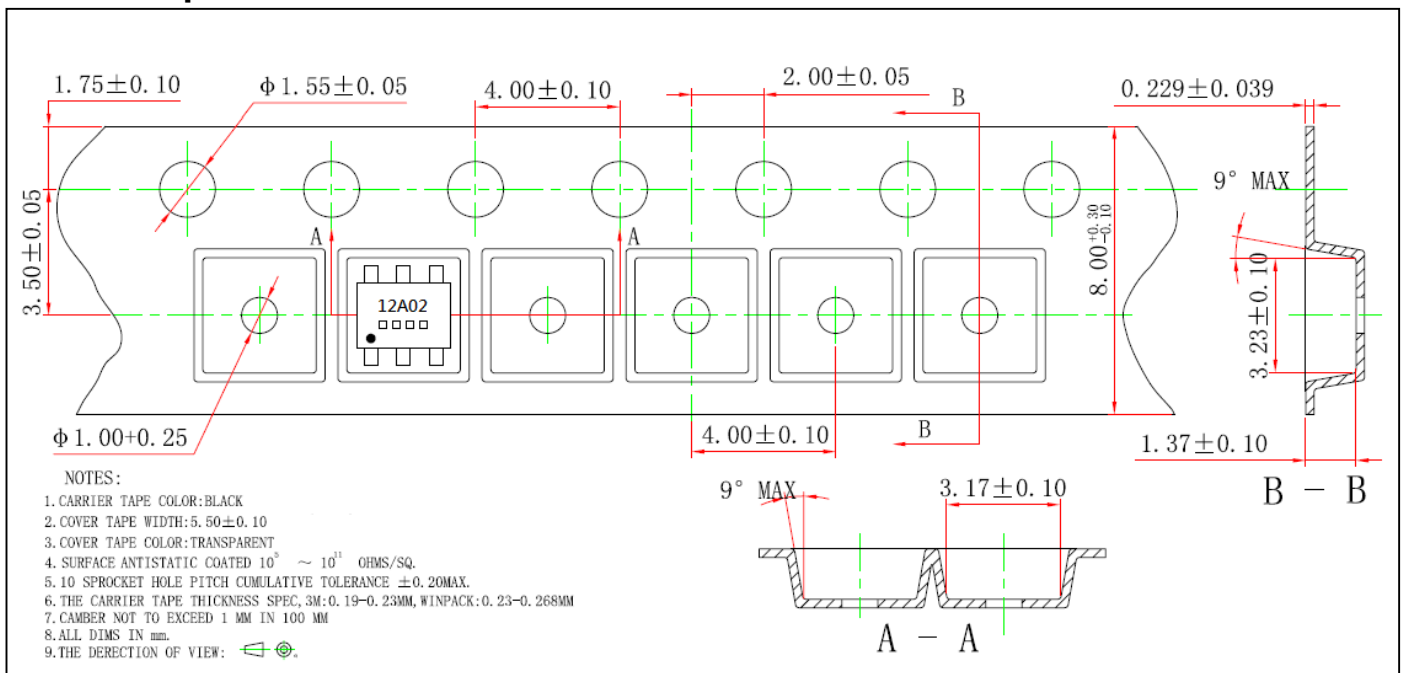
Transient Thermal Response Curves



Reel Dimension



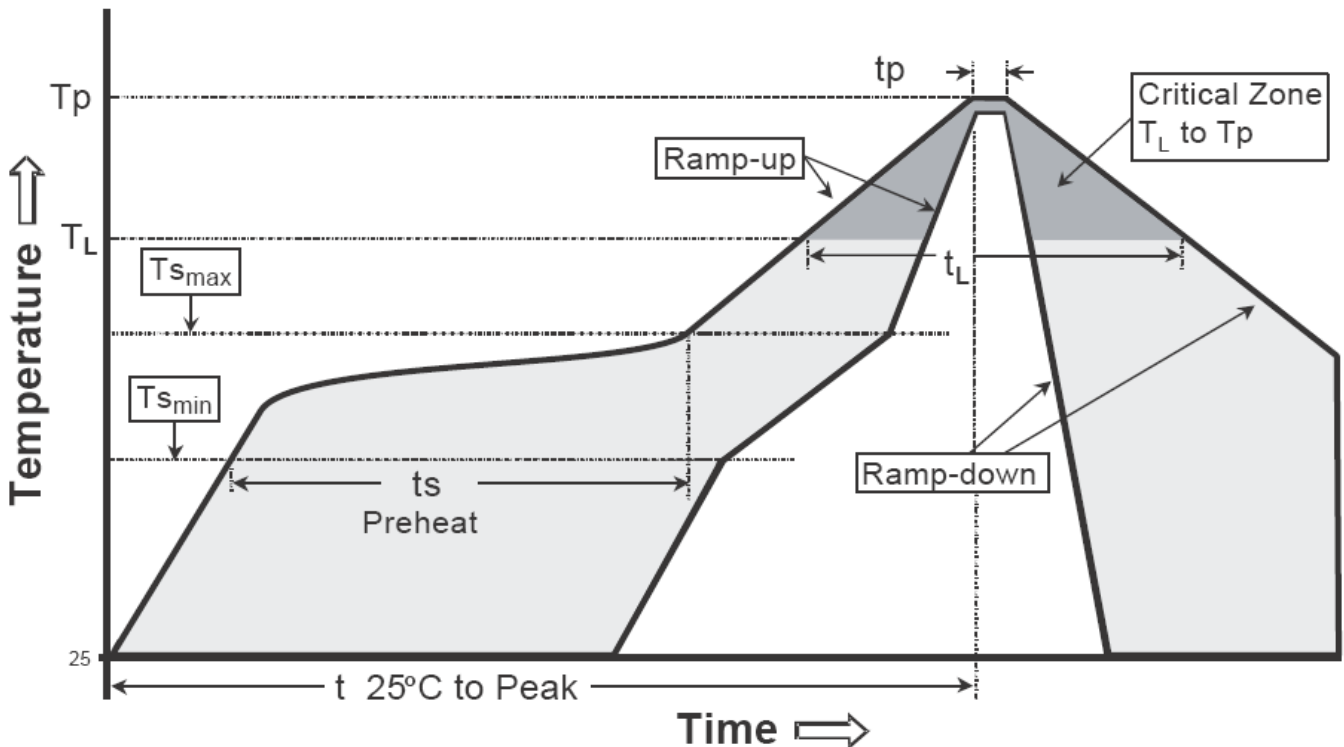
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

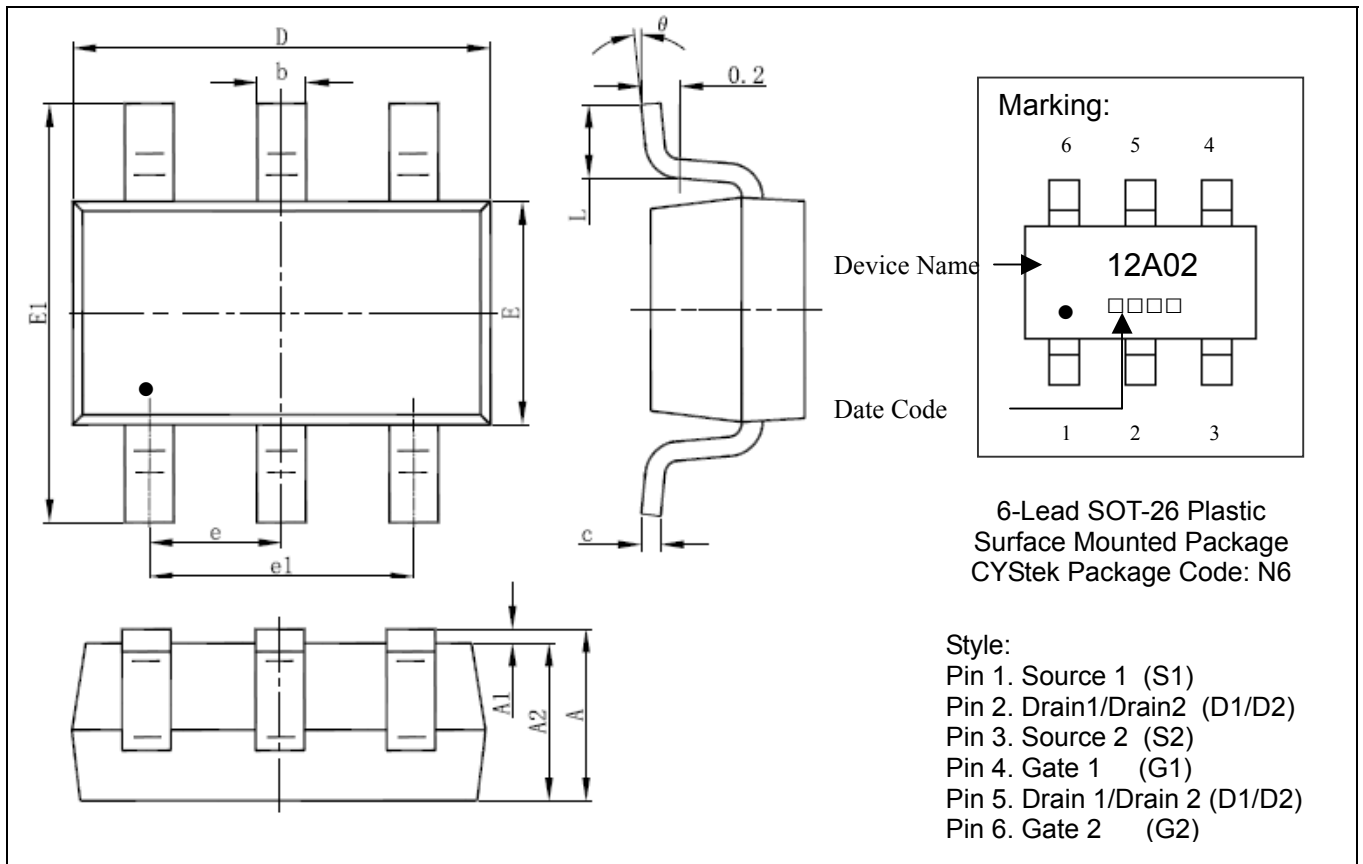
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-26 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049	E	1.500	1.700	0.059	0.067
A1	0.000	0.100	0.000	0.004	E1	2.650	2.950	0.104	0.116
A2	1.050	1.150	0.041	0.045	e	0.950 (BSC)		0.037 (BSC)	
b	0.300	0.500	0.012	0.020	e1	1.800	2.000	0.071	0.079
c	0.100	0.200	0.004	0.008	L	0.300	0.600	0.012	0.024
D	2.820	3.020	0.111	0.119	θ	0°	8°	0°	8°

Notes : 1.Controlling dimension : millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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