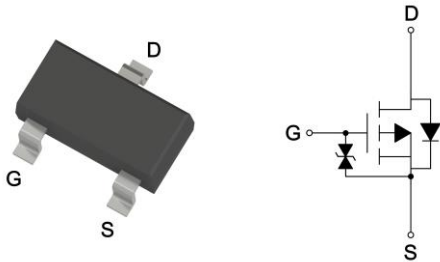


## Product Summary

$BV_{DSS}$	-8	V
$R_{DS(ON)}$ typ. @ $V_{GS}=-4.5V, I_D=-4A$	12	mΩ
$R_{DS(ON)}$ typ. @ $V_{GS}=-2.5V, I_D=-2A$	15	
$R_{DS(ON)}$ typ. @ $V_{GS}=-1.8V, I_D=-1A$	20	
$I_D$ @ $V_{GS}=-4.5V, T_A=25^\circ C$	-5	A

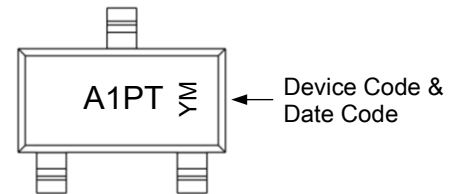
## SOT-23



## Features

- Low On Resistance
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free
- ESD protected gate, typical 5kV (HBM)

## Marking



YM: Date Code Marking

Y: Year Code, the last digit of Christian year

M: Month Code

1: Jan	2: Feb	3: Mar	4: Apr	5: May	6: Jun
7: Jul	8: Aug	9: Sep	A: Oct	B: Nov	C: Dec

## Ordering Information

Device	Package	Shipping
MTA010P01KN3-0-T1-G	SOT-23	3000pcs / Tape & Reel

0: Product rank, zero for no rank products.

T1: Packing spec, T1 : 3000pcs / tape & reel, 7" reel

G: Environment friendly grade: S for RoHS compliant products, G for RoHS compliant and green compound products.

## Absolute Maximum Ratings ( $T_A=25^\circ C$ )

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DS}$	-8	V	
Gate-Source Voltage	$V_{GS}$	$\pm 8$		
Continuous Drain Current @ $V_{GS}=-4.5V, T_A=25^\circ C$	$I_D$	-5	A	
Continuous Drain Current @ $V_{GS}=-4.5V, T_A=70^\circ C$		-4		
Pulsed Drain Current	$I_{DM}$	-20		
Continuous Body Diode Forward Current @ $T_A=25^\circ C$	$I_S$	-0.5		
Pulsed Body Diode Forward Current @ $T_A=25^\circ C$	$I_{SM}$	-2		
Total Power Dissipation	$P_D$	$T_A=25^\circ C$		0.7
		$T_A=70^\circ C$	0.4	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150	$^\circ C$	
Steady State Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	178	$^\circ C/W$	

Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ , unless otherwise specified)

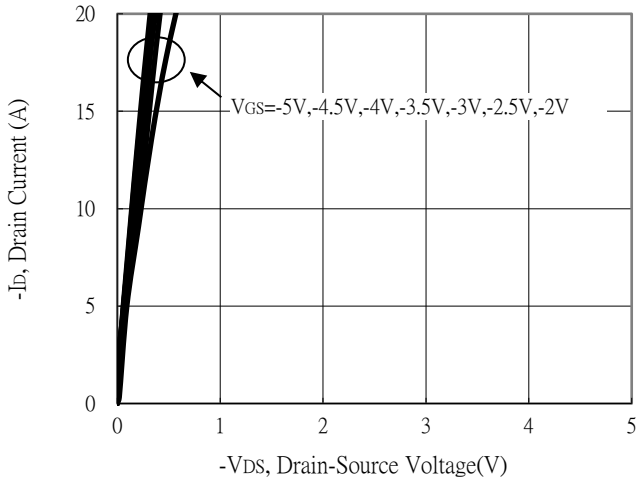
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
$BV_{DSS}$	-8	-	-	V	$V_{GS}=0V, I_D=-250\mu A$
$V_{GS(th)}$	-0.3	-	-1		$V_{DS}=V_{GS}, I_D=-250\mu A$
$G_{FS}$	-	15	-	S	$V_{DS}=-5V, I_D=-4A$
$I_{GSS}$	-	-	$\pm 10$	$\mu A$	$V_{GS}=\pm 8V, V_{DS}=0V$
$I_{DSS}$	-	-	-1		$V_{DS}=-6V, V_{GS}=0V$
$R_{DS(ON)}$	-	12	17	m $\Omega$	$V_{GS}=-4.5V, I_D=-4A$
	-	15	23		$V_{GS}=-2.5V, I_D=-2A$
	-	20	36		$V_{GS}=-1.8V, I_D=-1A$
<b>Source-Drain Diode</b>					
$V_{SD}$ *c	-	-0.81	-1.2	V	$I_S=-4A, V_{GS}=0V$

## Note:

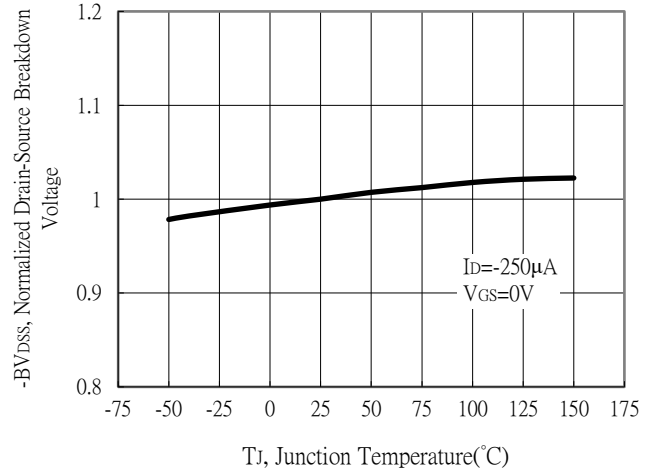
- \*a. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The power dissipation  $P_D$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design.
- \*b. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^{\circ}\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
- \*c. Pulse Test : Pulse Width $\leq 300\mu s$ , Duty Cycle $\leq 2\%$ .
- \*d. Independent of operating temperature.

## Typical Characteristics

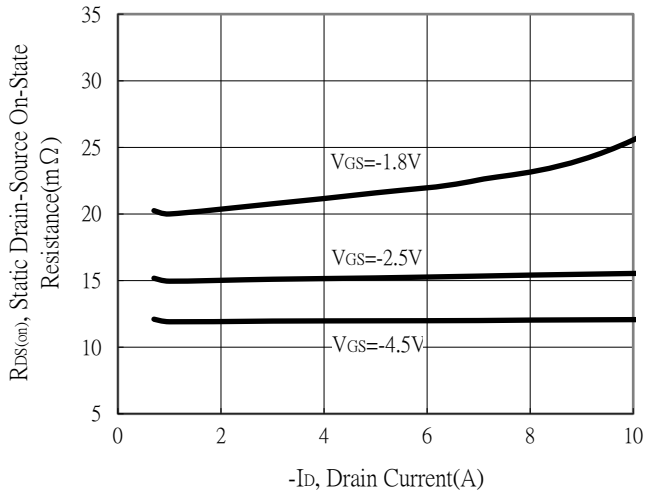
Typical Output Characteristics



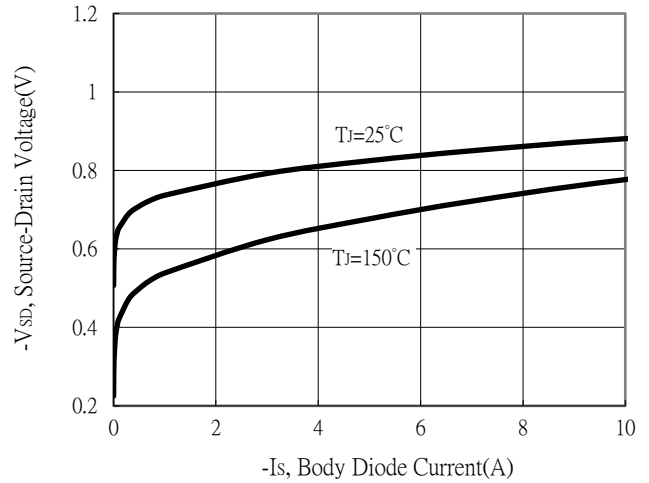
Breakdown Voltage vs Ambient Temperature



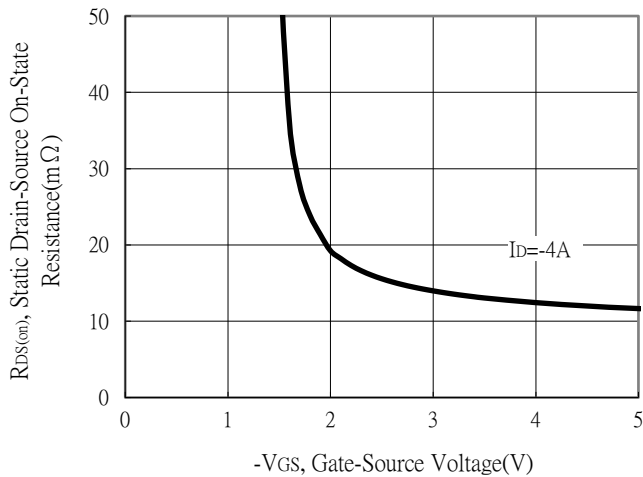
Static Drain-Source On-State resistance vs Drain Current



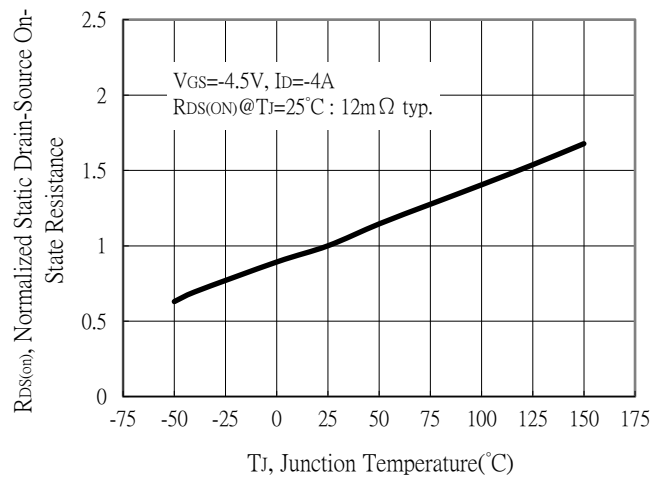
Body Diode Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

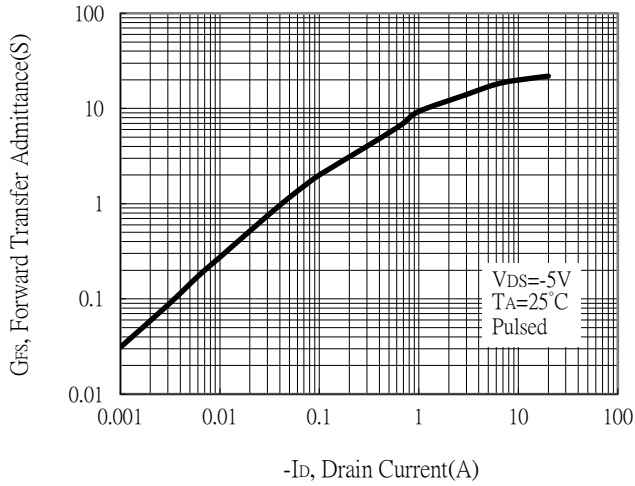


Drain-Source On-State Resistance vs Junction Temperature

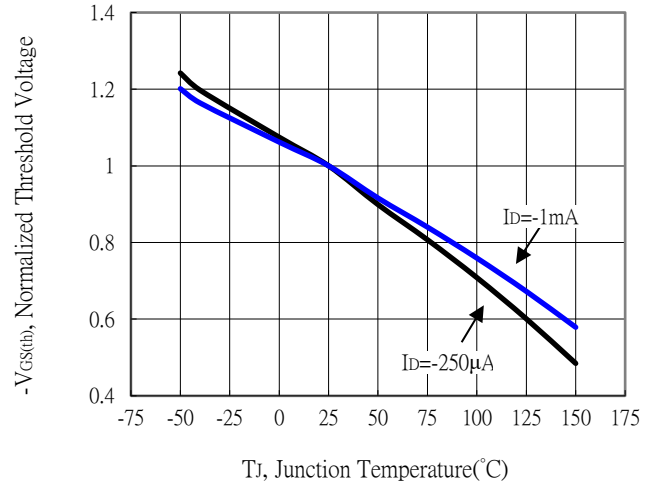


## Typical Characteristics

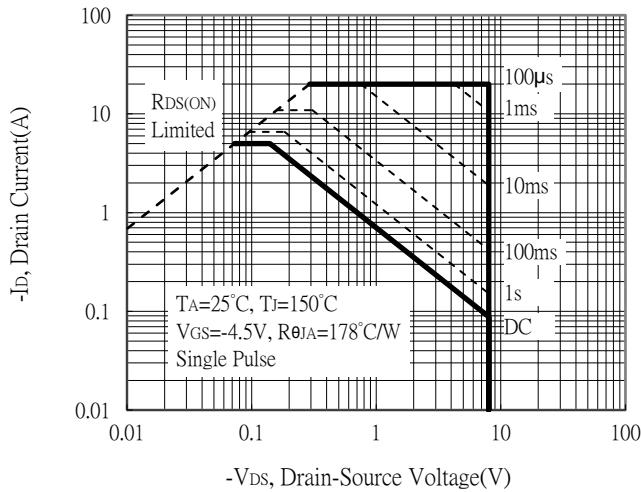
Forward Transfer Admittance vs Drain Current



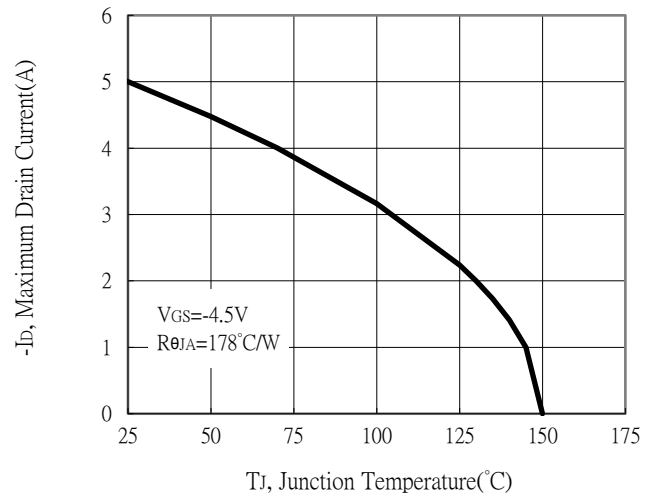
Threshold Voltage vs Junction Temperature



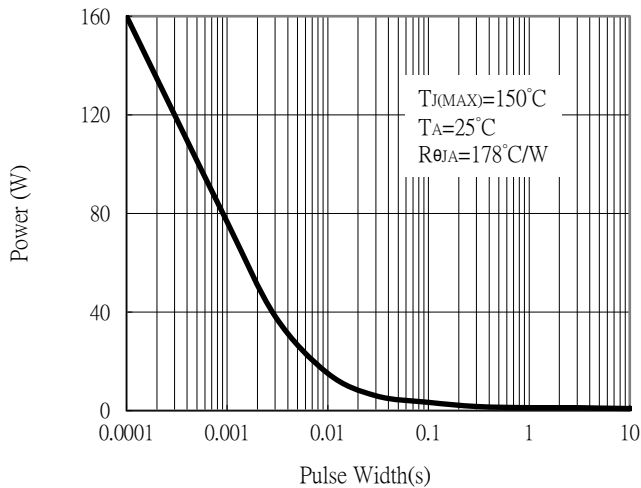
Maximum Safe Operating Area



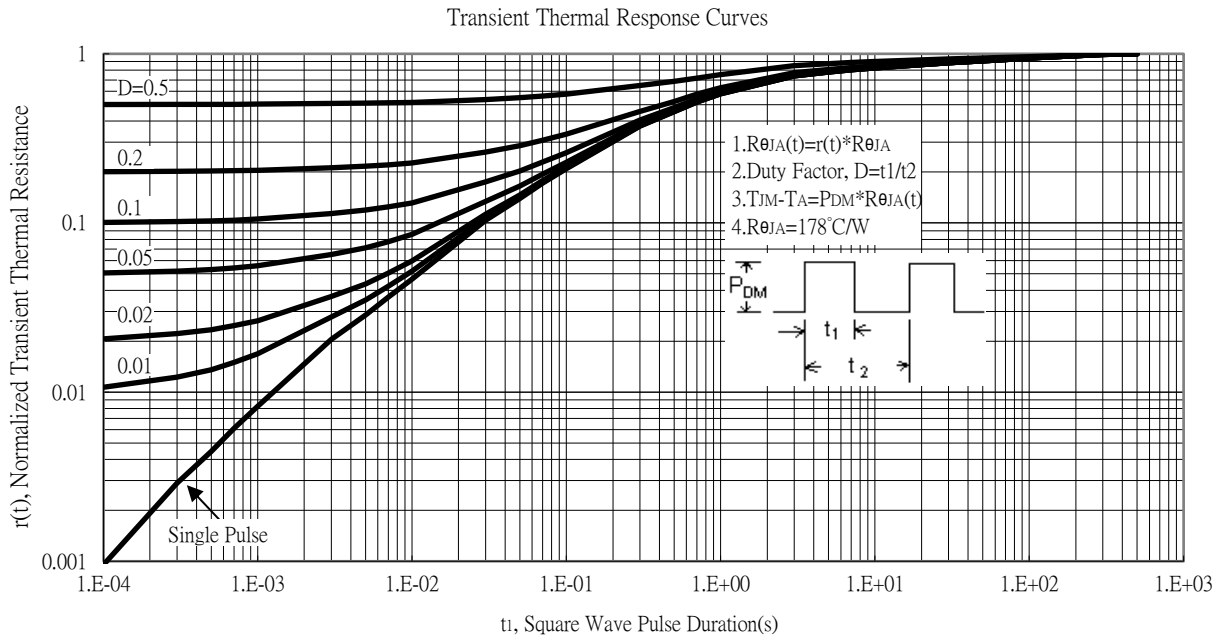
Maximum Drain Current vs Junction Temperature



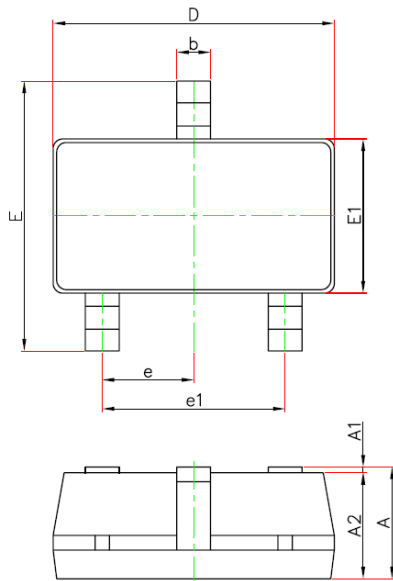
Single Pulse Power Rating, Junction to Ambient



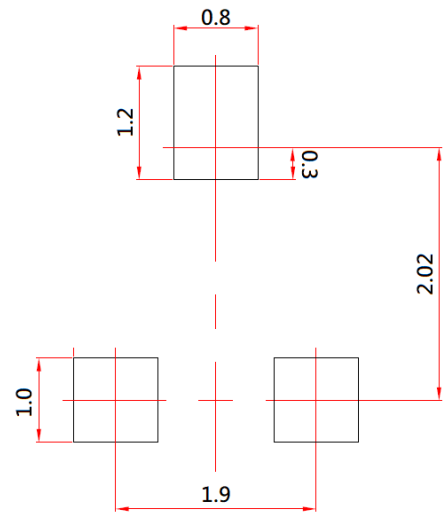
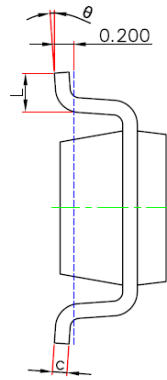
## Typical Characteristics



## SOT-23 Dimension



3-Lead SOT-23 Plastic Surface Mount Package  
CYS Package Code: N3



Unit : mm

Recommended Soldering Footprint

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.041	0.049	1.05	1.25	E1	0.059	0.067	1.50	1.70
A1	0.000	0.004	0.00	0.10	E	0.104	0.116	2.65	2.95
A2	0.041	0.045	1.05	1.15	e	0.037	BSC	0.95	BSC
b	0.012	0.020	0.30	0.50	e1	0.071	0.079	1.80	2.00
c	0.004	0.008	0.10	0.20	L	0.012	0.024	0.30	0.60
D	0.111	0.119	2.82	3.02	$\theta$	0°	8°	0°	8°

### Note:

- Controlling dimension: millimeters.
- Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
- If there is any question with packing specification or packing method, please contact your local CYStek sales office.

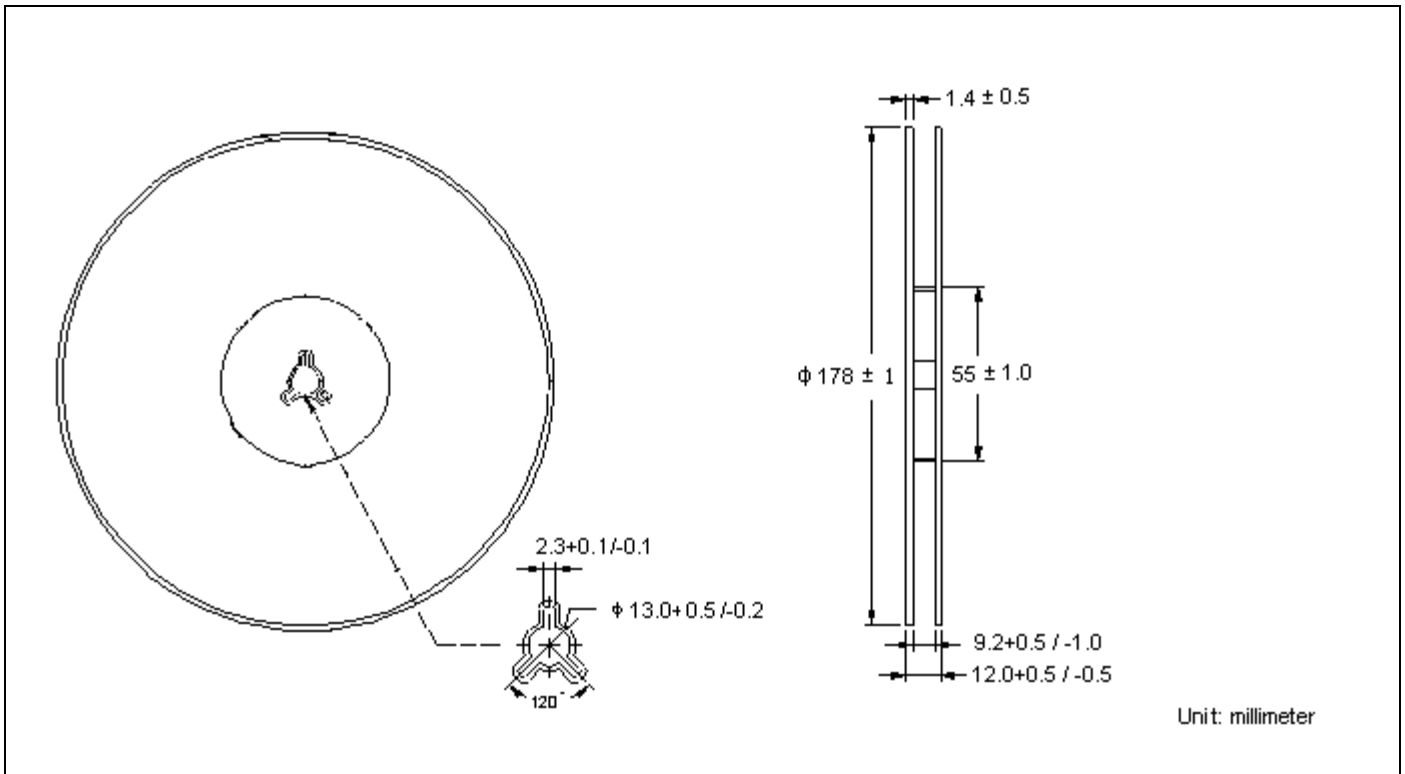
### Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

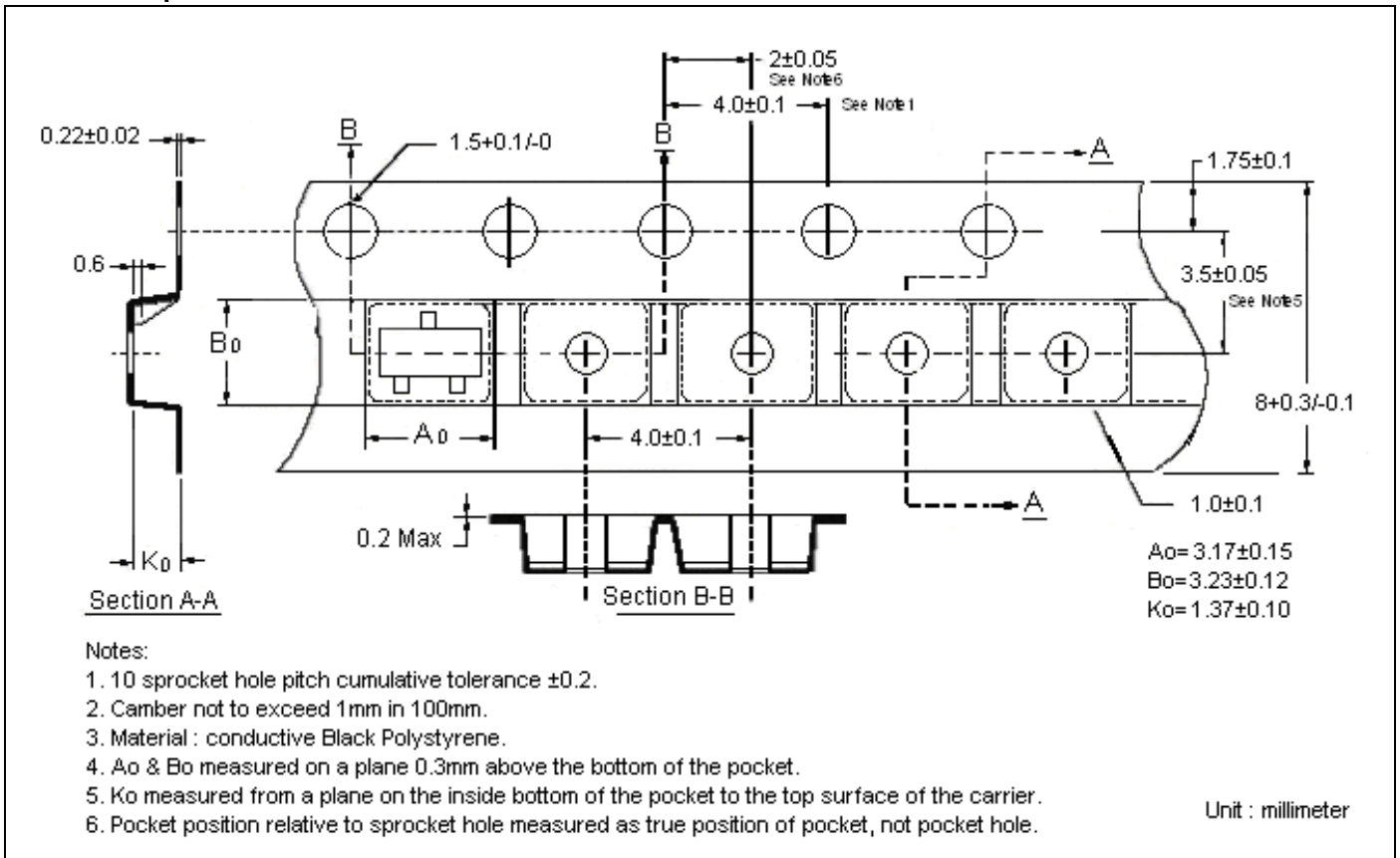
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## Reel Dimension



## Carrier Tape Dimension



**Notes:**

1. 10 sprocket hole pitch cumulative tolerance  $\pm 0.2$ .
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4.  $A_0$  &  $B_0$  measured on a plane 0.3mm above the bottom of the pocket.
5.  $K_0$  measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

### Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

### Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (TS max to TP)	3°C/second max.	3°C/second max.
Preheat -Temperature Min (TS min) -Temperature Max (TS max) -Time (ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (TL) -Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature (tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

**Note:**

- All temperatures refer to topside of the package, measured on the package body surface.