

# 2A / 800V Switch Control Diode

# DISD0280BF

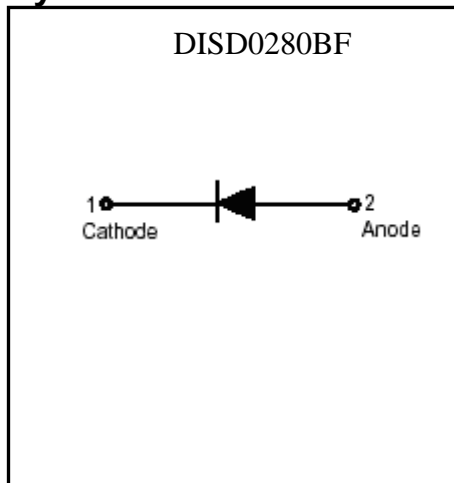
## Features

- High current capability
- Smoothly soft reverse recovery time (trr)
- Low profile surface mounted package in order to minimize board space
- Pb-free lead plating and halogen-free package

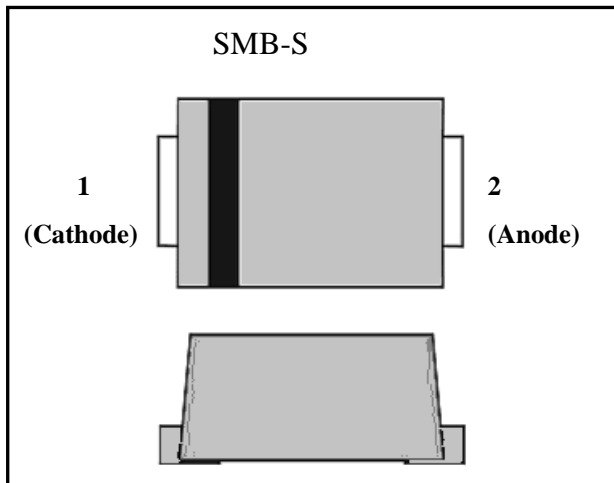
## Mechanical data

- Case : Molded plastic, SMB-S/JEDEC DO-214AA
- Epoxy : UL94-V0 rated flame retardant
- Terminals : Plated terminals, solderable per MIL-STD-750 method 2026
- Polarity : Indicated by cathode band
- Mounting position : Any
- Weight: 0.072 gram

## Symbol

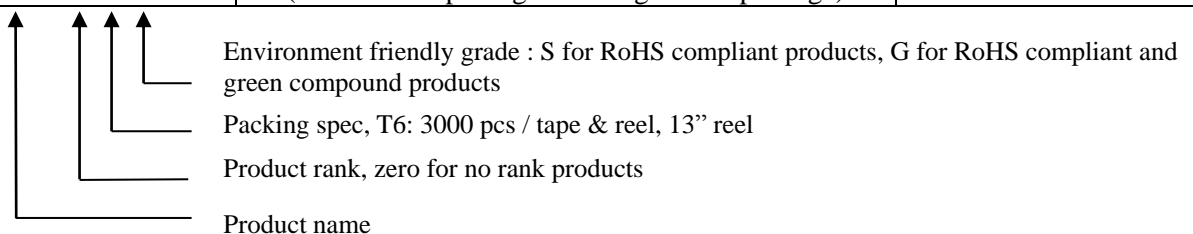


## Outline



## Ordering Information

Device	Package	Shipping
DISD0280BF-0-T6-G	SMB-S (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





**Absolute Maximum Ratings** ( $T_A=25^{\circ}\text{C}$ , unless otherwise noted)

Parameters	Conditions	Symbol	Value	Units
Repetitive peak reverse voltage		$V_{RRM}$	800	V
RMS voltage		$V_{RMS}$	560	V
Continuous reverse voltage		$V_R$	800	V
Forward rectified current	Single phase half wave, 60Hz @ $T_J=25^{\circ}\text{C}$	$I_{F(AV)}$	2	A
Repetitive Peak Forward Current	Single phase half wave, 60Hz @ $T_J=25^{\circ}\text{C}$	$I_{FRM}$	3.14	A
Forward surge current	8.3ms single half sine-wave superimposed on rated load (JEDEC method)	$I_{FSM}$	14	A
Maximum reverse recovery time	$I_F=1\text{A}$ , $dI_F/dt=100\text{A}/\mu\text{s}$	$t_{rr}$	3	$\mu\text{s}$
Power Dissipation	$T_A=25^{\circ}\text{C}$ (Note )	$P_D$	1.9	W
	$T_A=70^{\circ}\text{C}$ (Note )		1.2	
	$T_C=25^{\circ}\text{C}$		9.6	
	$T_C=100^{\circ}\text{C}$		3.8	
Storage temperature range		$T_{stg}$	-55~+150	$^{\circ}\text{C}$
Operating junction temperature range		$T_j$	-55~+150	$^{\circ}\text{C}$

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	13	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note )	$R_{th,j-a}$	68	

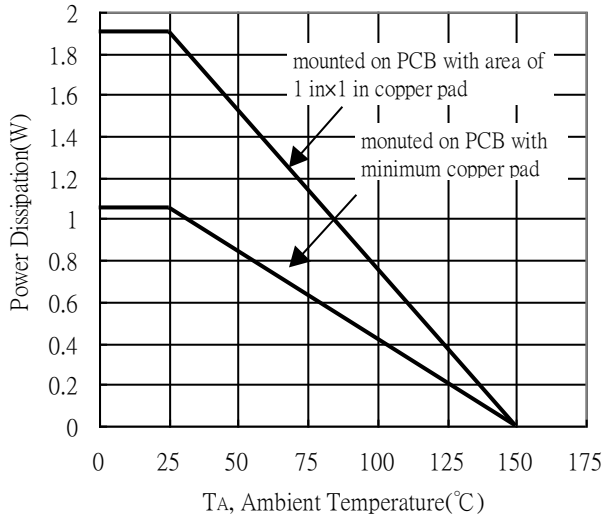
Note : Mounted on PCB with 1inch x 1inch copper pad area.

**Characteristics** ( $T_A=25^{\circ}\text{C}$ , unless otherwise noted)

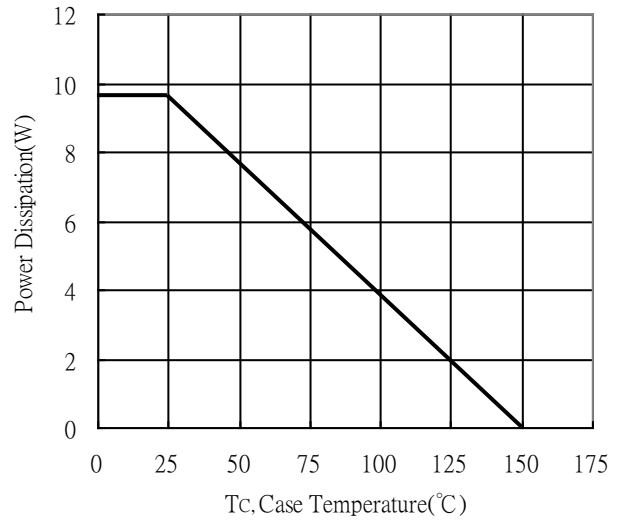
Characteristic	Symbol	Condition	Min.	Typ	Max.	Unit
Continuous reverse voltage	$V_R$	$I_R=100\mu\text{A}$	800	-	-	V
Forward Voltage	$V_F 1$	$I_F=500\text{mA}$	-	-	1.1	V
	$V_F 2$	$I_F=1\text{A}$	-	-	1.3	
Reverse Leakage Current	$I_R$	$V_R=800\text{V}$	-	-	10	$\mu\text{A}$
Junction Capacitance	$C_J$	$V_R=10\text{V}$ , $f=1\text{MHz}$	-	4.2	-	pF

## Typical Characteristics

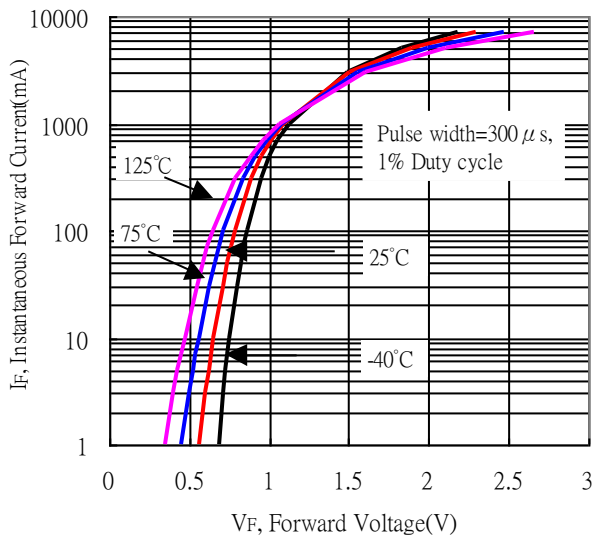
Power Derating Curve



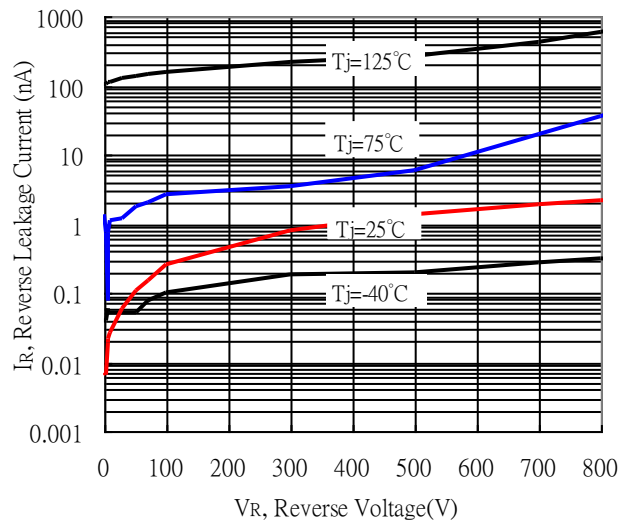
Power Derating Curve



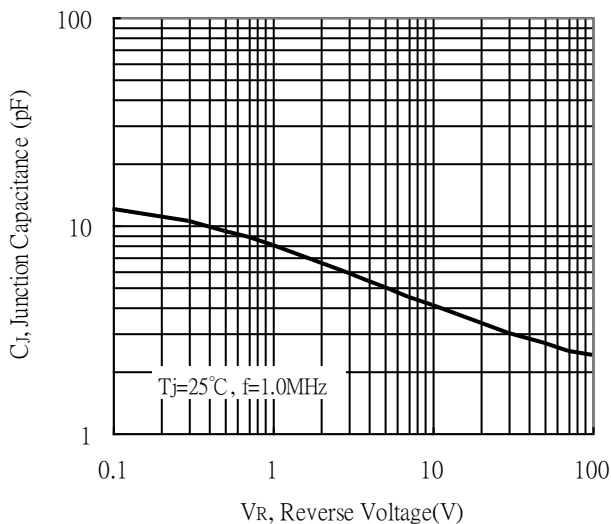
Forward Current vs Forward Voltage



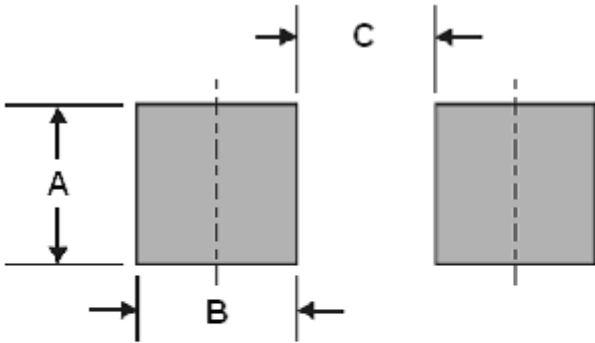
Reverse Leakage Current vs Reverse Voltage



Junction Capacitance vs Reverse Voltage



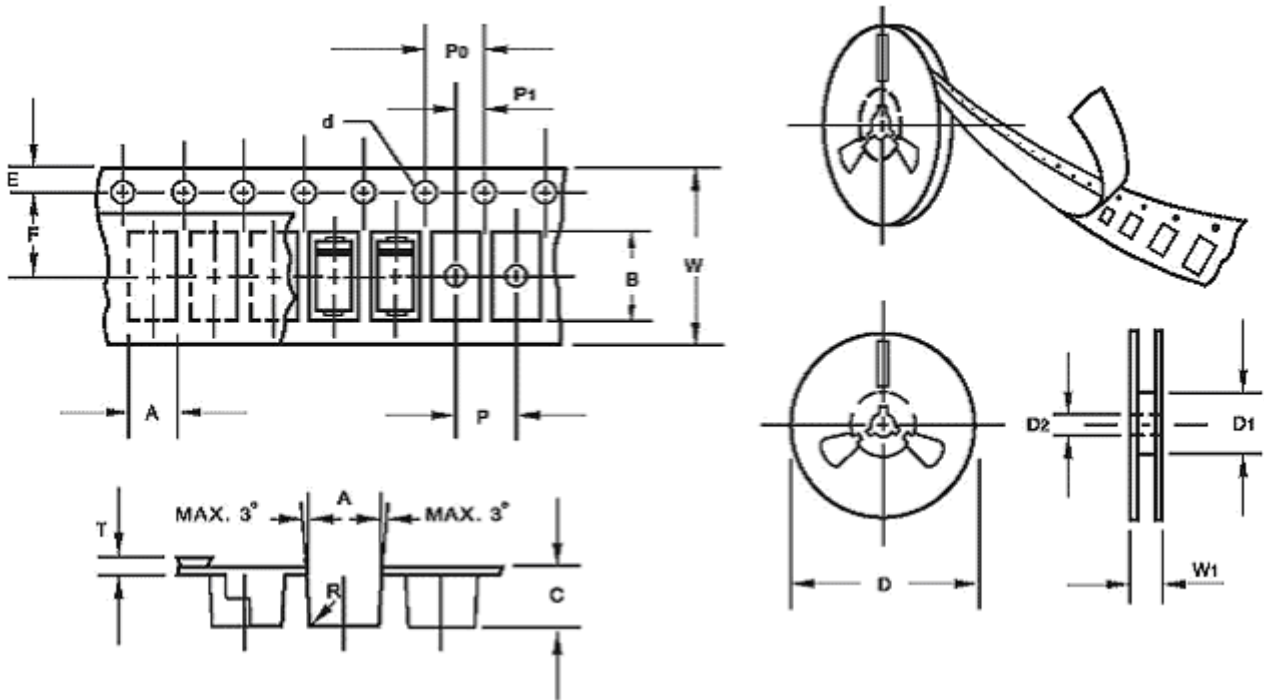
**Recommended Footprint**



Dimensions in inches and (millimeter)

DIM	Inches	Millimeters
	Typ	Typ
A	0.078	2.00
B	0.059	1.50
C	0.110	2.80

### Taping Reel Dimension



unit : mm

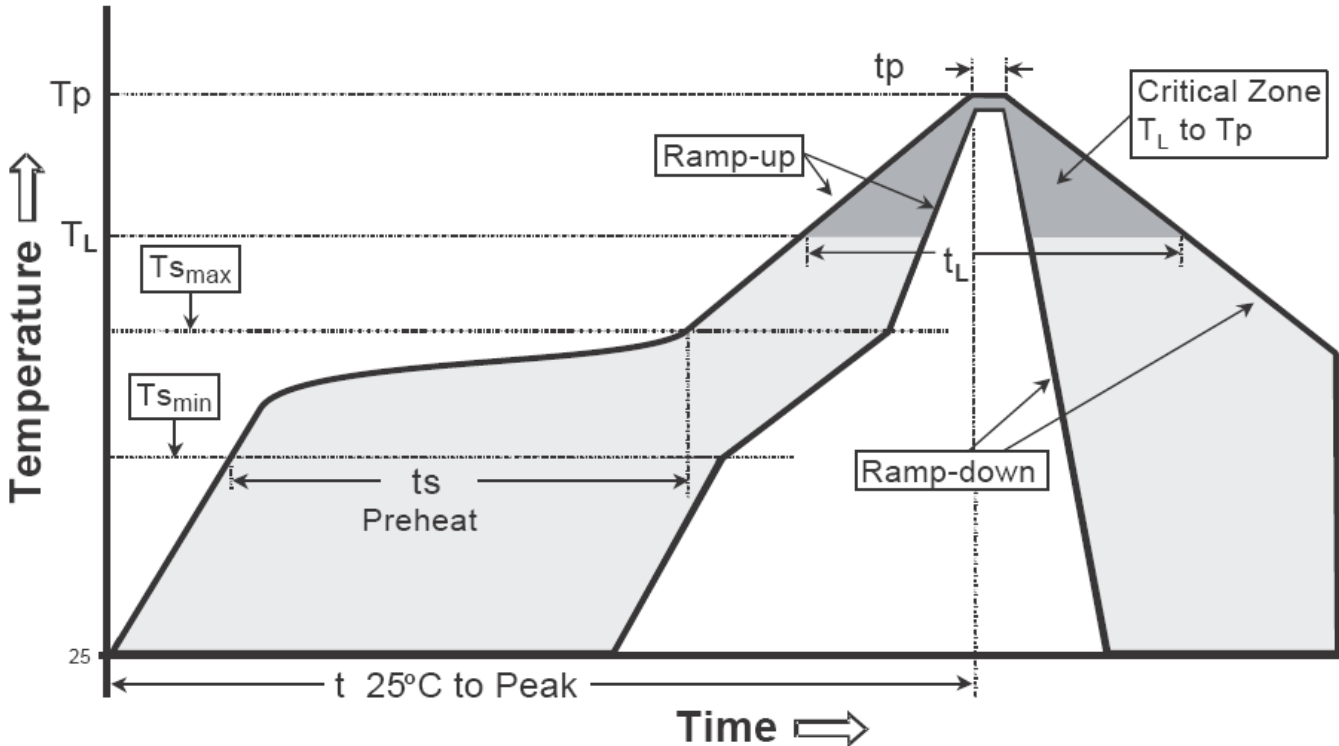
Item	Tolerance	Symbol	
Carrier width	0.10	A	3.81
Carrier length	0.10	B	5.74
Carrier depth	0.10	C	2.24
Sprocket hole	0.10	d	1.50
Reel outside diameter	2.00	D	330.00
Reel inner diameter	min.	D1	50.00
Feed hole diameter	0.50	D2	13.00
Sprocket hole position	0.10	E	1.75
Punch hole position	0.10	F	5.50
Punch hole pitch	0.10	P	8.00
Sprocket hole pitch	0.10	P0	4.00
Embossment center	0.10	P1	2.00
Overall tape thickness	0.10	T	0.23
Tape width	0.30	W	12.00
Reel width	1.00	W1	18.00

Note : Devices are packed in accordance with EIA standard RS-481-A and specification given above.

**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

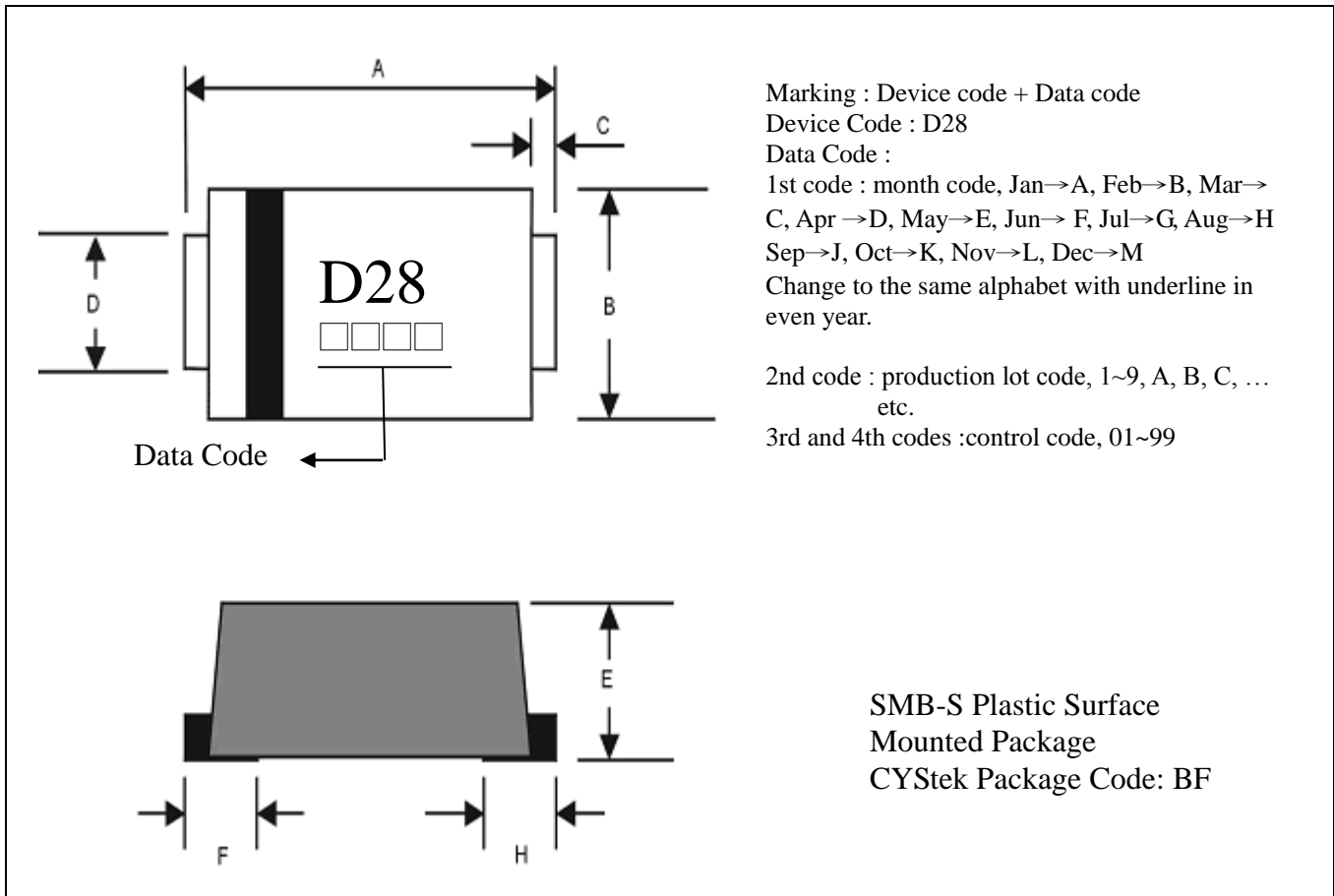
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**SMB-S/DO-214AA Dimension**



\*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.205	0.220	5.2	5.6	E	0.060	0.067	1.5	1.7
B	0.122	0.138	3.1	3.5	F	0.040*		1.0*	
C	0.020*		0.5*		H	0.040*		1.0*	
D	0.075	0.083	1.9	2.1	-	-	-	-	-

Notes : 1.Controlling dimension : millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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