

High Voltage NPN Epitaxial Planar Transistor

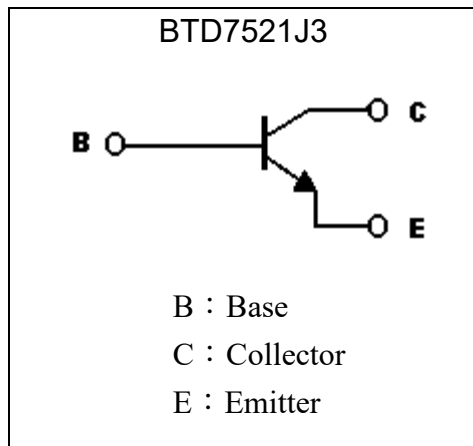
BTD7521J3

BV_{DSS}	90V
I_D	10A
$R_{CE(SAT)}$	0.1 Ω

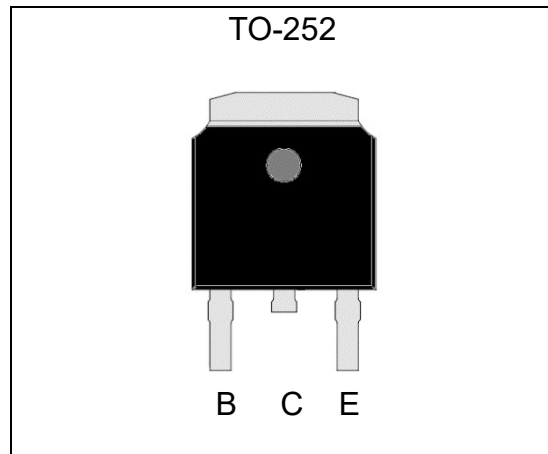
Features

- High BV_{CEO}
- Very high current gain
- Pb-free lead plating package

Symbol

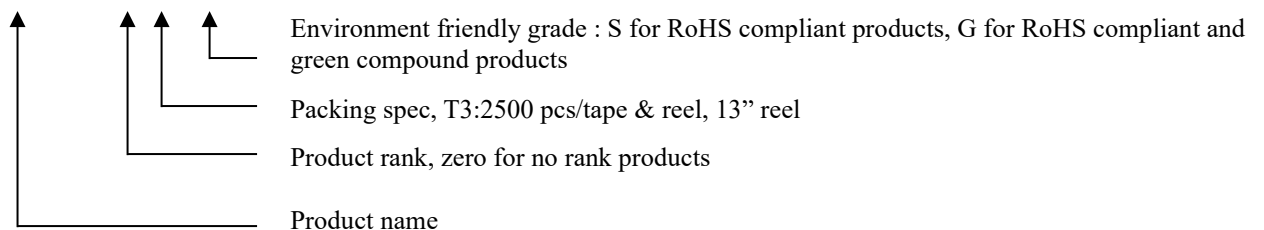


Outline



Ordering Information

Device	Package	Shipping
BTD7521J3-0-T3-G	TO-252 (RoHS compliant and halogen-free package)	2500 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V _{CBO}	90	V
Collector-Emitter Voltage	V _{CEO}	90	V
Emitter-Base Voltage	V _{EBO}	7	V
Collector Current (DC)	I _C	10	A
Collector Current (Pulse)	I _{CP}	20 (Note 1)	
Power Dissipation @ T _A =25°C	P _D	1.75 (Note 2)	W
Power Dissipation @ T _C =25°C	P _D	30	
Thermal Resistance, Junction to Ambient	R _{θJA}	71.4 (Note 2)	°C/W
Thermal Resistance, Junction to Case	R _{θJC}	4.2	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{stg}	-55~+150	°C

Note : 1. Single Pulse , Pw ≤ 300μs, Duty ≤ 2%.
 2. When mounted on a PCB with the minimum pad size.

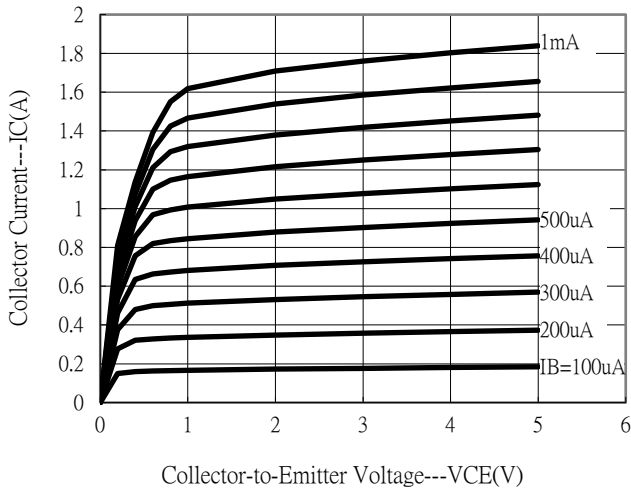
Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	90	-	-	V	I _C =1mA, I _E =0
BV _{CEO}	90	-	-	V	I _C =10mA, I _B =0
BV _{EBO}	7	-	-	V	I _C =100μA, I _C =0
I _{CBO}	-	-	10	μA	V _{CB} =90V, I _E =0
I _{EBO}	-	-	10	μA	V _{EB} =7V, I _C =0
*V _{CE(sat)}	-	-	0.5	V	I _C =5A, I _B =50mA
*R _{CE(sat)}	-	-	0.1	Ω	I _C =5A, I _B =50mA
*V _{BE(sat)}	-	-	1.2	V	I _C =5A, I _B =50mA
*h _{FE}	1000	-	-	-	V _{CE} =5V, I _C =1A
*h _{FE}	500	-	-	-	V _{CE} =5V, I _C =5A
Cob	-	130	-	pF	V _{CB} =10V, f=1MHz

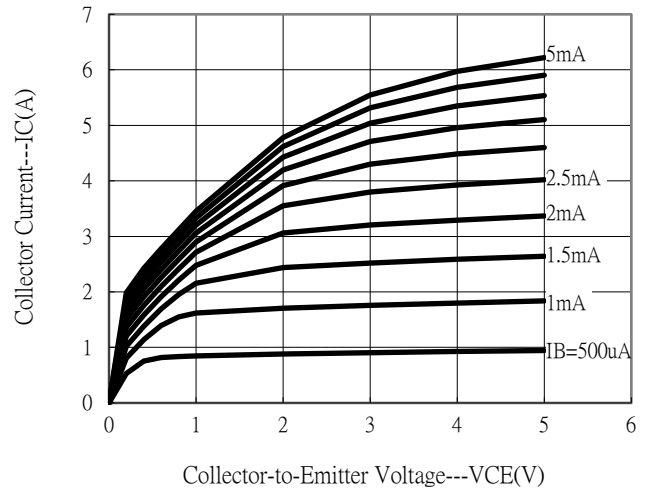
*Pulse Test : Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

Typical Characteristics

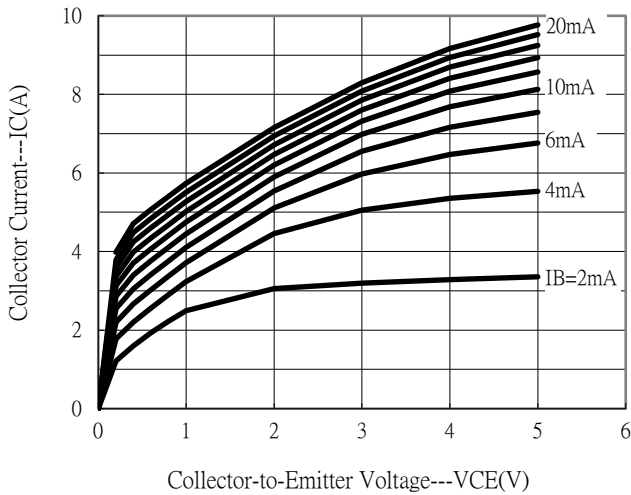
Emitter Grounded Output Characteristics



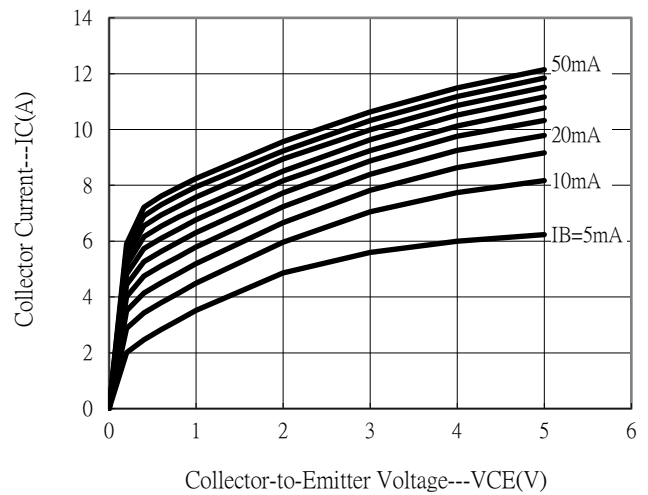
Emitter Grounded Output Characteristics



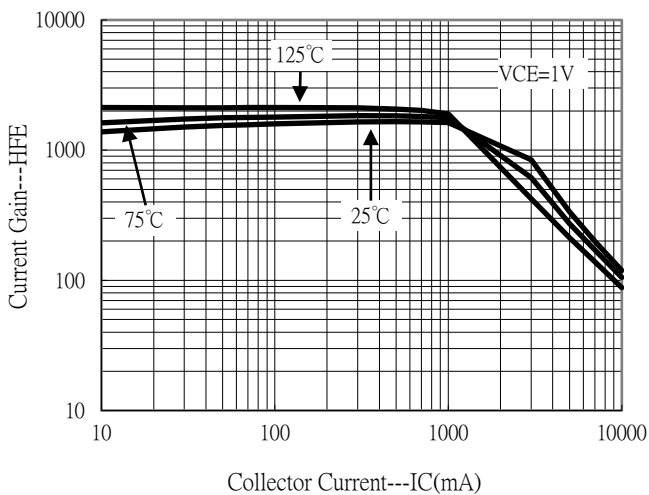
Emitter Grounded Output Characteristics



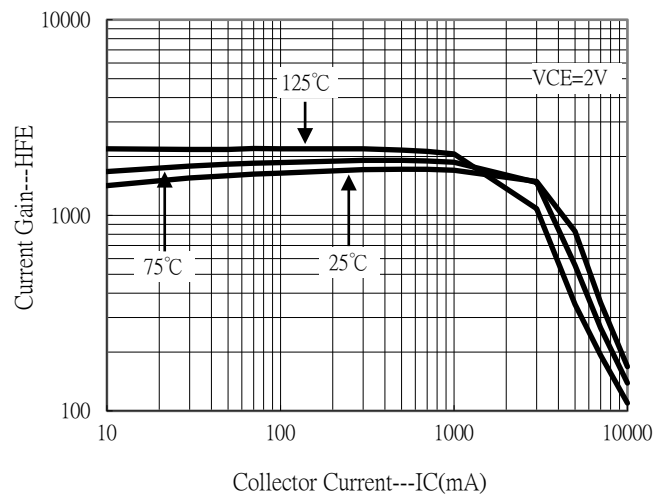
Emitter Grounded Output Characteristics



Current Gain vs Collector Current

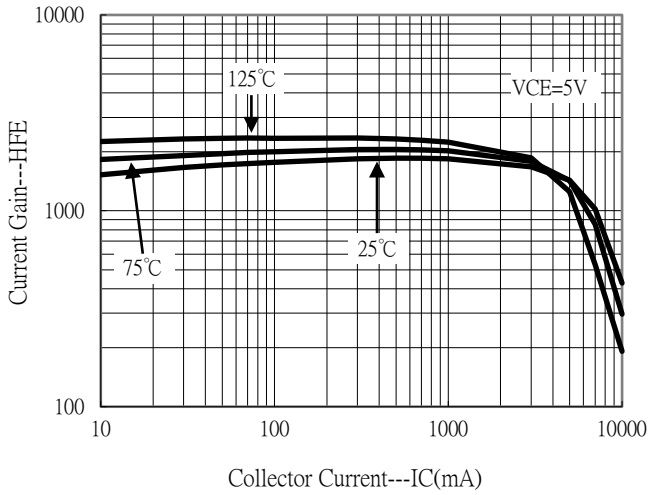


Current Gain vs Collector Current

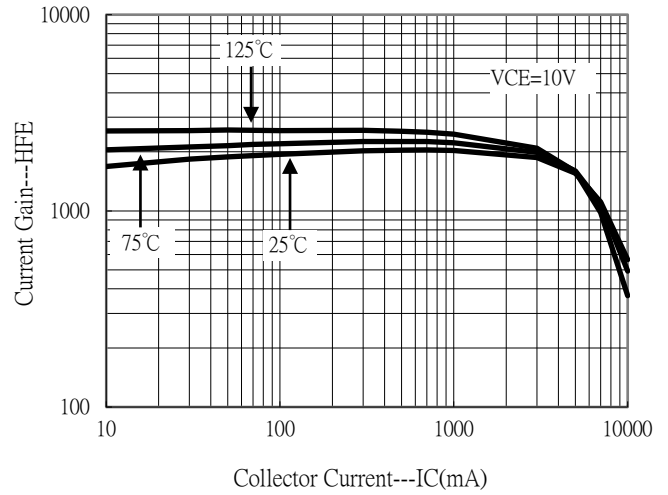


Typical Characteristics(Cont.)

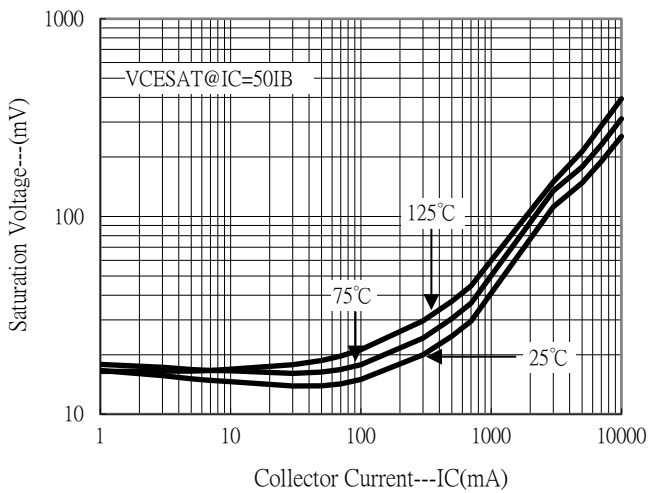
Current Gain vs Collector Current



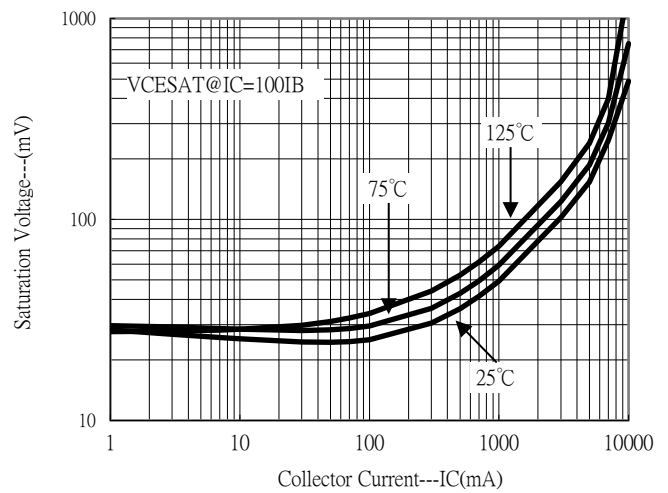
Current Gain vs Collector Current



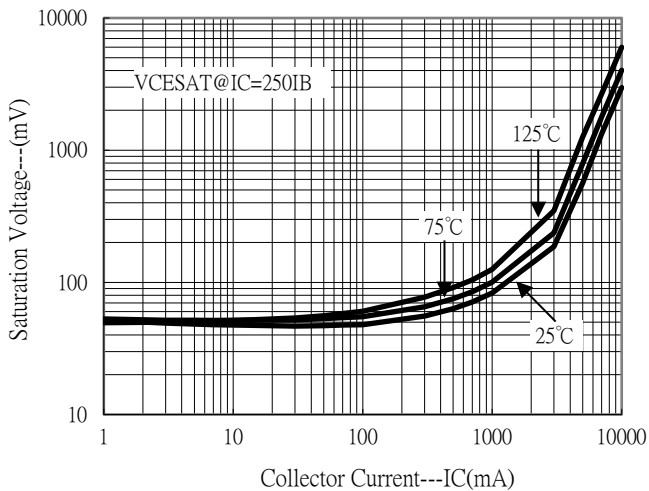
Saturation Voltage vs Collector Current



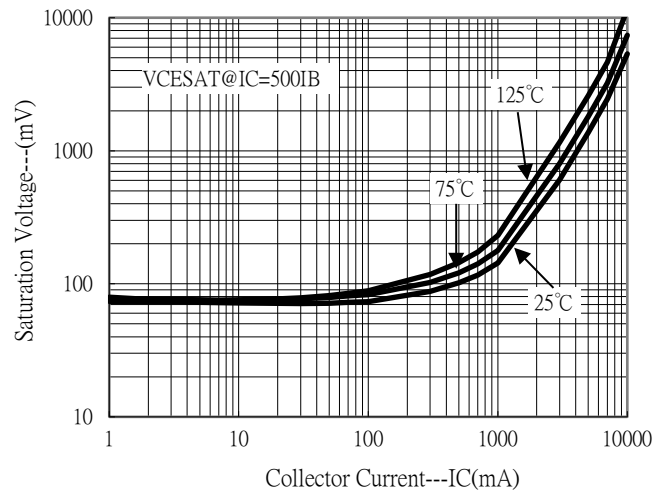
Saturation Voltage vs Collector Current



Saturation Voltage vs Collector Current

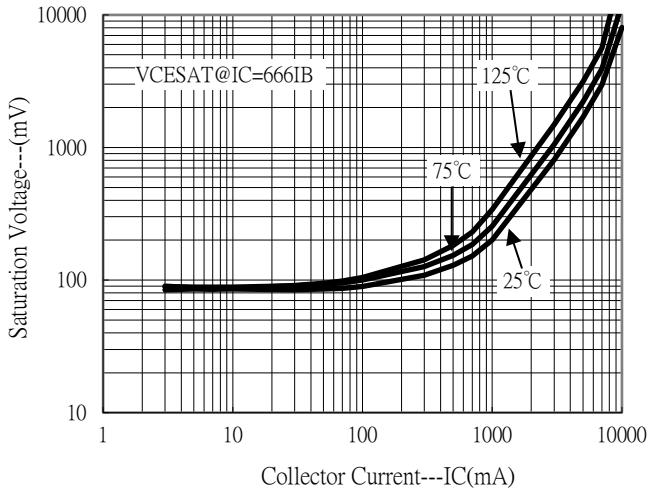


Saturation Voltage vs Collector Current

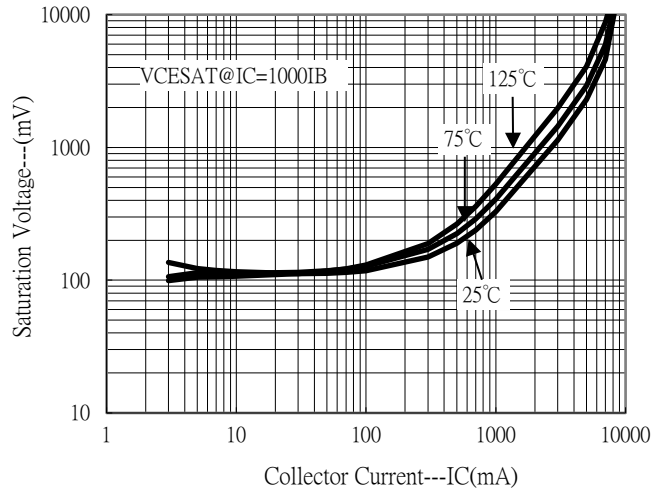


Typical Characteristics(Cont.)

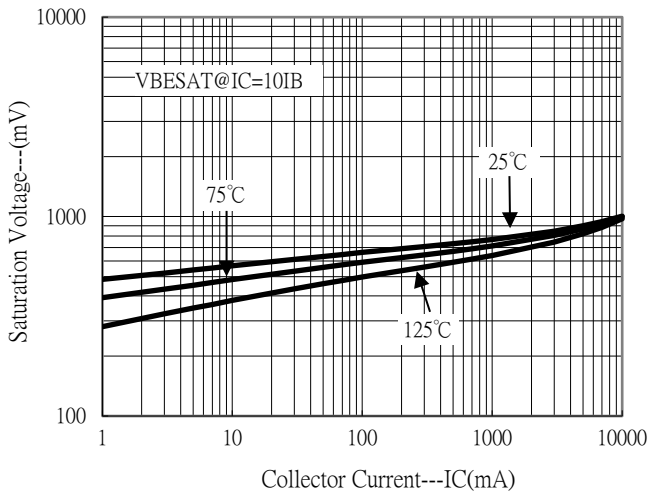
Saturation Voltage vs Collector Current



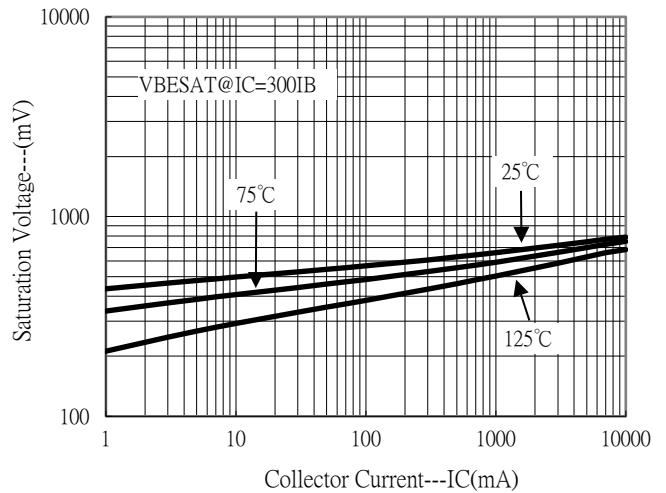
Saturation Voltage vs Collector Current



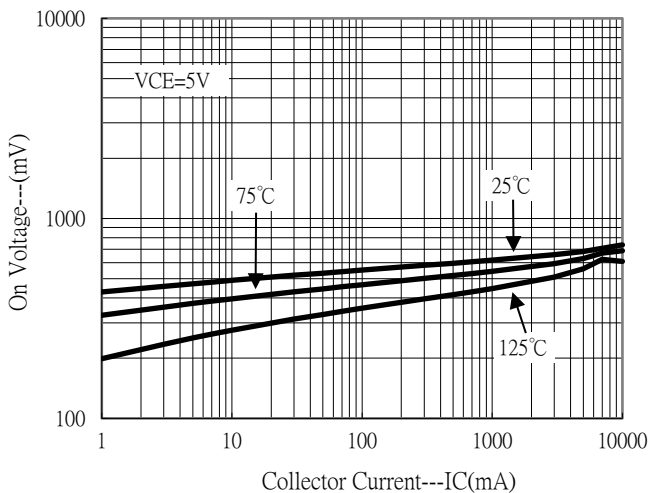
Saturation Voltage vs Collector Current



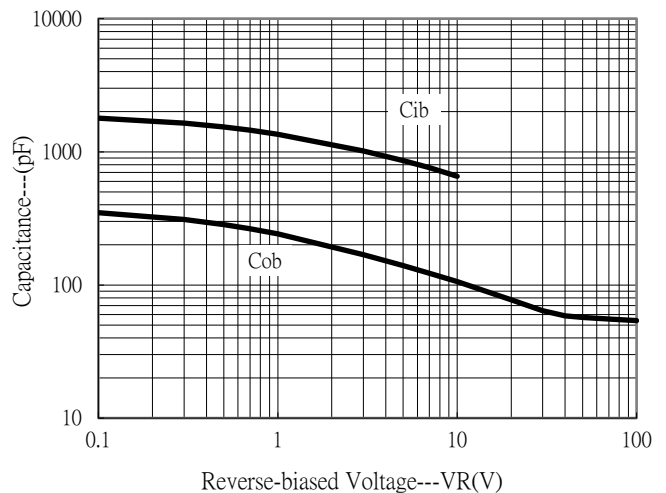
Saturation Voltage vs Collector Current



On Voltage vs Collector Current



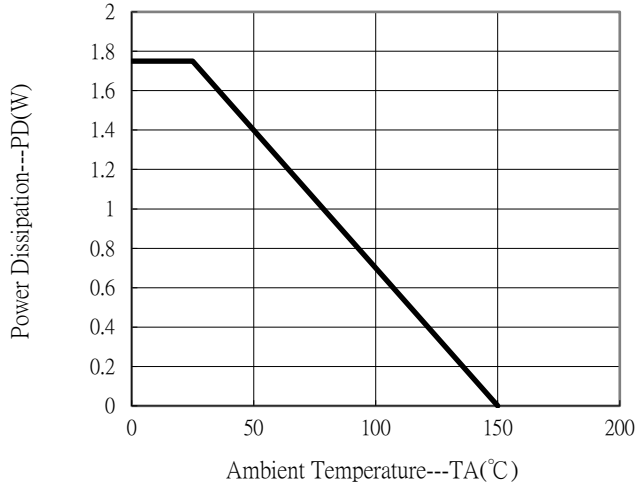
Capacitance vs Reverse-biased Voltage



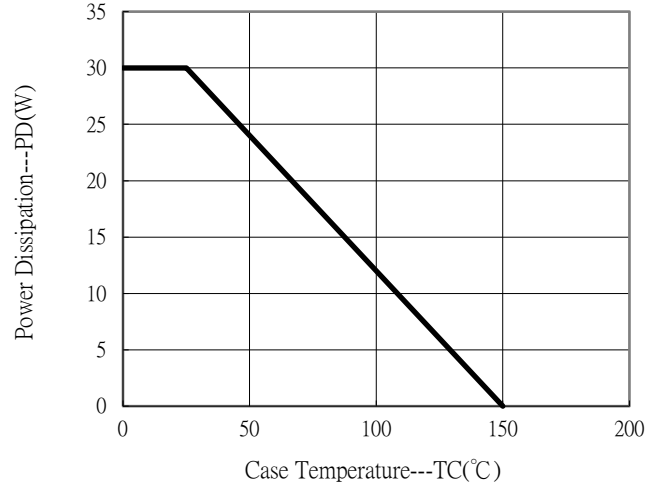


Typical Characteristics(Cont.)

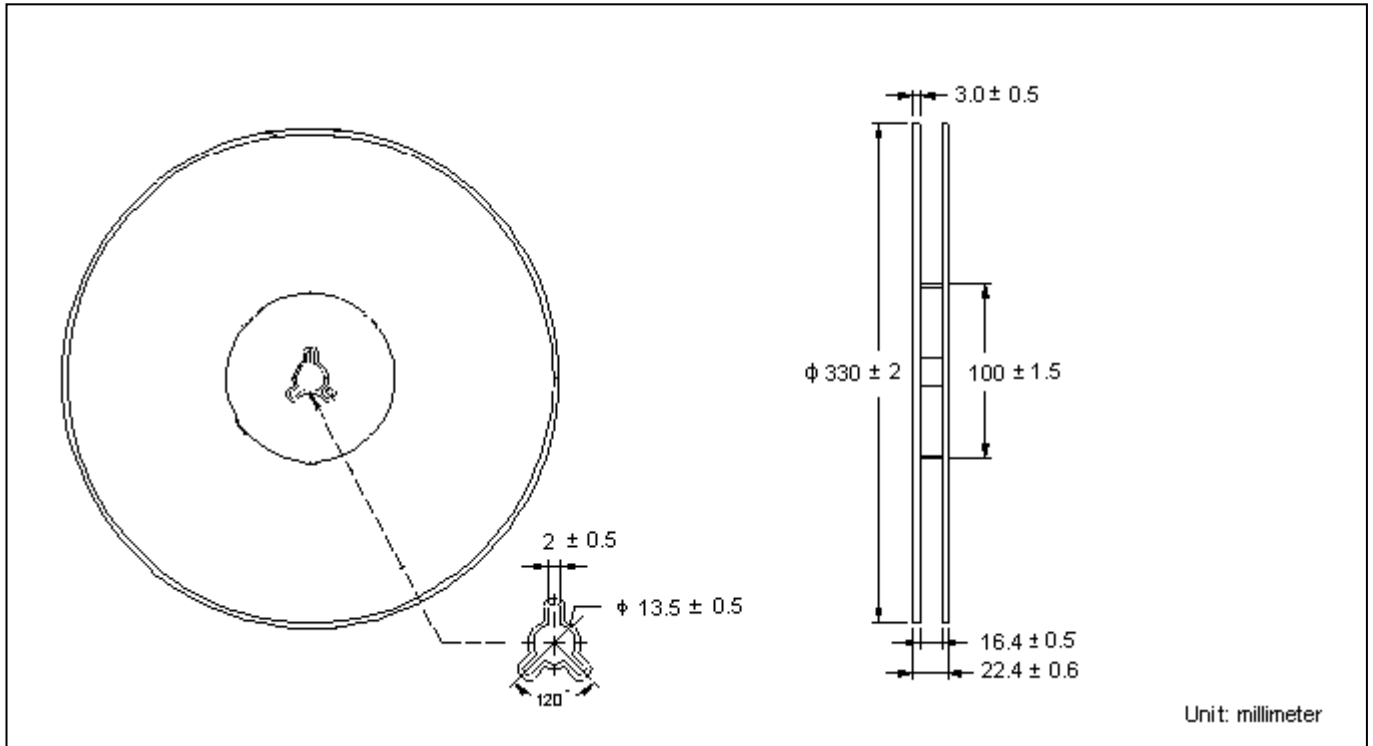
Power Derating Curve



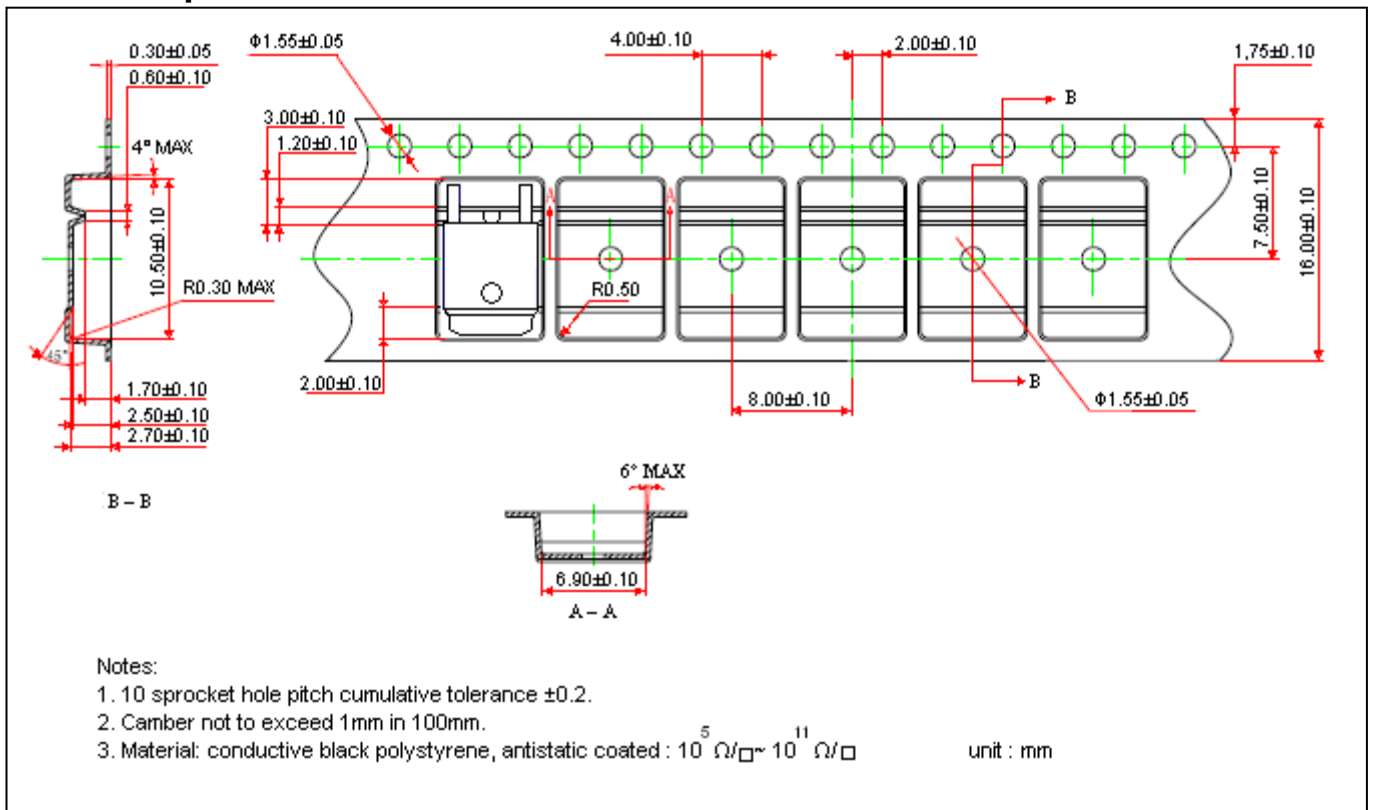
Power Derating Curve



Reel Dimension



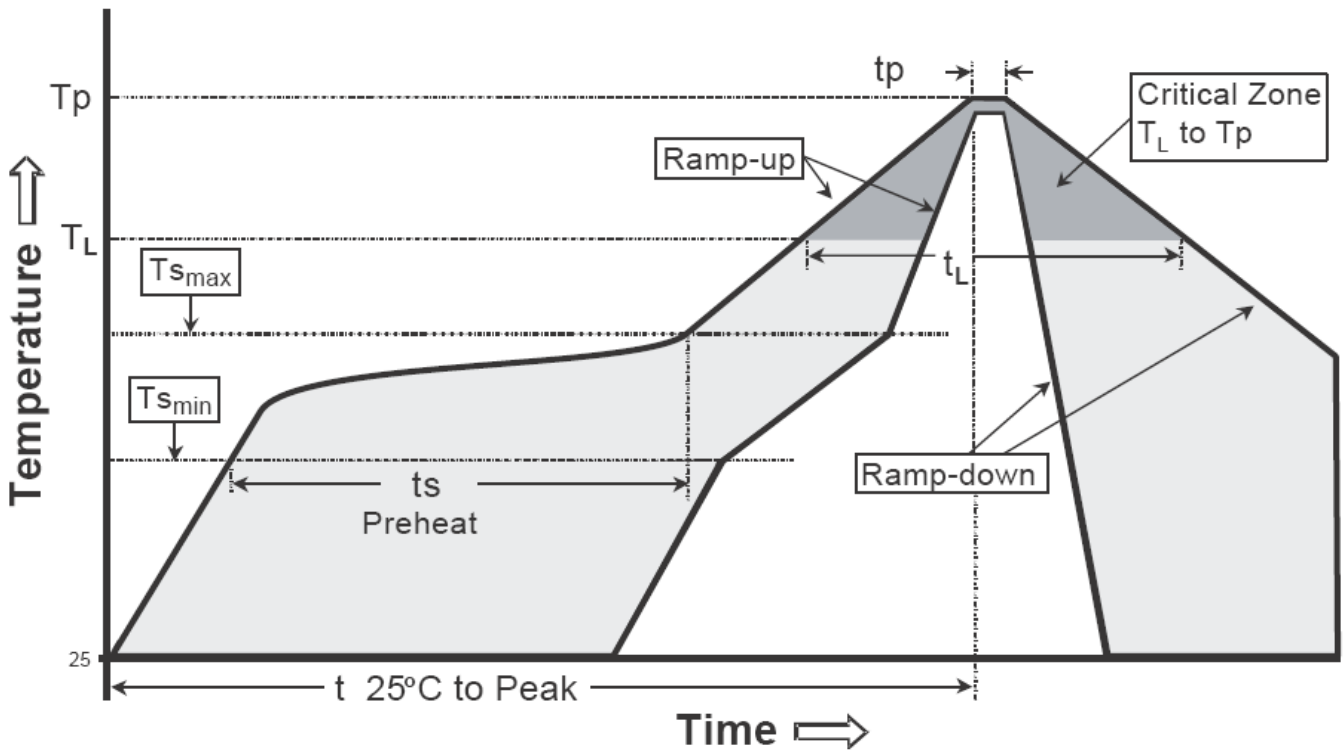
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

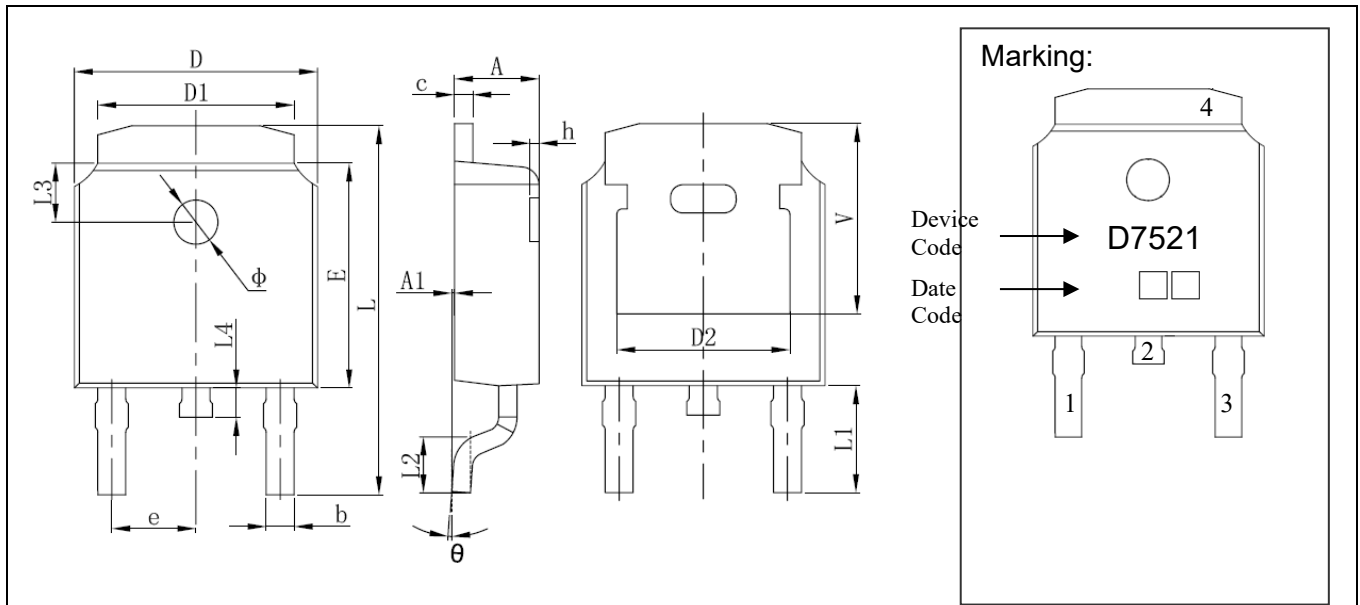
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



3-Lead TO-252 Plastic Surface Mount Package
 CYS Package Code: J3

Style: Pin 1.Base 2.Collector 3.Emitter
 4.Collector

Date Code: Year+Month
 Year: 3→2003, 4→2004
 Month: 1→1, 2→2, . . .
 9→9, A→10, B→11, C→12

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	L	0.382	0.406	9.712	10.312
A1	0.000	0.005	0.000	0.127	L1	0.114	REF	2.900	REF
b	0.025	0.030	0.635	0.770	L2	0.055	0.067	1.400	1.700
c	0.018	0.023	0.460	0.580	L3	0.063	REF	1.600	REF
D	0.256	0.264	6.500	6.700	L4	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	Φ	0.043	0.051	1.100	1.300
D2	0.190	REF	4.830	REF	θ	0°	8°	0°	8°
E	0.236	0.244	6.000	6.200	h	0.000	0.012	0.000	0.300
e	0.086	0.094	2.186	2.386	V	0.207	REF	5.250	REF

- Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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