General Purpose NPN Epitaxial Planar Transistor

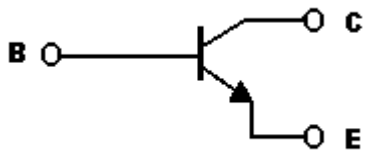
BTC2880N3

Features

- High breakdown voltage, $BV_{CEO} \geq 100V$
- Large continuous collector current capability
- Low collector saturation voltage
- Pb-free lead plating and halogen-free package

Symbol

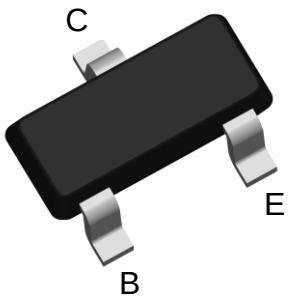
BTC2880N3



B：Base
C：Collector
E：Emitter

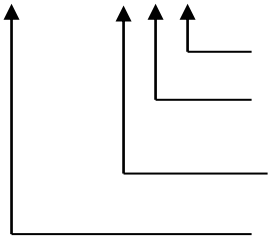
Outline

SOT-23



Ordering Information

Device	Package	Shipping
BTC2880N3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, T1 :3000 pcs/tape & reel, 7” reel

Product rank, zero for no rank products

Product name

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V_{CBO}	180	V
Collector-Emitter Voltage	V_{CEO}	100	V
Emitter-Base Voltage	V_{EBO}	7	V
Collector Current	I_C	2	A
Peak Collector Current	I_{CM}	5	A
Base Current	I_B	0.4	A
Power Dissipation	P_D	225	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	556	°C/W
Operating Junction and Storage Temperature Range	Tj ; Tstg	-55~+150	°C

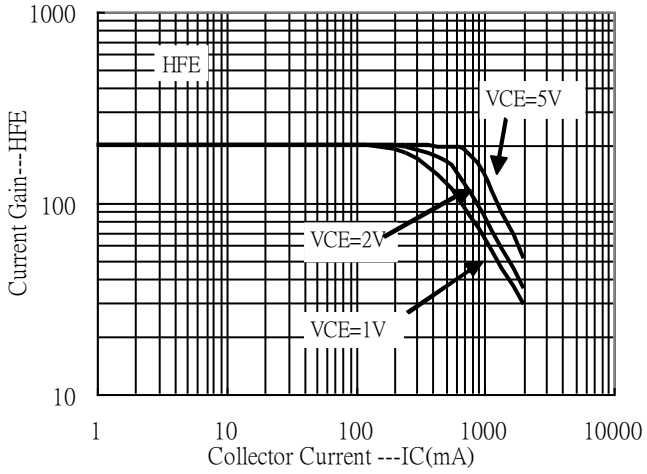
Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{CBO}	180	-	-	V	I_C=50μA
BV_{CEO}	100	-	-	V	I_C=1mA
BV_{EBO}	7	-	-	V	I_E=50μA
I_{CBO}	-	-	100	nA	V_{CB}=180V
I_{EBO}	-	-	100	nA	V_{EB}=6V
*$V_{CE(sat)}$	-	0.11	0.2	V	I_C=500mA, I_B=50mA
*$V_{CE(sat)}$	-	0.22	0.4	V	I_C=1A, I_B=50mA
*$V_{CE(sat)}$	-	0.44	1	V	I_C=2A, I_B=100mA
*$V_{BE(sat)}$	-	0.85	1.2	V	I_C=500mA, I_B=50mA
*$V_{BE(on)}$	-	0.77	0.9	V	V_{CE}=5V, I_C=500mA
*h_{FE} 1	120	-	-	-	V_{CE}=5V, I_C=50mA
*h_{FE} 2	150	-	250	-	V_{CE}=5V, I_C=100mA
*h_{FE} 3	80	-	-	-	V_{CE}=5V, I_C=1A
f_T	50	-	-	MHz	V_{CE}=10V, I_C=50mA, f=100MHz
Cob	-	18	40	pF	V_{CB}=10V, I_E=0A,f=1MHz

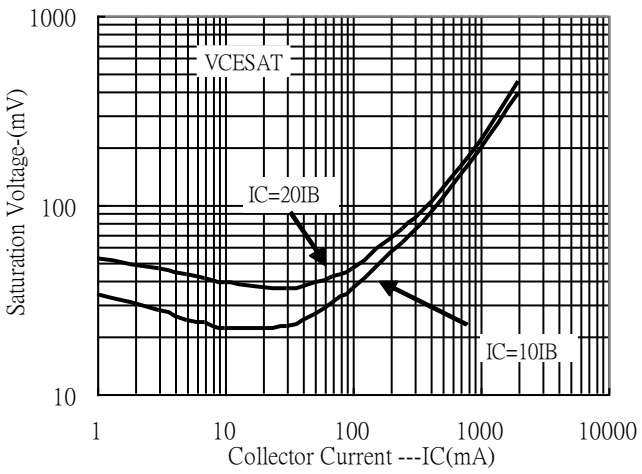
*Pulse Test: Pulse Width ≤380μs, Duty Cycle≤2%

Typical Characteristics

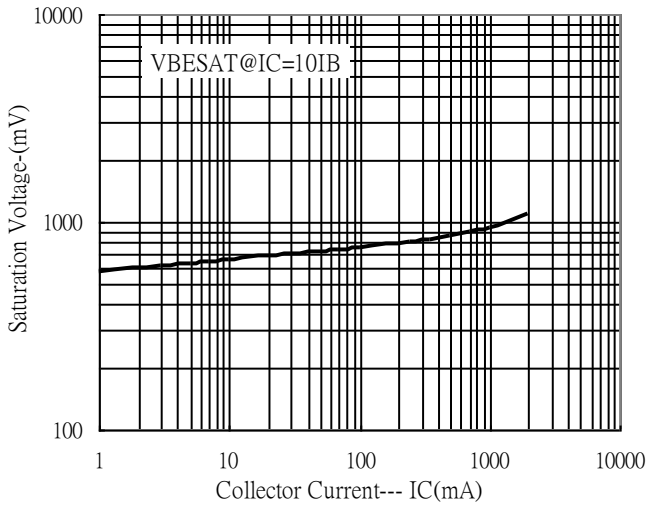
Current Gain vs Collector Current



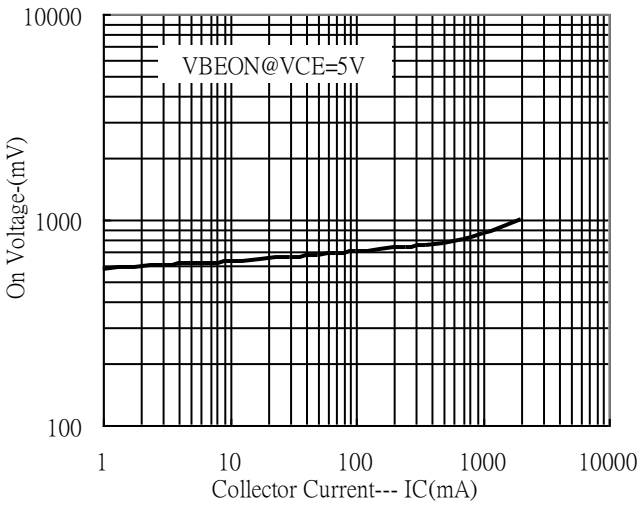
Saturation Voltage vs Collector Current



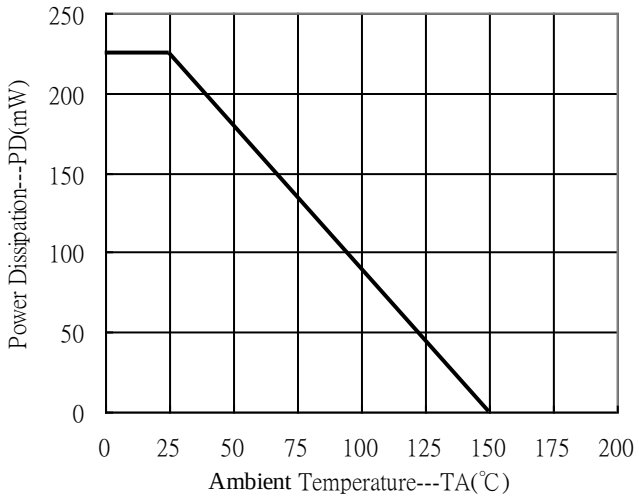
Saturation Voltage vs Collector Current



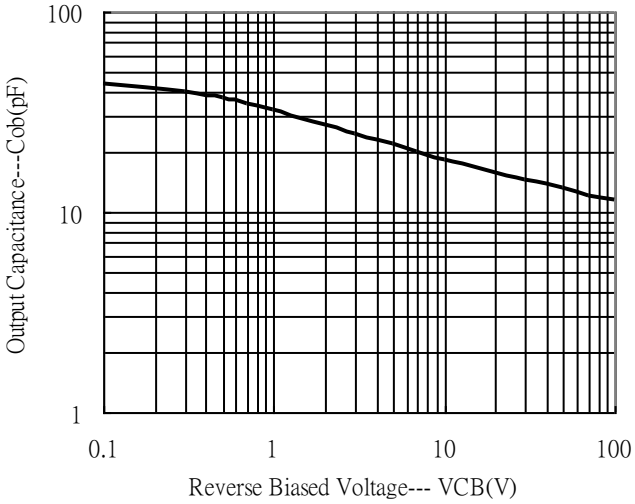
On Voltage vs Collector Current



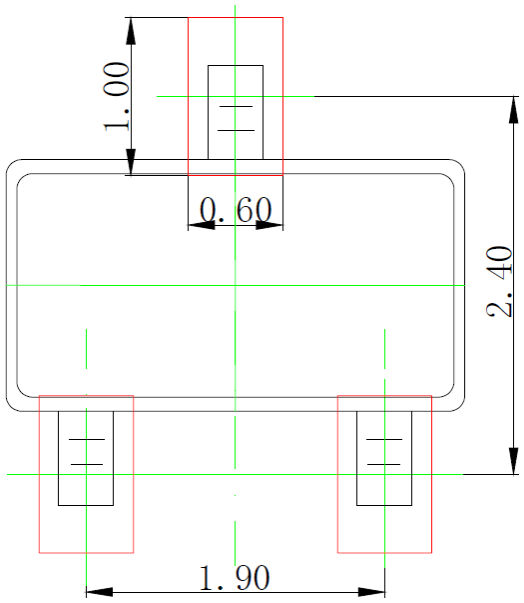
Power Derating Curve



Output Capatiance vs Reverse Biased Voltage

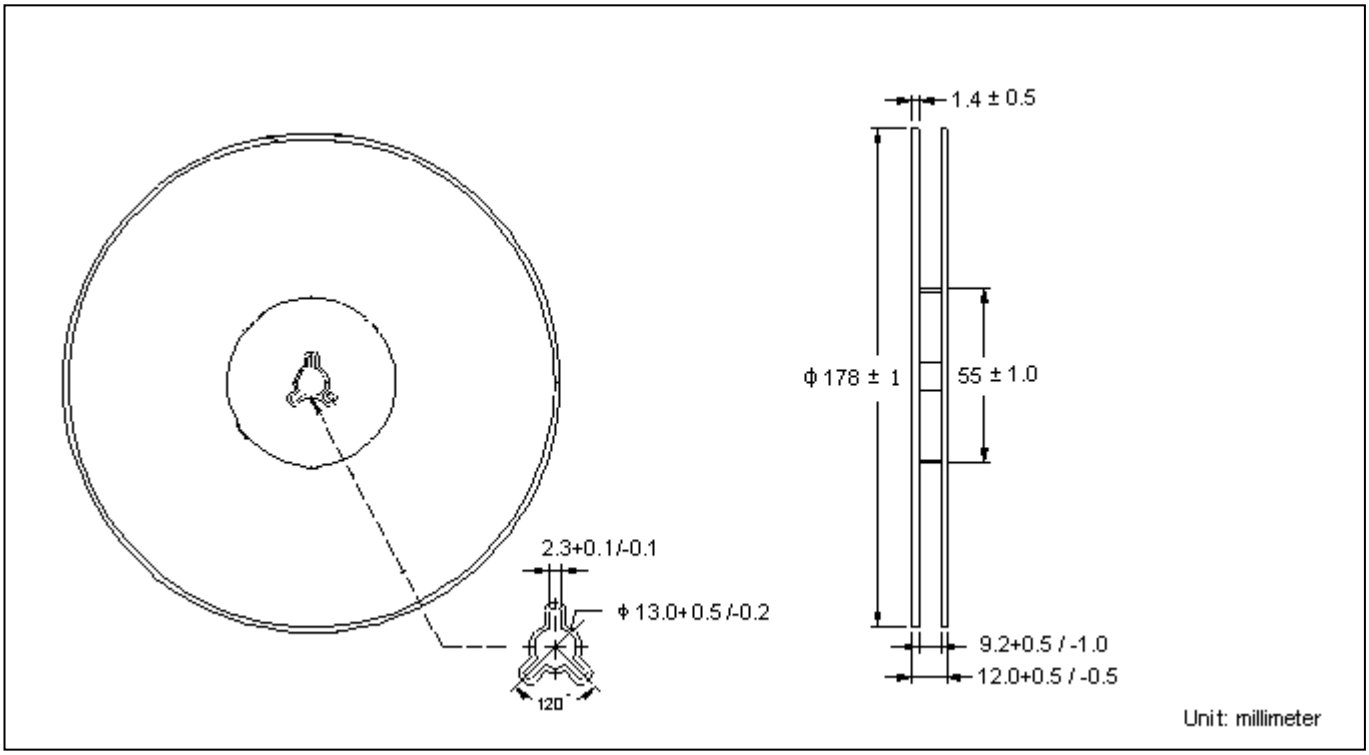


Recommended Soldering Footprint

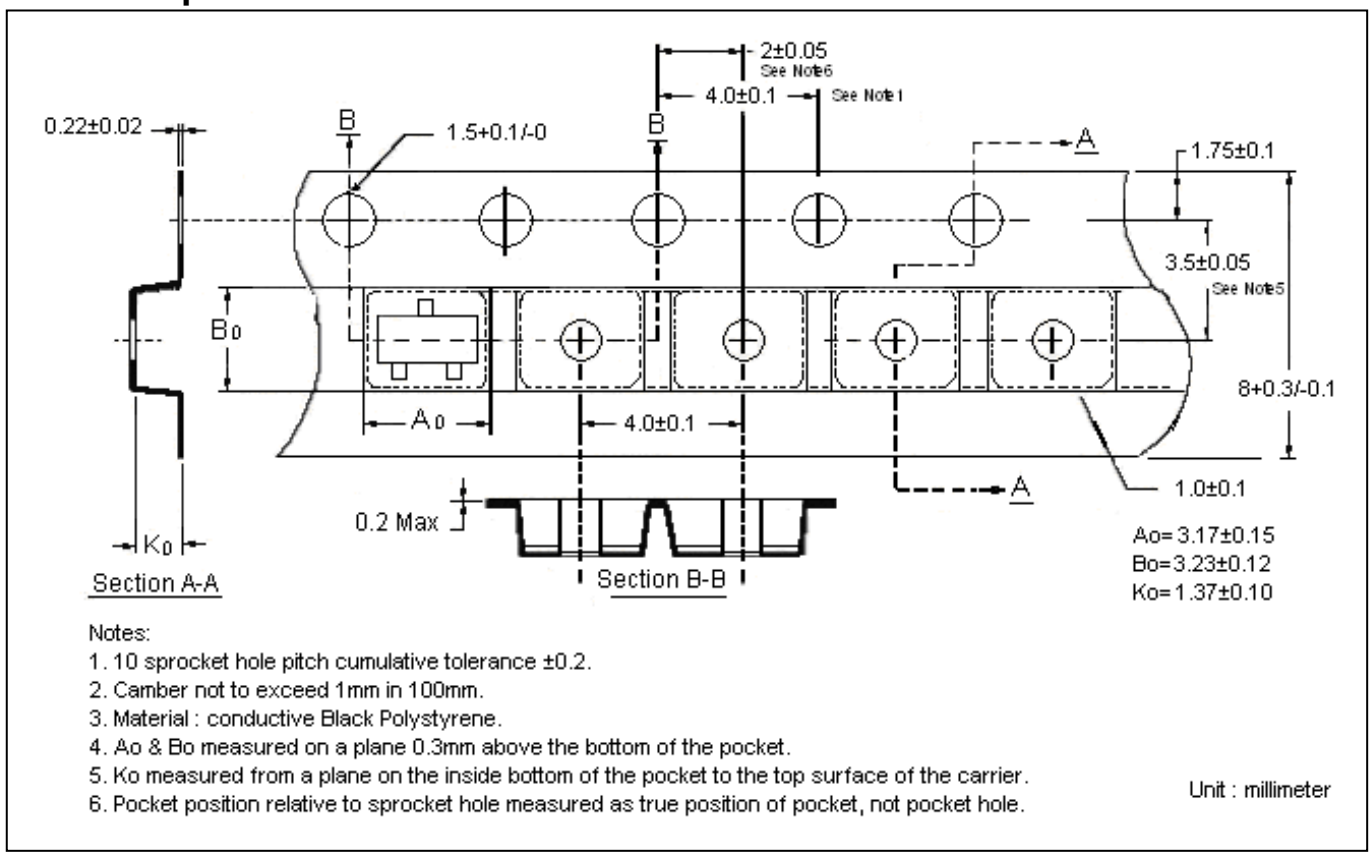


Uint:mm

Reel Dimension



Carrier Tape Dimension



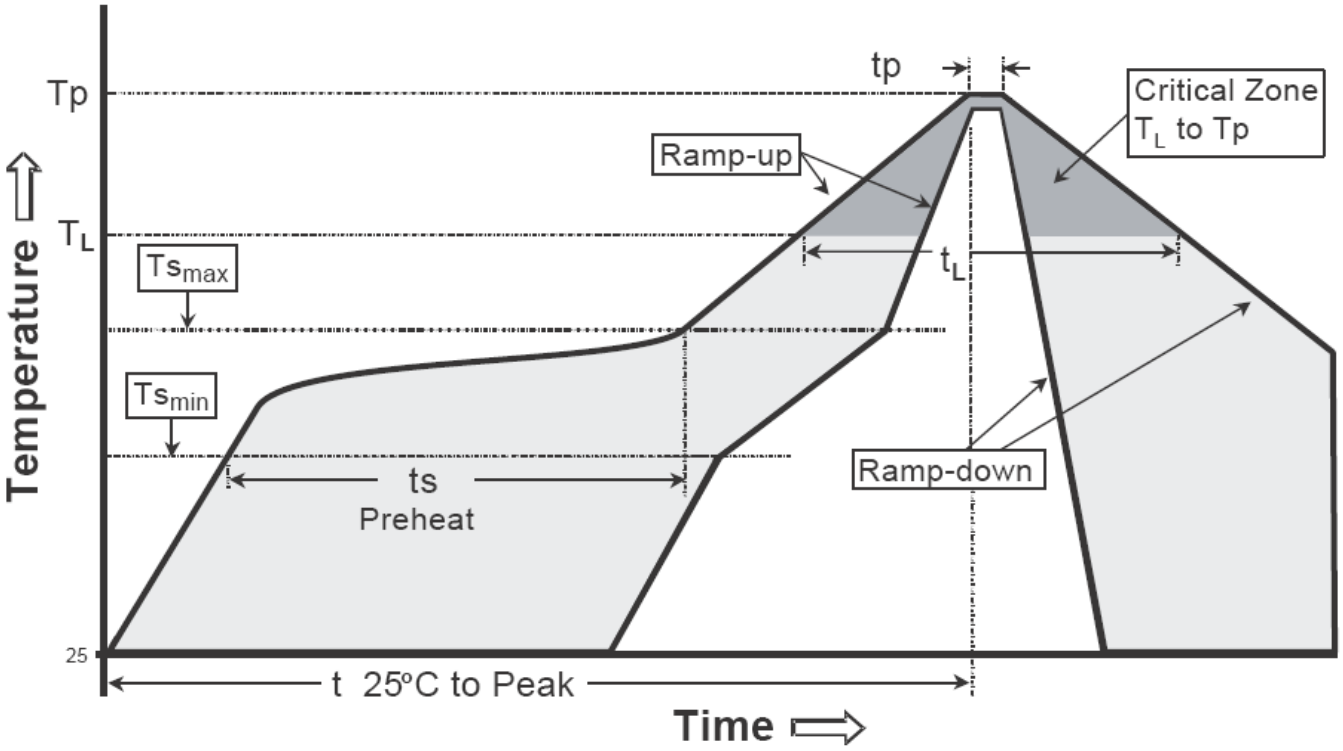
Notes:
1. 10 sprocket hole pitch cumulative tolerance ±0.2.
2. Camber not to exceed 1mm in 100mm.
3. Material : conductive Black Polystyrene.
4. Ao & Bo measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

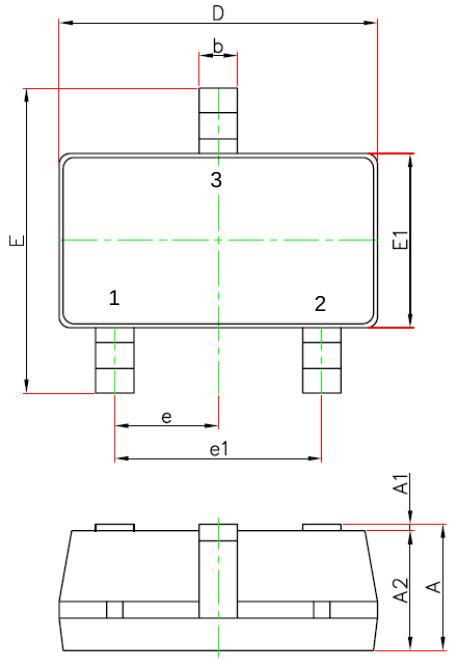
Recommended temperature profile for IR reflow

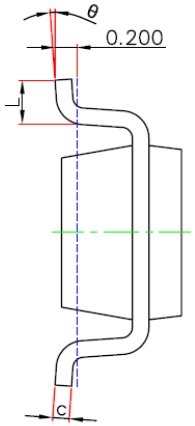


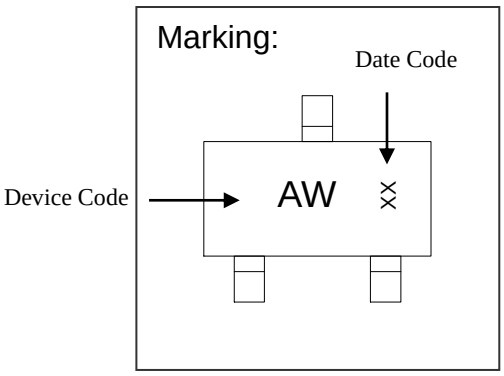
Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time($t_{s\ min}$ to $t_{s\ max}$)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-23 Dimension







Style : Pin 1.Base 2.Emitter 3.Collector

Date Code: Year+Month
Year: 3→2003, 4→2004
Month: 1→1, 2→2, • • •
9→9, A→10, B→11, C→12

3-Lead SOT-23 Plastic
Surface Mounted Package
CYStek Package Code: N3

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.041	0.049	1.05	1.25	E1	0.059	0.067	1.50	1.70
A1	0.000	0.004	0.00	0.10	E	0.104	0.116	2.65	2.95
A2	0.041	0.045	1.05	1.15	e	0.037 BSC		0.95 BSC	
b	0.012	0.020	0.30	0.50	e1	0.071	0.079	1.80	2.00
c	0.004	0.008	0.10	0.20	L	0.012	0.024	0.30	0.60
D	0.111	0.119	2.82	3.02	θ	0°	8°	0°	8°

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.