

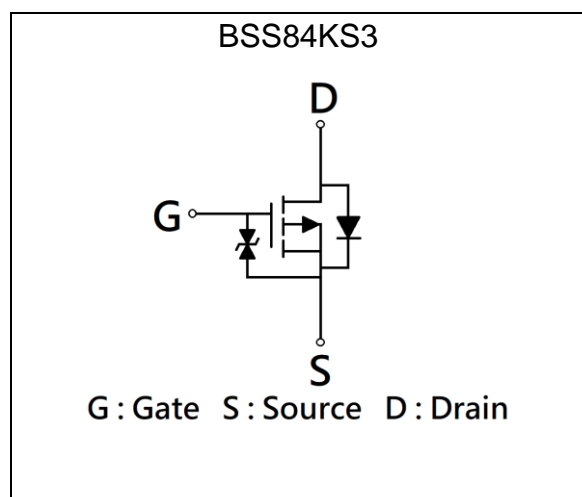
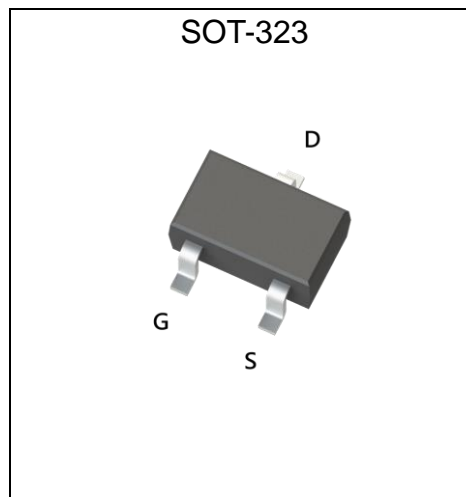
**50V P-CHANNEL Enhancement Mode MOSFET**

# BSS84KS3

$BV_{DSS}$	-50V
$I_D$	-170mA
$R_{DS(on)}@-10V$	$8\Omega$ (MAX)
$R_{DS(on)}@-5V$	$10\Omega$ (MAX)
$R_{DS(on)}@-3V$	$17\Omega$ (MAX)

**Features**

- Low gate charge
- Excellent thermal and electrical capabilities
- Pb-free lead plating and halogen-free package

**Equivalent Circuit**

**Outline**

**Ordering Information**

Device	Package	Shipping
BSS84KS3-0-T1-G	SOT-323 (Pb-free lead plating & halogen-free package)	3000 pcs / Tape & Reel



**Absolute Maximum Ratings** (Tj=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V <sub>DS</sub>	-50	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current @ T <sub>A</sub> =25°C, V <sub>GS</sub> =-5V	I <sub>D</sub>	-170	mA
Pulsed Drain Current (Note 1)	I <sub>DM</sub>	-520	mA
Maximum Power Dissipation @ T <sub>A</sub> =25°C (Note 2)	P <sub>D</sub>	350	mW
Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>th,ja</sub>	360	°C/W
Operating Junction and Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C

Note : 1. Pulse width ≤ 10μs, duty cycle ≤ 2%.  
 2. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board, t ≤ 5s.

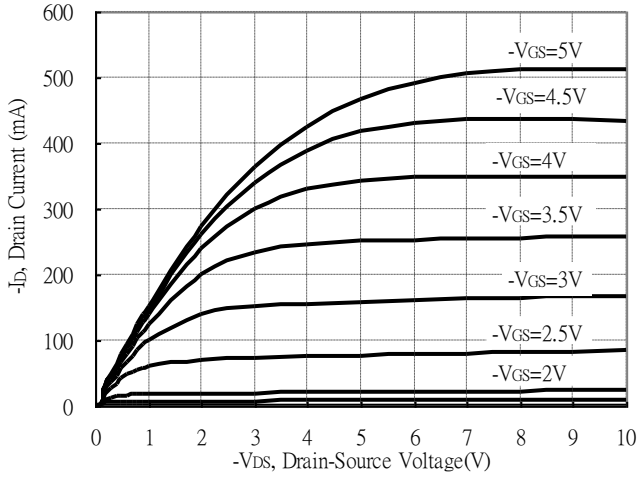
**Electrical Characteristics** (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-50	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1	-1.4	-2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-1mA
G <sub>FS</sub>	80	-	-	mS	V <sub>DS</sub> =-10V, I <sub>D</sub> =-100mA
I <sub>GSS</sub>	-	-	±10	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	-1		V <sub>DS</sub> =-40V, V <sub>GS</sub> =0
*R <sub>DS(ON)</sub>	-	5	8	Ω	V <sub>GS</sub> =-10V, I <sub>D</sub> =-100mA
	-	6	10		V <sub>GS</sub> =-5V, I <sub>D</sub> =-100mA
	-	9	17		V <sub>GS</sub> =-3V, I <sub>D</sub> =-30mA
<b>Dynamic</b>					
C <sub>iss</sub>	-	25	-	pF	V <sub>DS</sub> =-5V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	7	-		
C <sub>rss</sub>	-	2	-		
*t <sub>d(ON)</sub>	-	2.5	-	ns	V <sub>DS</sub> =-15V, I <sub>D</sub> =-100mA, V <sub>GS</sub> =-5V, R <sub>G</sub> =3.3Ω
*t <sub>r</sub>	-	2	-		
*t <sub>d(OFF)</sub>	-	7.3	-		
*t <sub>f</sub>	-	3	-		
*Q <sub>g</sub>	-	1.2	-	nC	V <sub>DS</sub> =-40V, I <sub>D</sub> =-500mA, V <sub>GS</sub> =-5V
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	-130	mA	
*I <sub>SM</sub>	-	-	-520		
*V <sub>SD</sub>	-	-0.85	-1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =-130mA

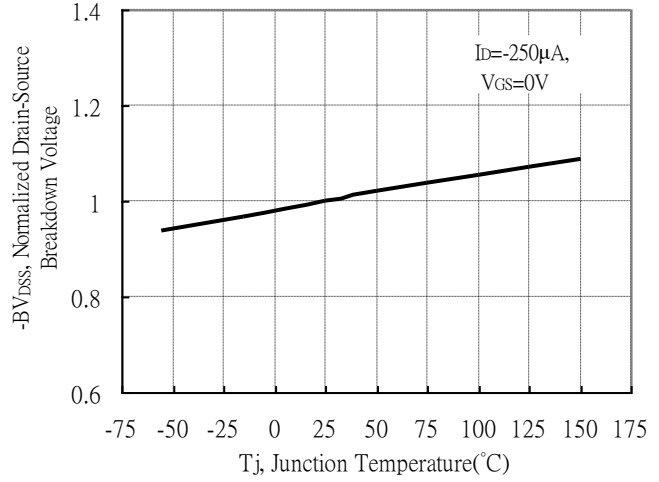
\*Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

## Typical Characteristics

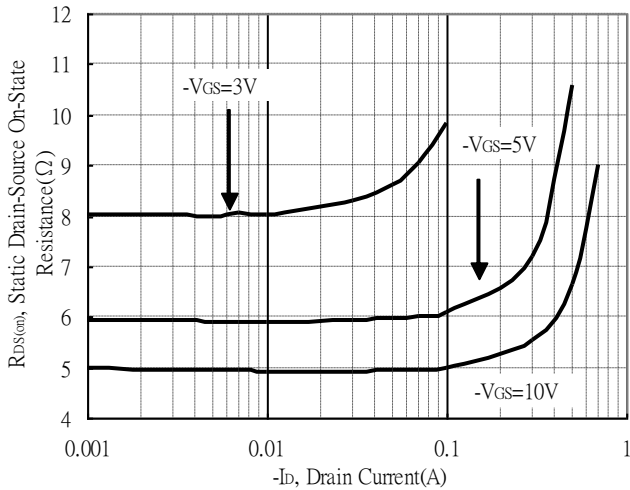
Typical Output Characteristics



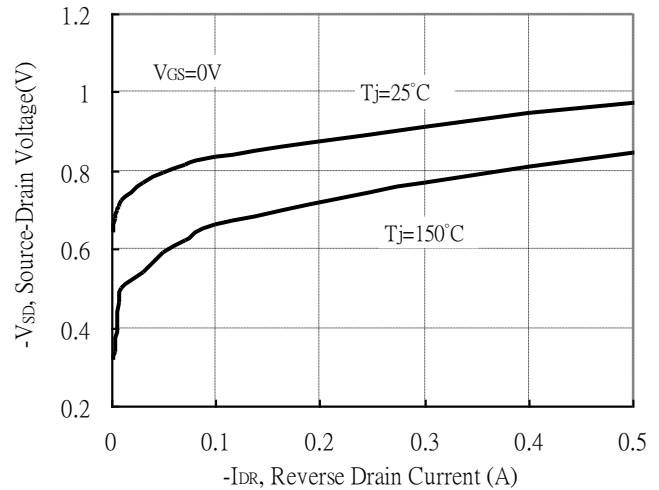
Breakdown Voltage vs Ambient Temperature



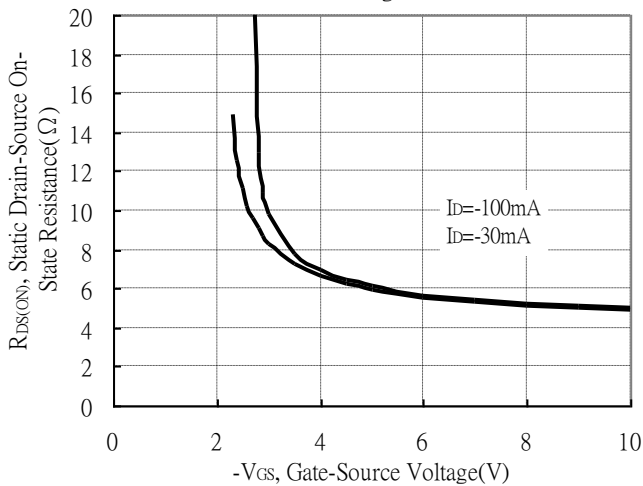
Static Drain-Source On-State resistance vs Drain Current



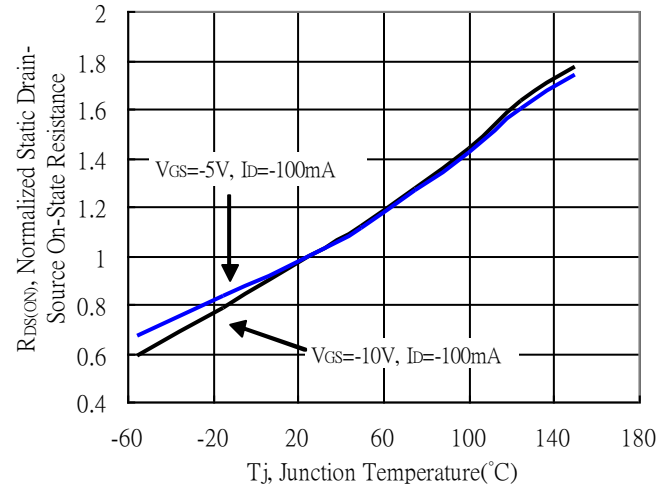
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

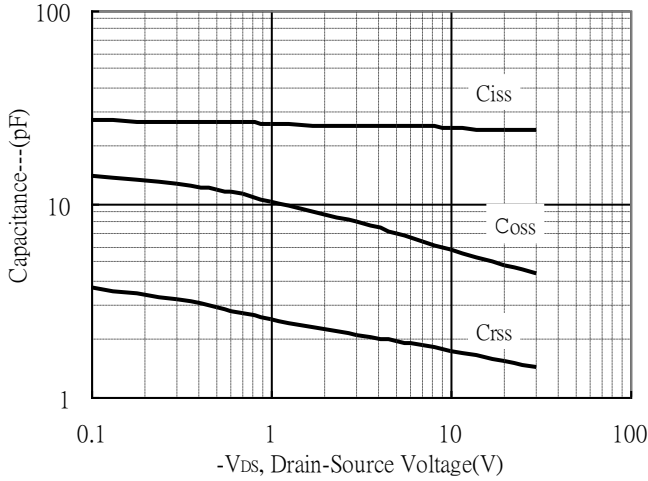


Drain-Source On-State Resistance vs Junction Temperature

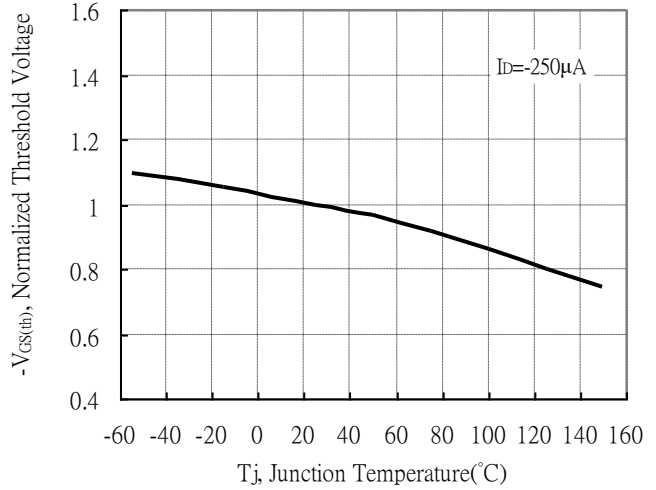


**Typical Characteristics(Cont.)**

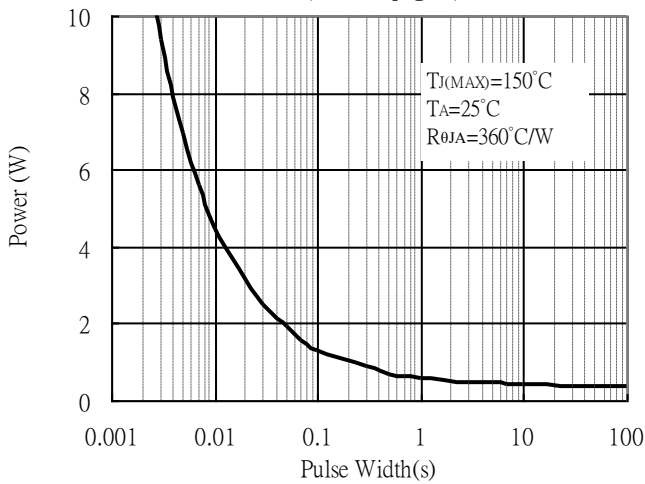
Capacitance vs Drain-to-Source Voltage



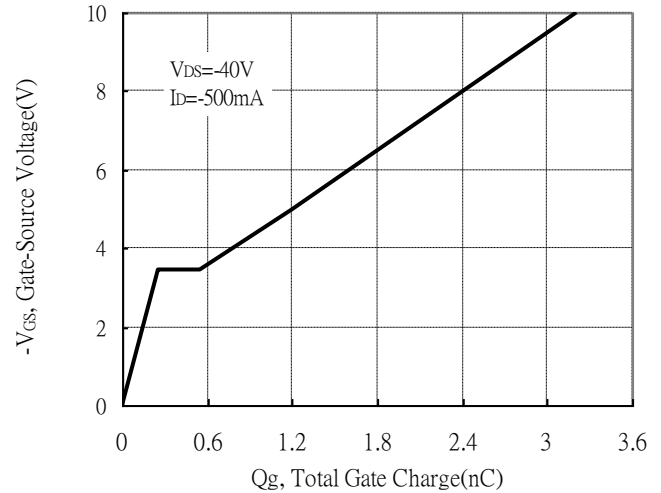
Threshold Voltage vs Junction Temperature



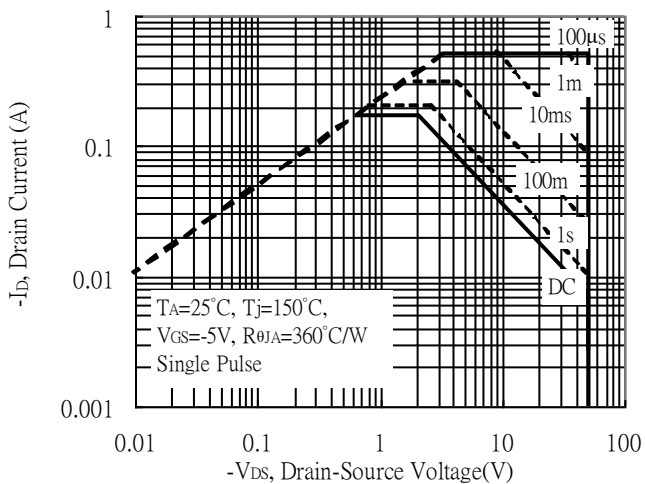
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 1)



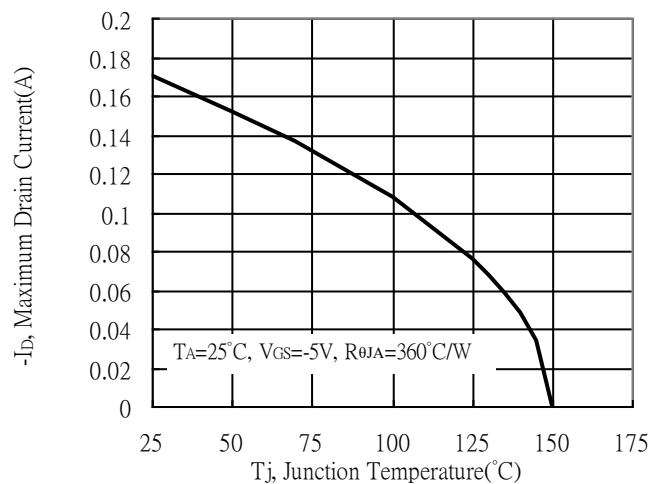
Gate Charge Characteristics



Maximum Safe Operating Area

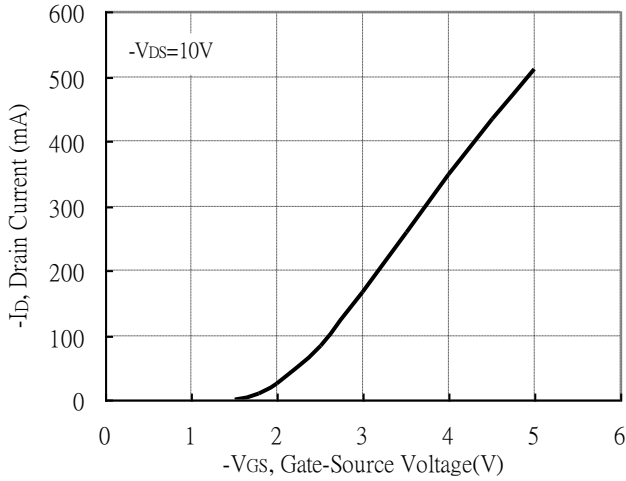


Maximum Drain Current vs Junction Temperature

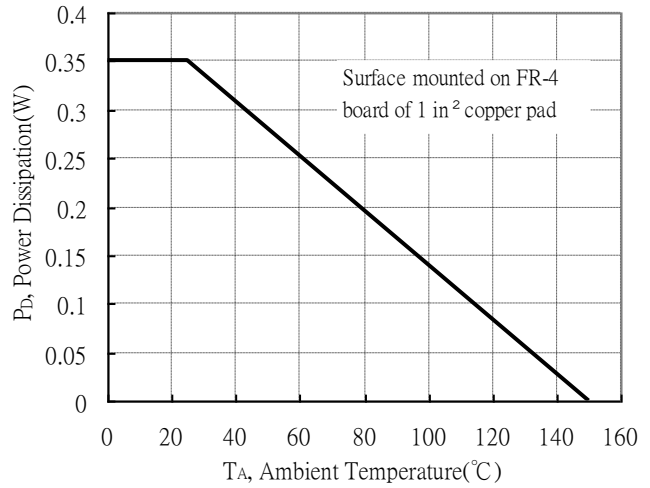


**Typical Characteristics(Cont.)**

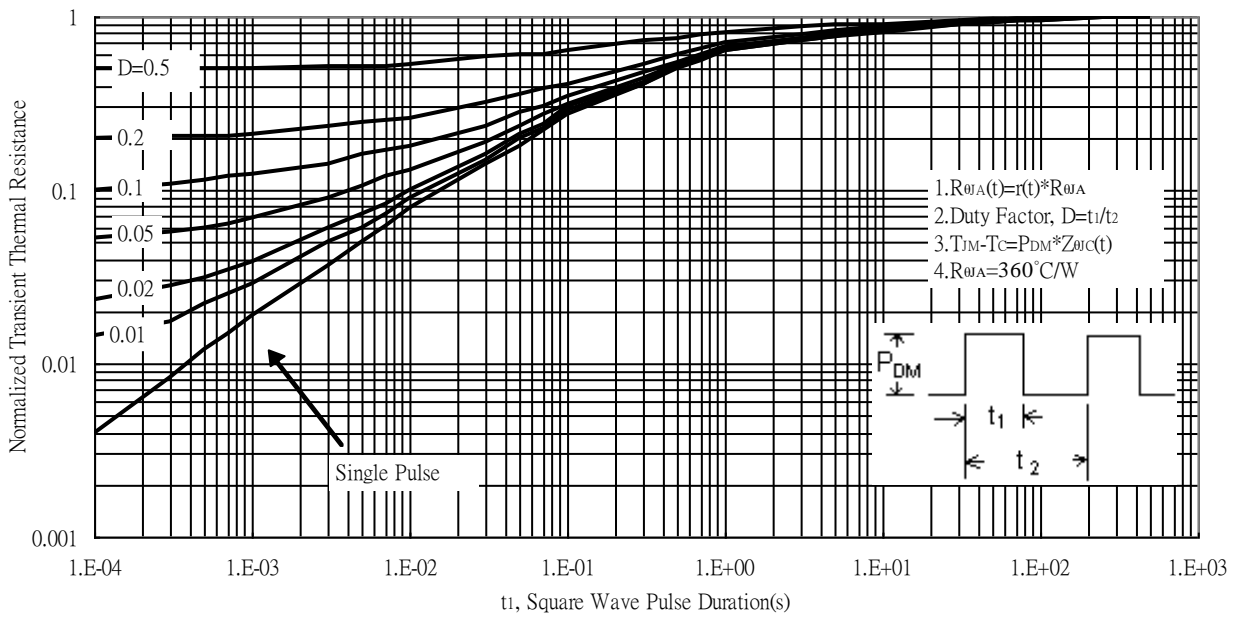
Typical Transfer Characteristics



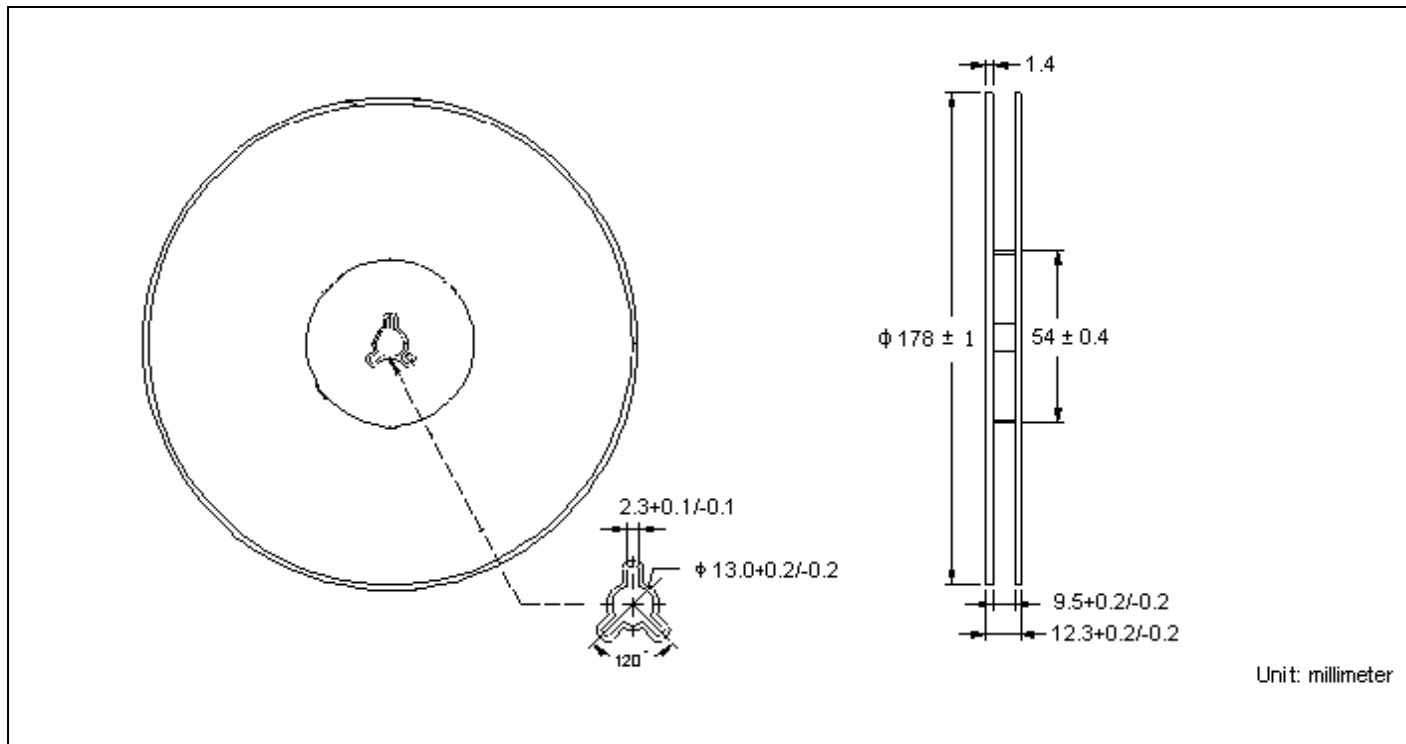
Power Derating Curve



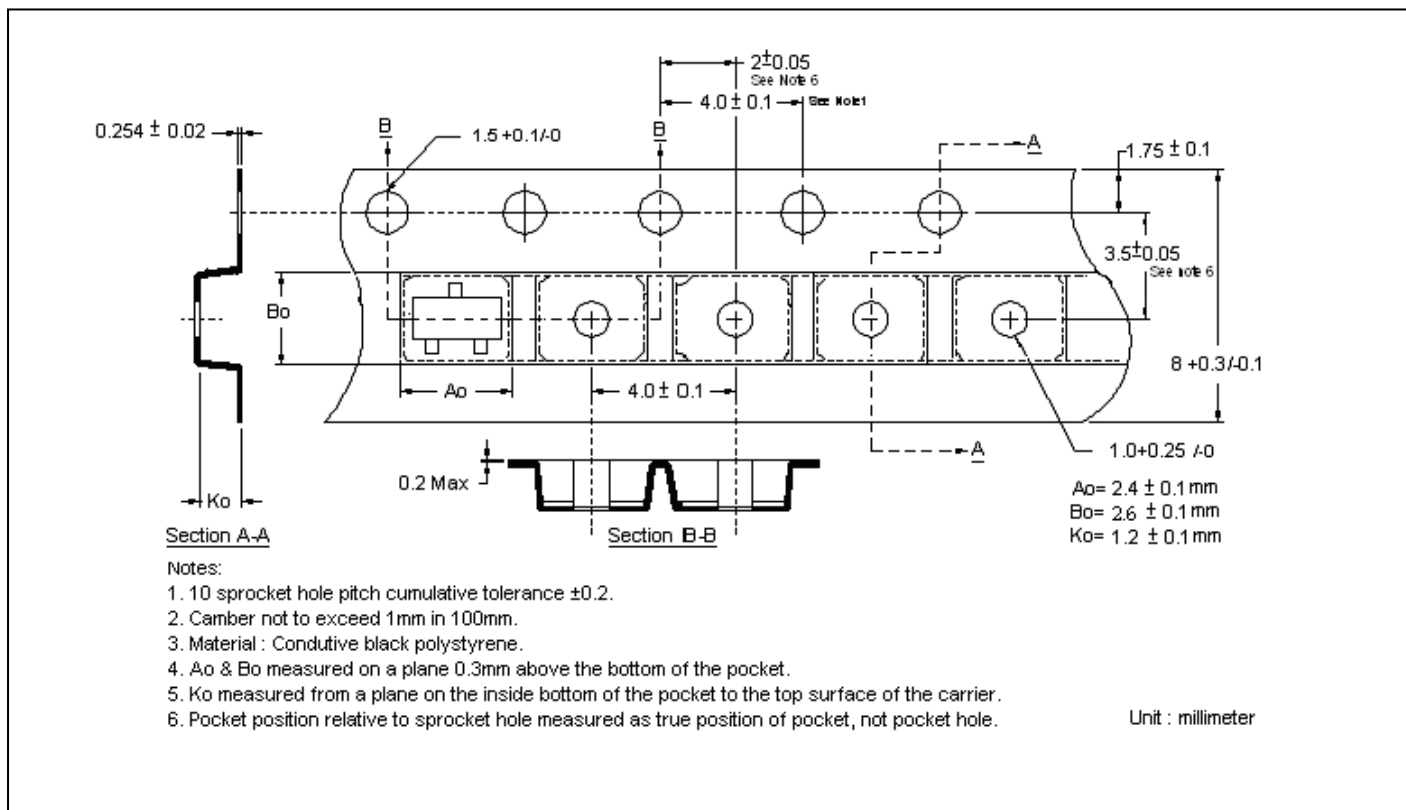
Transient Thermal Response Curves



### Reel Dimension



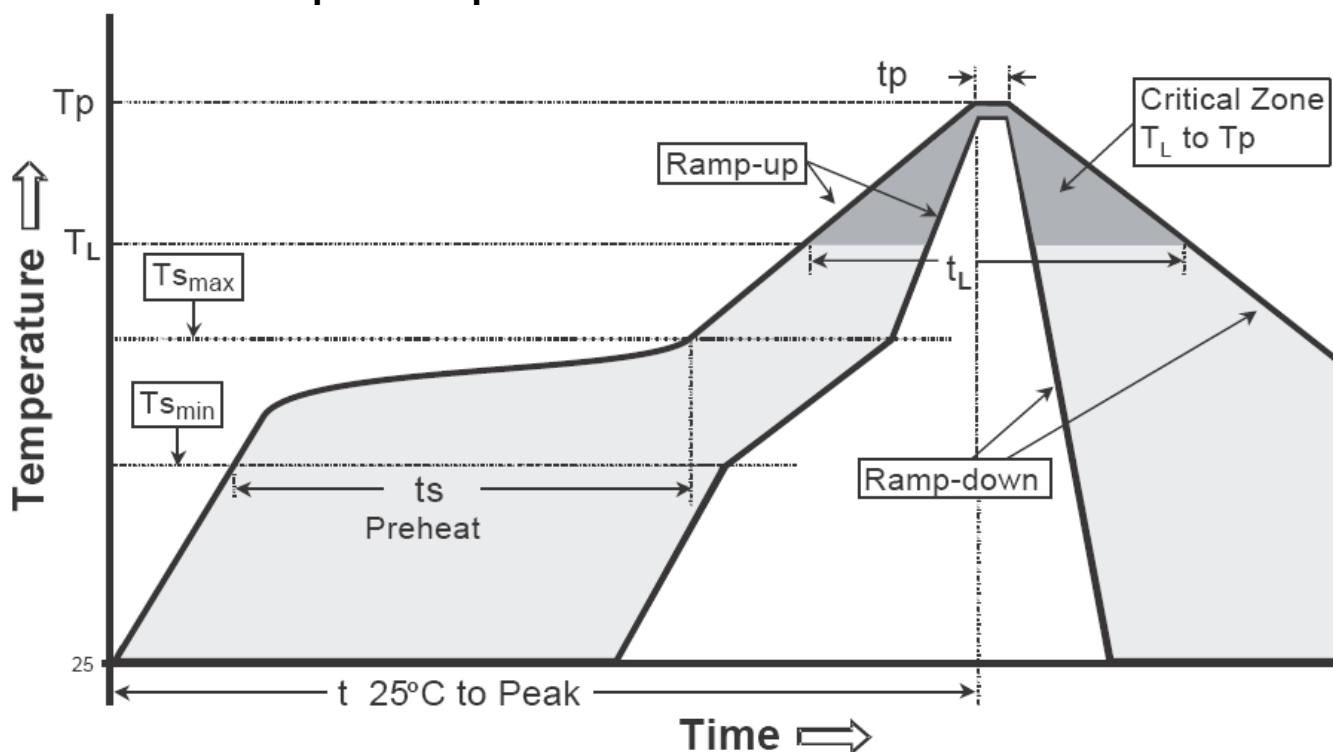
### Carrier Tape Dimension



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

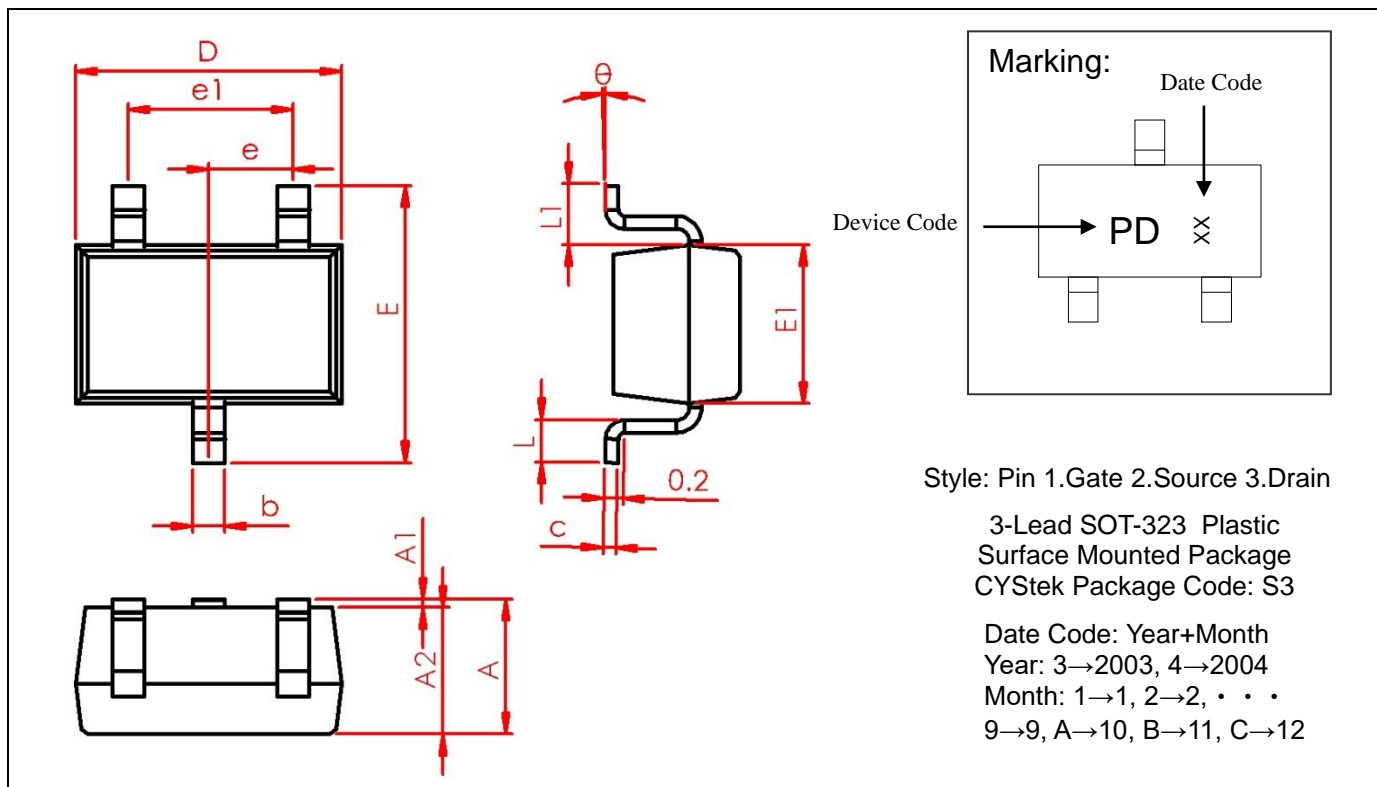
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**SOT-323 Dimension**



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.035	0.043	0.900	1.100	E1	0.045	0.053	1.150	1.350
A1	0.000	0.004	0.000	0.100	e	0.026 TYP		0.650 TYP	
A2	0.035	0.039	0.900	1.000	e1	0.047	0.055	1.200	1.400
b	0.008	0.016	0.200	0.400	L	0.010	0.018	0.260	0.460
c	0.003	0.006	0.080	0.150	L1	0.021 REF		0.525 REF	
D	0.079	0.087	2.000	2.200	θ	0°	8°	0°	8°
E	0.085	0.096	2.150	2.450					

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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