

30Amp. MOS BARRIER RECTIFIER

SKM3045CTF3

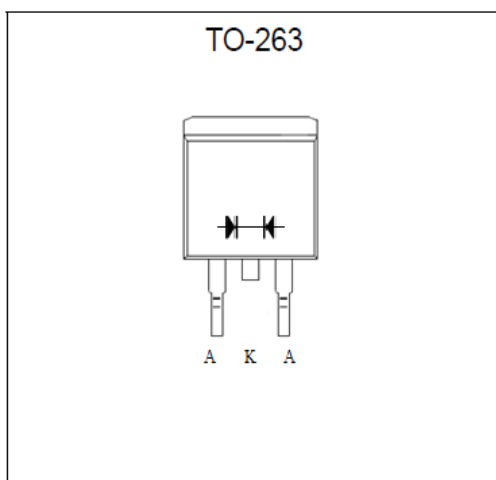
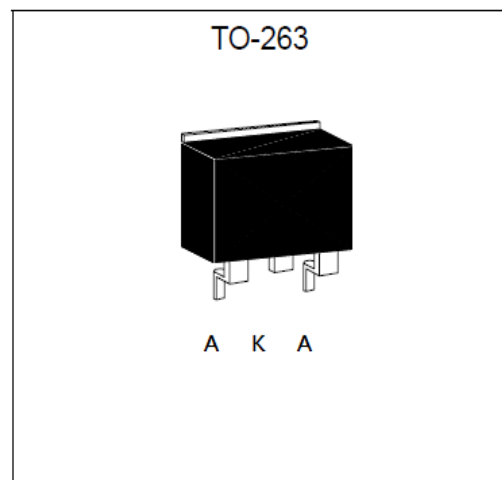
$I_{F(AV)}$	2 x 15A
V_{RRM}	45V
V_F	0.47V
T_j	150°C

Features

- 150°C operating junction temperature
- Softest, fast switching capability
- Reduced ultra-low forward voltage drop (VF) ; better efficiency and cooler operation.
- Lead-Free Finish; RoHS Compliant
- Halogen and Antimony Free. “Green” Device
- MCD technology provides a superior avalanche capability than schottky diodes

Mechanical Data

- Case: JEDEC TO-263 molded plastic
- Terminals: Pure tin plated, lead-free, solderable per MIL-STD-750 method 2026
- Epoxy: UL 94V-0 rate flame retardant
- Polarity: As marked.

Equivalent Circuit

Outline




Maximum Ratings and Electrical Characteristics

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

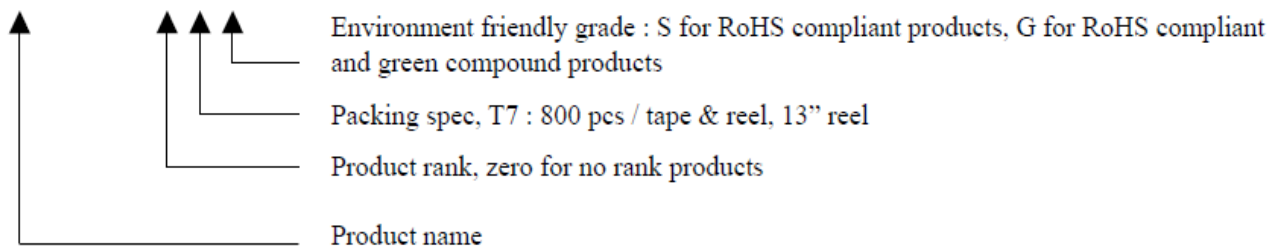
Parameter	Symbol	Min.	Typ.	Max.	Units
Maximum DC blocking voltage	V _{DC}			45	V
Maximum Recurrent peak reverse voltage	V _{RRM}			45	V
Maximum RMS voltage	V _{RMS}			32	V
Maximum instantaneous forward voltage at I _F =15A	V _F	T _C =25°C	0.47	0.54	V
		T _C =125°C	0.46		
Maximum instantaneous reverse current at	I _R	V _R =45 V, T _C =25°C	70	200	μA
		V _R =45 V, T _C =125°C	20	50	mA
Maximum Average forward rectified current @ T _C =100°C	I _{F(AV)}			15	A
Non-repetitive peak forward surge current @ 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I _{FSM}			250	A
Peak Repetitive Reverse Surge Current (2uS-1Khz)	I _{RRM}			2	A
Maximum Rate of Voltage Change (at Rated VR)	dv/dt			10000	V/uS
Storage temperature range	T _{stg}	-55		150	°C
Operating junction temperature range	T _J	-55		150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Typical Thermal Resistance, Junction-to-case	R _{th,j-c}	2	°C/W
Typical Thermal Resistance, Junction-to-ambient	R _{th,j-a}	60	°C/W

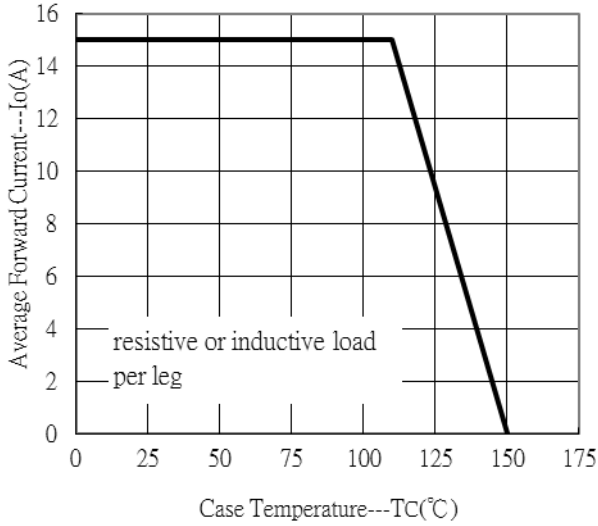
Ordering Information

Device	Package	Shipping
SKM3045CTF3-0-T7-S	TO-263 (Pb-free lead plating)	800 pcs / Tape & Reel

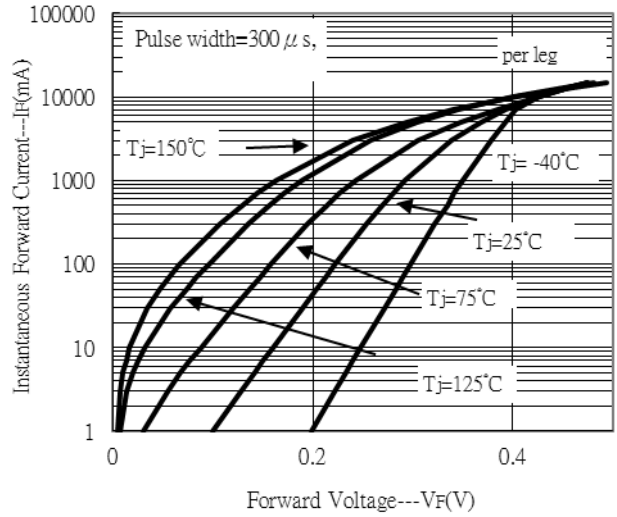


Typical Characteristics

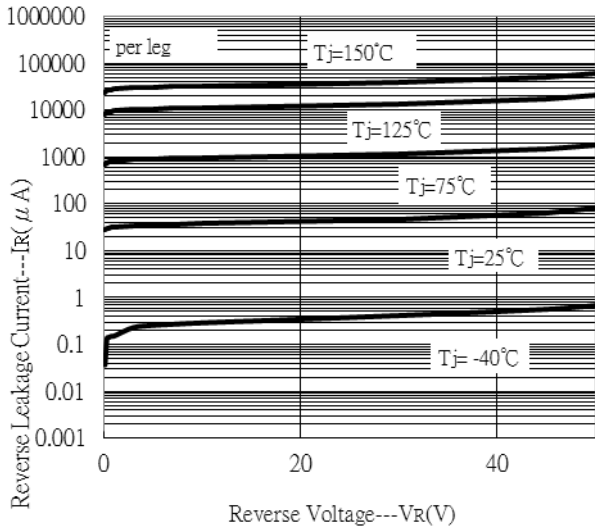
Forward Current Derating Curve



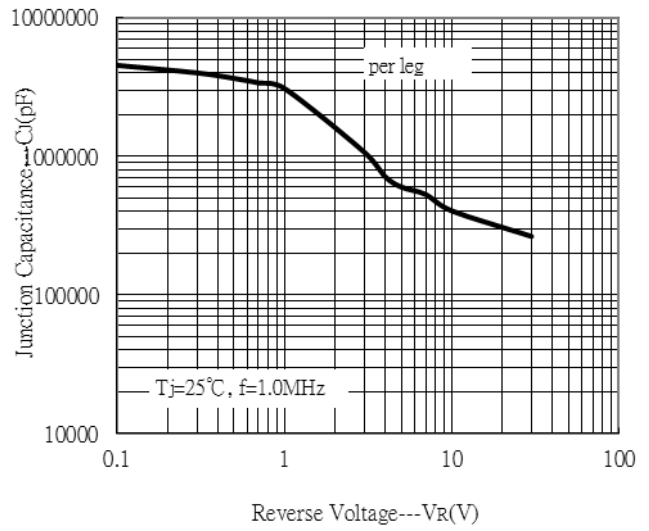
Forward Current vs Forward Voltage



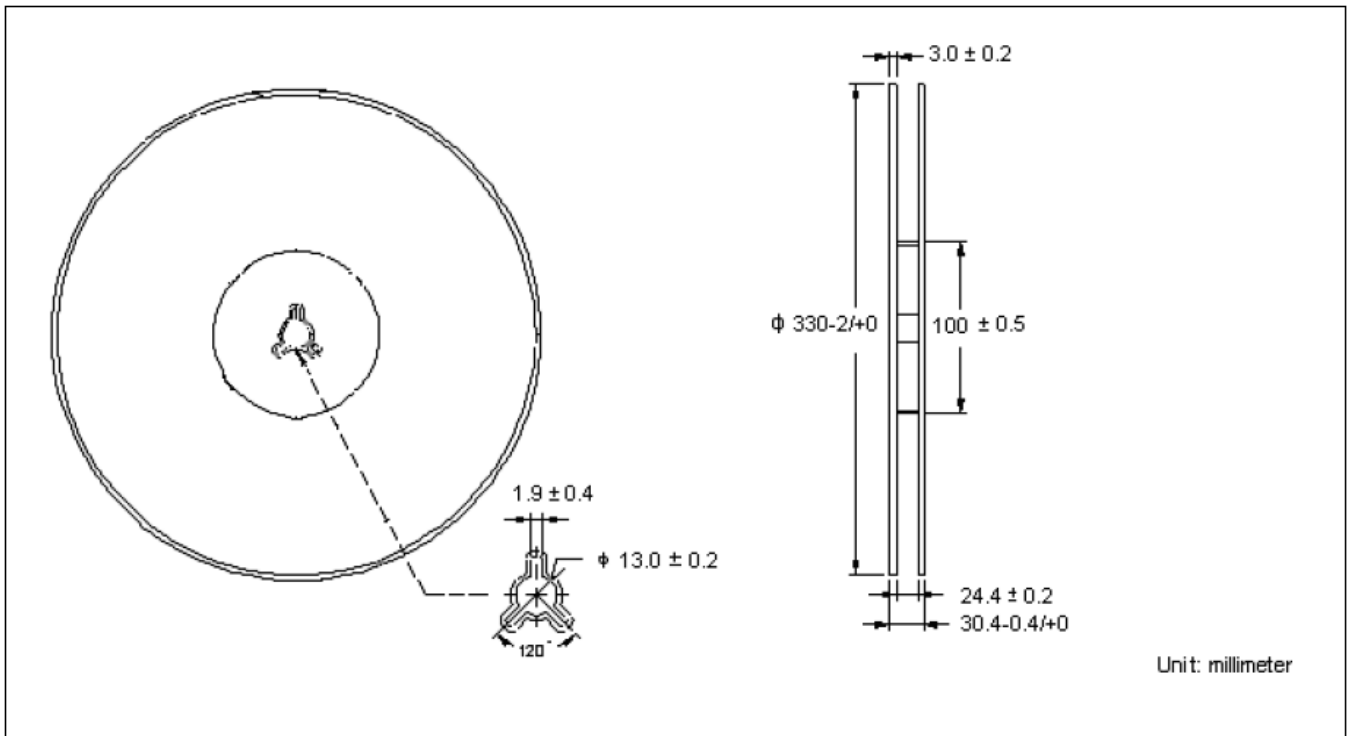
Reverse Leakage Current vs Reverse Voltage



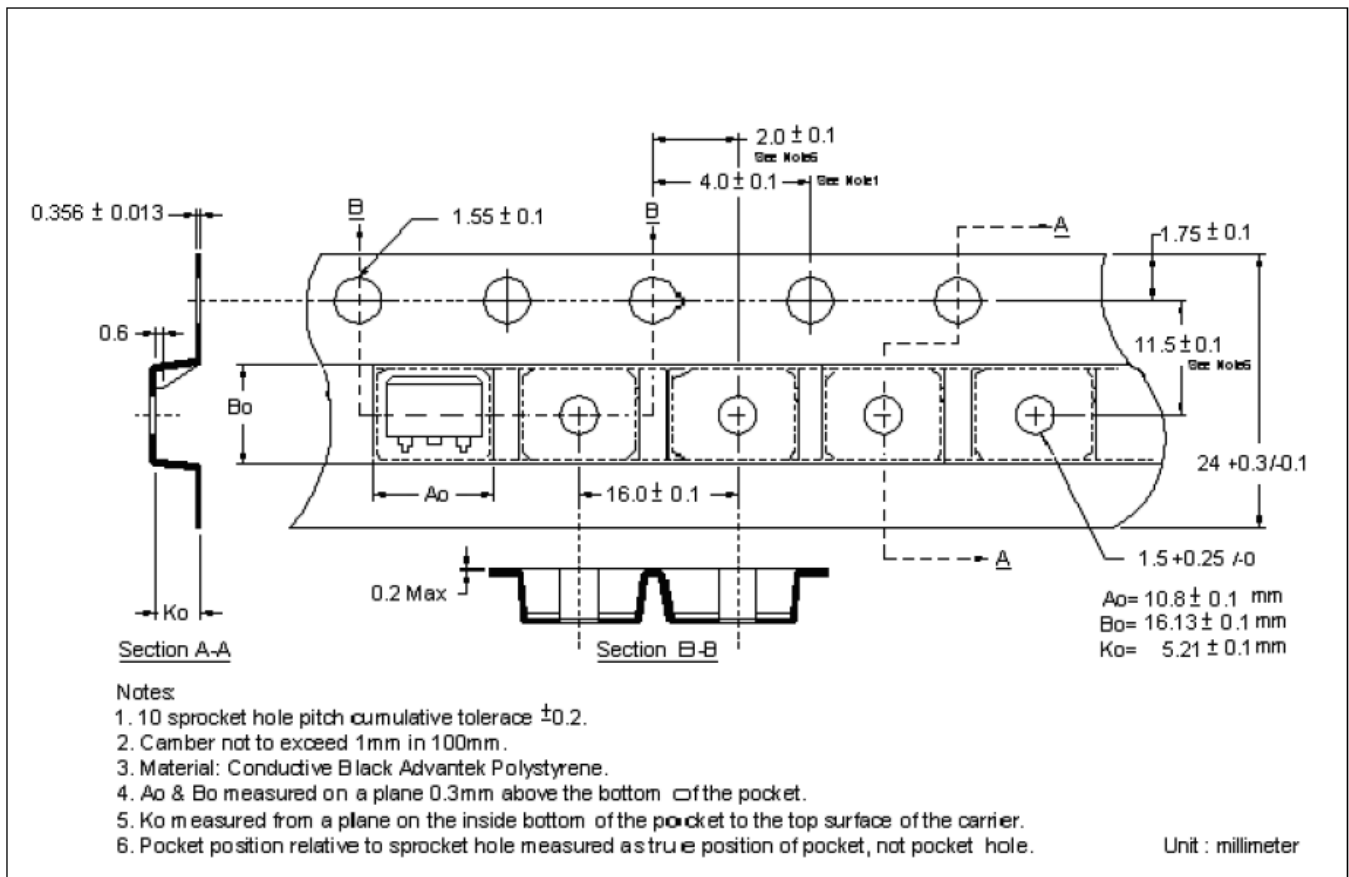
Junction Capacitance vs Reverse Voltage



Reel Dimension



Carrier Tape Dimension

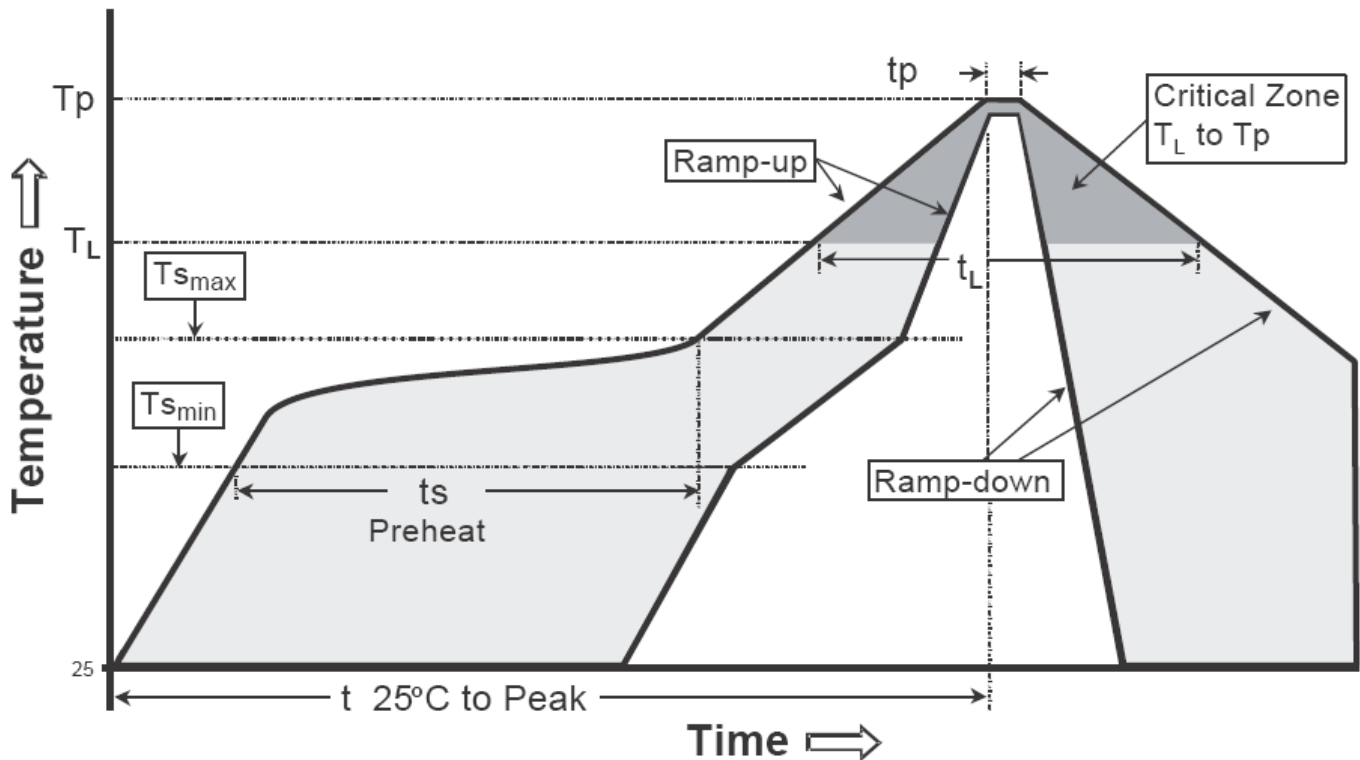


Notes:

1. 10 sprocket hole pitch cumulative tolerance ± 0.2 .
2. Camber not to exceed 1mm in 100mm.
3. Material: Conductive Black Advantek Polystyrene.
4. A_o & B_o measured on a plane 0.3mm above the bottom of the pocket.
5. K_o measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Recommended wave soldering condition

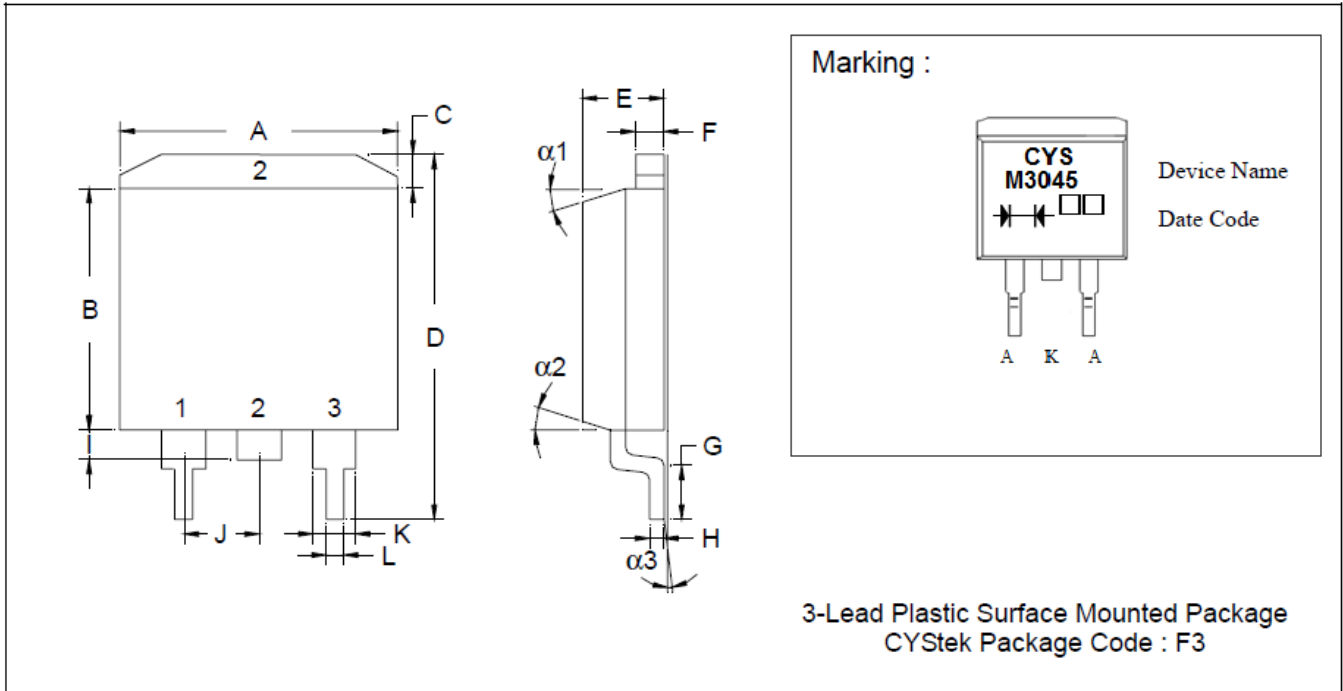
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-263 Dimension



*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.3800	0.4050	9.65	10.29	I	0.0500	0.0700	1.27	1.78
B	0.3300	0.3700	8.38	9.40	J	-	*0.1000	-	*2.54
C	-	0.0550	-	1.40	K	0.0450	0.0550	1.14	1.40
D	0.5750	0.6250	14.61	15.88	L	0.0200	0.0390	0.51	0.99
E	0.1600	0.1900	4.06	4.83	$\alpha 1$	-	-	6°	8°
F	0.0450	0.0550	1.14	1.40	$\alpha 2$	-	-	6°	8°
G	0.0900	0.1100	2.29	2.79	$\alpha 3$	-	-	0°	5°
H	0.0180	0.0290	0.46	0.74					

- Notes :**
1. Controlling dimension : millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.