

Spec. No. : C752SA Issued Date : 2006.07.13 Revised Date :2014.09.24

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### 1.0Amp. Surface Mount Schottky Barrier Diodes

# SK12SA thru SK1BSA

#### **Features**

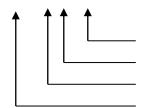
- For surface mounted applications.
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications.
- Plastic material used carries Underwriters Laboratory Flammability Classification 94V-0.
- Low leakage current.
- High surge capability.
- High temperature soldering: 250°C/10 seconds at terminals.
- Exceeds environmental standards of MIL-S-19500/228.

#### **Mechanical Data**

- Case: Molded plastic, JEDEC DO-214AC/SMA.
- Terminals: Solder plated, solderable per MIL-STD-750 method 2026.
- Polarity: Indicated by cathode band.
- Mounting position: Any.
- Weight: 0.064 gram, 0.002 ounce.

#### **Ordering Information**

9			
Device	Package	Shipping	Marking
SK12SA-0-T4-G			SK12
SK13SA-0-T4-G			SK13
SK14SA-0-T4-G	SMA		SK14
SK15SA-0-T4-G	(Pb-free lead plating and halogen-free	7500 pcs / Tape & Reel	SK15
SK16SA-0-T4-G	package)		SK16
SK18SA-0-T4-G			SK18
SK1BSA-0-T4-G			SK1B



Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, T4: 7500 pcs / tape & reel, 13" reel

Product rank, zero for no rank products

Product name



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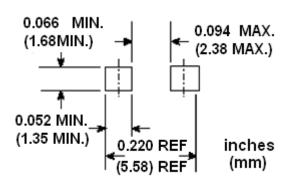
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## **Maximum Ratings and Electrical Characteristics**

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by	20%.)	1							
Parameter	Symbol Type						Units		
Farameter	Symbol	SK12	SK13	SK14	SK15	SK16	SK18	SK1B	Omis
Repetitive peak reverse voltage	Vrrm	20	30	40	50	60	80	100	V
Maximum RMS voltage	Vrms	14	21	28	35	42	56	70	V
Maximum DC blocking voltage	VR	20	30	40	50	60	80	100	V
Maximum instantaneous forward	VF		0.5		0	7	0	05	V
Voltage @ IF=1A (Note 1)	VF		0.3		0	0.7 0.85		83	V
Maximum average forward	T <sub>o</sub>								
rectified current @ TL=100°C	Io				1				A
Peak forward surge current @									
8.3ms, single half sine-wave	IFSM				30				A
superimposed on rated load	IFSM	30							Λ
(JEDEC method)									
Maximum DC reverse current at									
Rated DC blocking TJ=25°C	IR	0.5							mA
voltage TJ=100°C		10							
Typical thermal resistance,	Data	R th, JA 85				°C/W			
junction to lead	K tn, JA	65				C/ W			
Typical thermal resistance,	Rth, JC				18				°C/W
junction to case	Kui, JC				10				C/ VV
Power TA=25°C	PD	1.5			$\perp$ W				
Dissipation T <sub>C</sub> =25°C	PD	7						7 W	
Typical diode junction									
capacitance @ f=1MHz and	CJ	120				pF			
applied 4V reverse voltage									
Storage temperature	Tstg	-55 ~ +150					°C		
Operating temperature	TJ			-:	55 ~ +12	25			°C
	1								

## **Recommended soldering footprint**





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#### **Characteristic Curves**

FIG.1 - FORWARD CURRENT DERATING CURVE

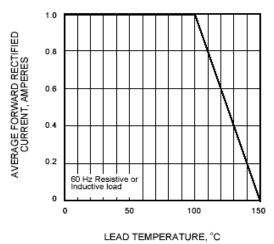


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

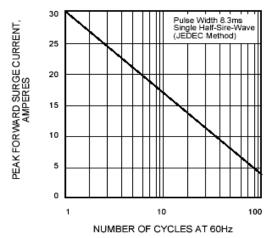


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

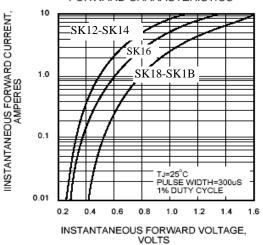
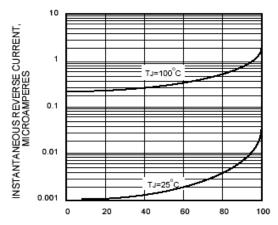
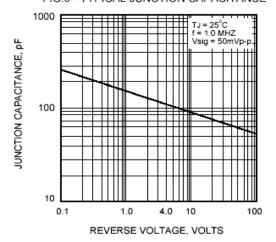


FIG.4 - TYPICAL REVERSE CHARACTERISTICS



PERCENT OF RATED PEAK REVERSE VOLTAGE,%

FIG.5 - TYPICAL JUNCTION CAPACITANCE

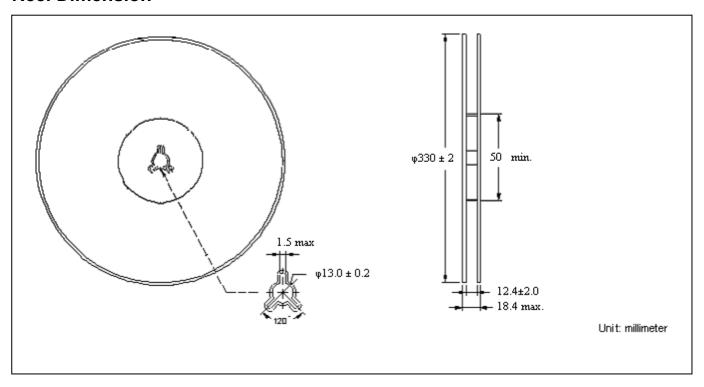




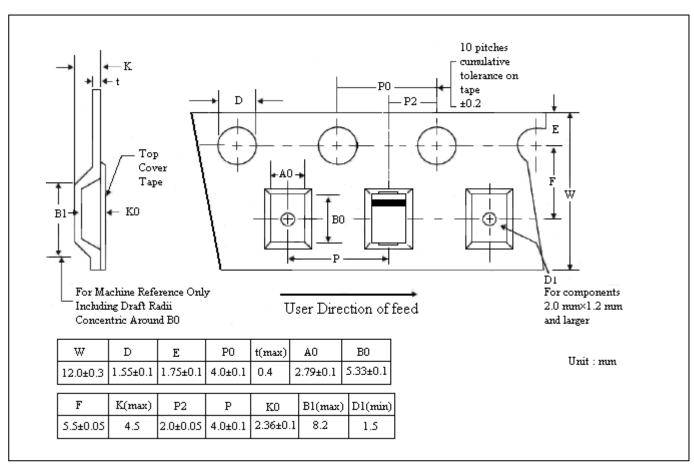
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#### **Reel Dimension**



### **Carrier Tape Dimension**





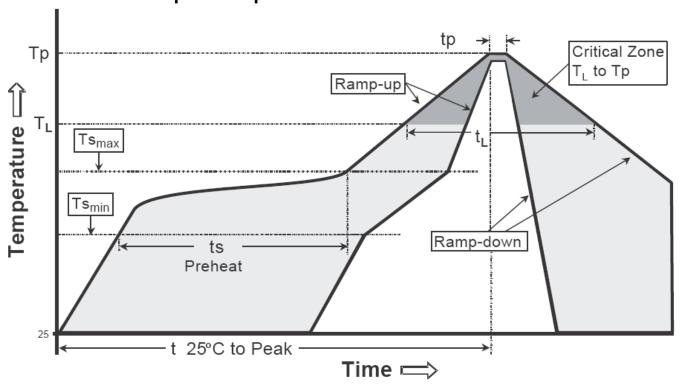
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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time		
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds		

### Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly	
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.	
Preheat			
-Temperature Min(Ts min)	100°C	150°C	
-Temperature Max(Ts max)	150°C	200°C	
-Time(ts min to ts max)	60-120 seconds	60-180 seconds	
Time maintained above:			
-Temperature (T∟)	183°C	217°C	
- Time (t∟)	60-150 seconds	60-150 seconds	
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C	
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds	
Ramp down rate	6°C/second max.	6°C/second max.	
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.	

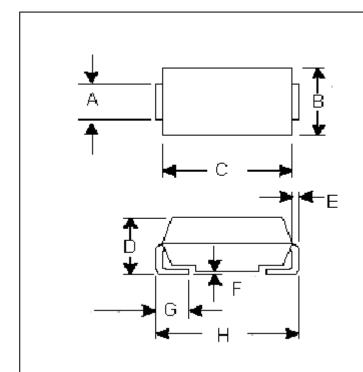
Note: All temperatures refer to topside of the package, measured on the package body surface.



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#### **SMA/DO-214AC Dimension**



Marking:			
CIZ 1 OC A	CTZ 1 2 C A	CIZ 1 AC A	

SK12SA	SK13SA	SK14SA	SK15SA
SK12	SK13	SK14	SK15

SK16SA	SK18SA	SK1BSA
SK16	SK18	SK1B

SMA/DO-214AC Plastic Surface Mounted Package CYStek Package Code : SA

\*:Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
ווועו	Min.	Max.	Min.	Max.	DIIVI	Min.	Max.	Min.	Max.
Α	0.052	0.062	1.32	1.60	Е	0.006	0.012	0.15	0.31
В	0.098	0.114	2.50	2.90	F	0.002	0.008	0.05	0.20
С	0.154	0.181	3.90	4.60	G	0.030	0.060	0.76	1.52
D	0.067	0.091	1.70	2.30	Η	0.188	0.208	4.80	5.28

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

#### Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class:UL94V-0.

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