

**P-Channel Enhancement Mode Power MOSFET**

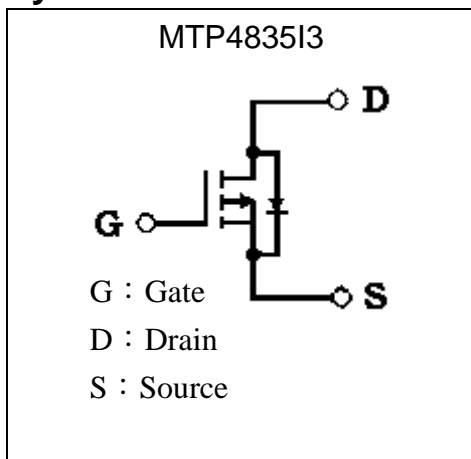
# MTP4835I3

<b>BV<sub>DSS</sub></b>	<b>-30V</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=-10V, T<sub>C</sub>=25°C</b>	<b>-40A</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=-10V, I<sub>D</sub>=-10A</b>	<b>17mΩ (typ)</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=-4.5V, I<sub>D</sub>=-6A</b>	<b>26mΩ (typ)</b>

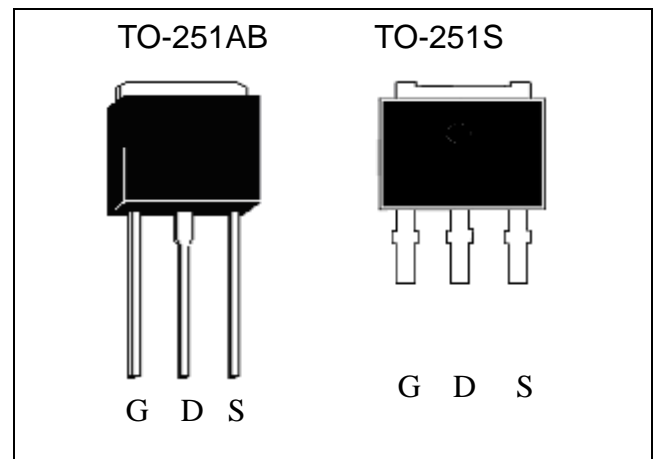
**Features**

- Single Drive Requirement
- Low On-resistance
- Fast switching Characteristic
- Pb-free lead plating package

**Symbol**

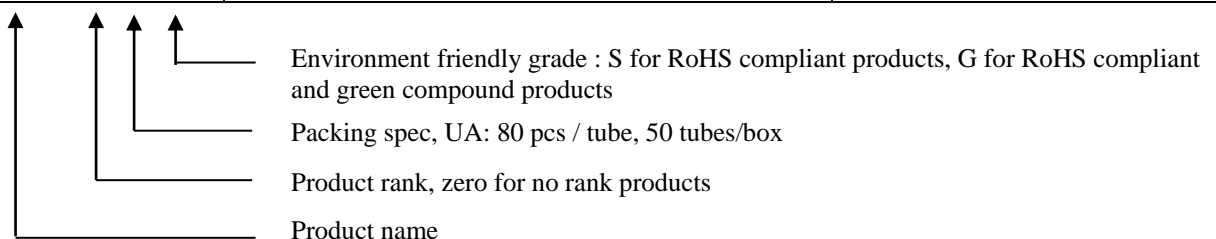


**Outline**



**Ordering Information**

Device	Package	Shipping
MTP4835I3B-0-UA-G	TO-251AB (RoHS compliant and halogen-free package)	80 pcs / tube, 50 tubes / box
MTP4835I3S-0-UA-G	TO-251S (RoHS compliant and halogen-free package)	80 pcs / tube, 50 tubes / box





**Absolute Maximum Ratings (Ta=25°C)**

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-30	V	
Gate-Source Voltage	V <sub>GS</sub>	±25		
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>C</sub> =25°C	I <sub>D</sub>	-40	A	
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>C</sub> =100°C		-25		
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =25°C		-10		
Continuous Drain Current @ V <sub>GS</sub> =-10V, T <sub>A</sub> =100°C		-6.3		
Pulsed Drain Current	I <sub>DM</sub>	-100 *1		
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C	50 *4	W
		T <sub>C</sub> =100°C	20 *4	
		T <sub>A</sub> =25°C	2.5	
		T <sub>A</sub> =100°C	1.0	
Single Pulse Avalanche Energy	E <sub>AS</sub>	25 *2	mJ	
Single Pulse Avalanche Current	I <sub>AS</sub>	-10	A	
Operating Junction and Storage Temperature	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C	

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>th,j-c</sub>	2.5	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>th,j-a</sub>	50 *3	

- Note : \*1. Pulse width limited by safe operating area.  
 \*2 . T<sub>j</sub>=25°C, V<sub>DD</sub>=-15V, L=0.5mH, R<sub>G</sub>=25Ω.  
 \*3 . The value of R<sub>th,j-a</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user's specific board design.  
 \*4 . The power dissipation P<sub>D</sub> is more useful in setting the upper dissipation limit for cases where additional heatsinking is used. It is used to determined the current rating, when this rating falls below the package limit.

**Characteristics (T<sub>j</sub>=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1.0	-1.4	-2.5		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =-250μA
G <sub>FS</sub>	-	12	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-10A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V
I <sub>DSS</sub>	-	-	-5		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>j</sub> =55°C
*R <sub>DS(ON)</sub>	-	17	25	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A
*R <sub>DS(ON)</sub>	-	26	35		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-6A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	19	-	nC	I <sub>D</sub> =-10A, V <sub>DS</sub> =-15V, V <sub>GS</sub> =-5V
*Q <sub>gs</sub>	-	8.5	-		
*Q <sub>gd</sub>	-	4.9	-		

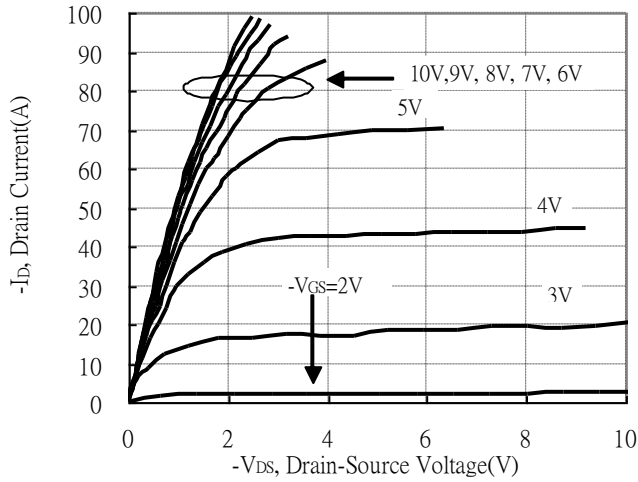


*td(ON)	-	18	-	ns	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω, I <sub>D</sub> =-1A
*tr	-	10	-		
*td(OFF)	-	56	-		
*tf	-	15	-		
Ciss	-	1718	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1MHz
Coss	-	140	-		
Crss	-	114	-		
<b>Source-Drain Diode</b>					
*V <sub>SD</sub>	-	-0.79	-1.2	V	I <sub>S</sub> =-5A, V <sub>GS</sub> =0V
*I <sub>S</sub>	-	-	-10	A	V <sub>D</sub> =V <sub>G</sub> =0V, V <sub>S</sub> =-1V
*trr	-	24	-	ns	I <sub>F</sub> =-10A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs
*Qrr	-	13	-	nC	

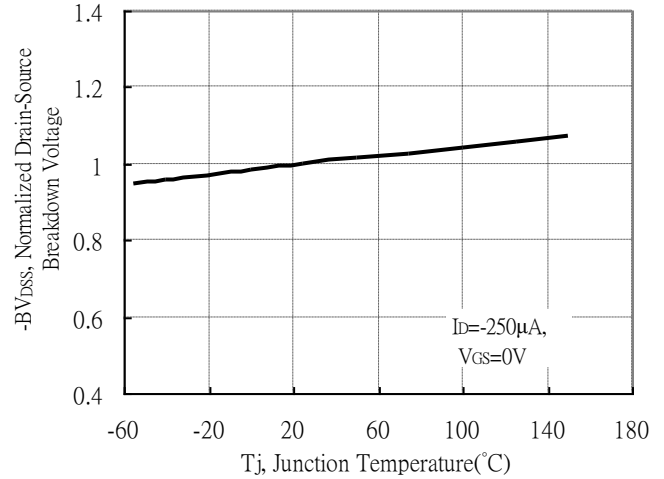
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

## Typical Characteristics

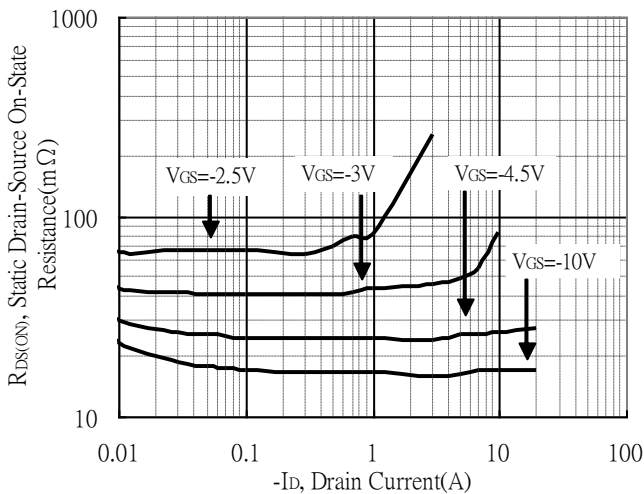
Typical Output Characteristics



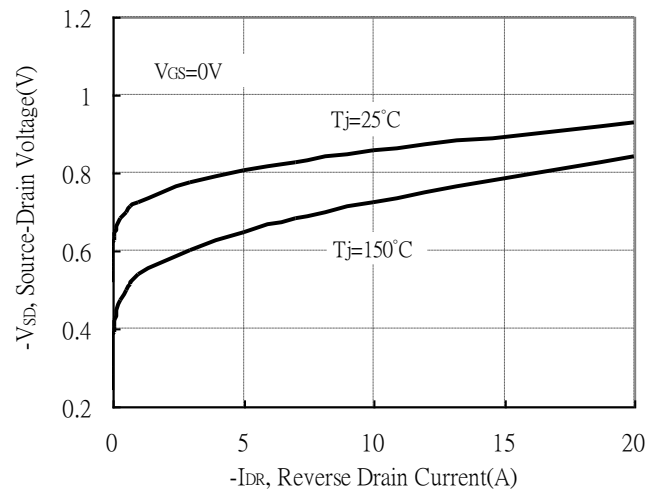
Breakdown Voltage vs Ambient Temperature



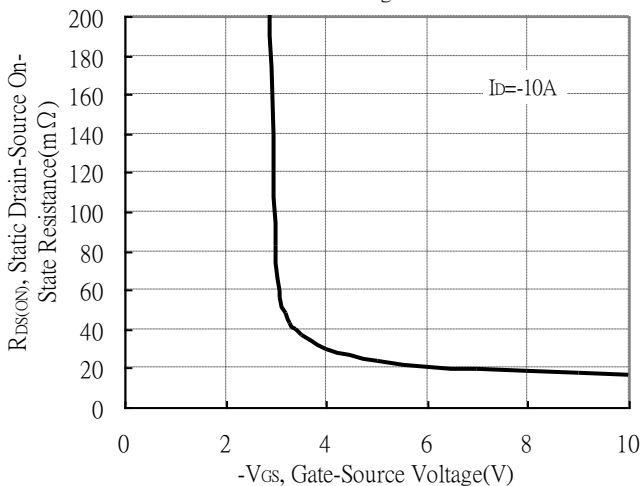
Static Drain-Source On-State resistance vs Drain Current



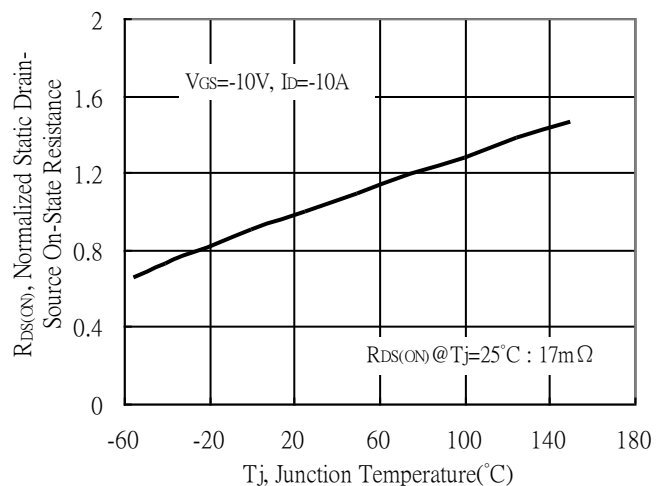
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

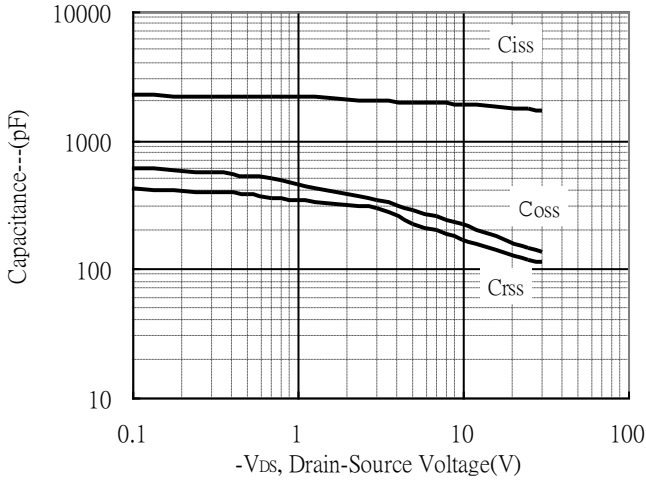


Drain-Source On-State Resistance vs Junction Temperature

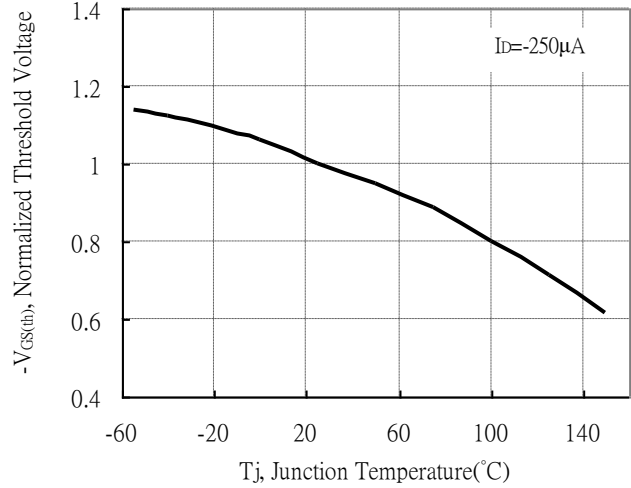


**Typical Characteristics(Cont.)**

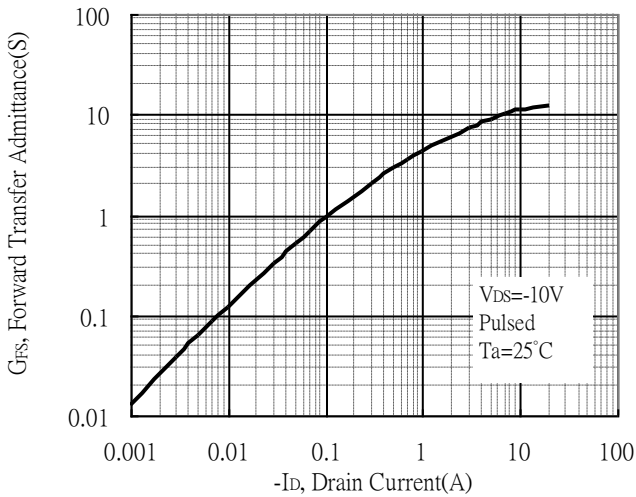
Capacitance vs Drain-to-Source Voltage



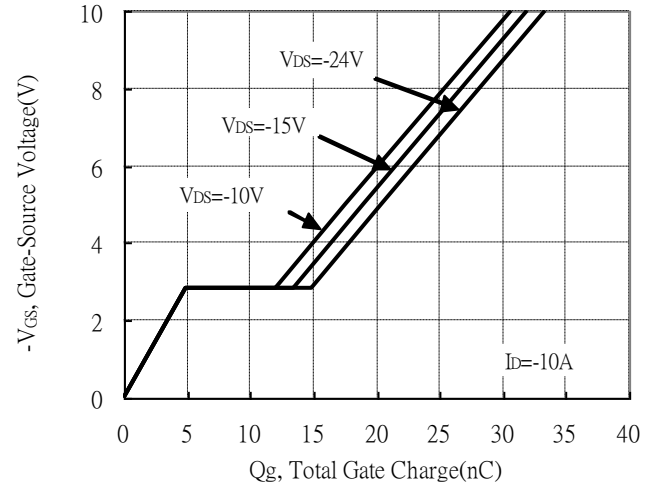
Threshold Voltage vs Junction Temperature



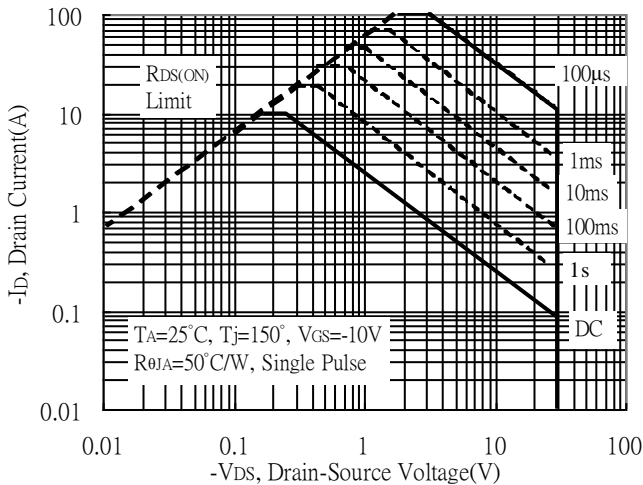
Forward Transfer Admittance vs Drain Current



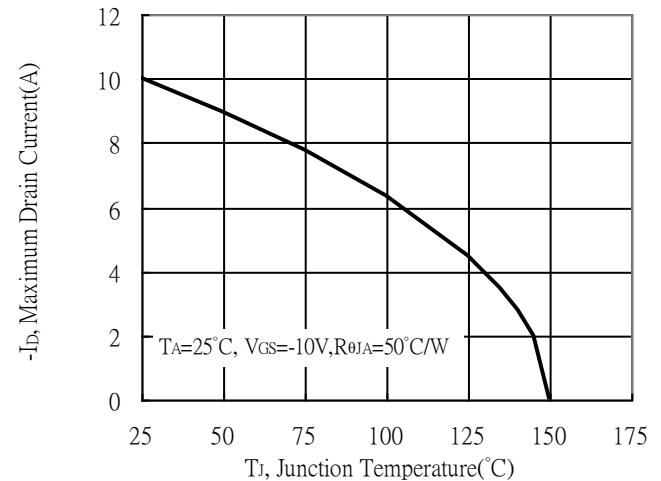
Gate Charge Characteristics



Maximum Safe Operating Area



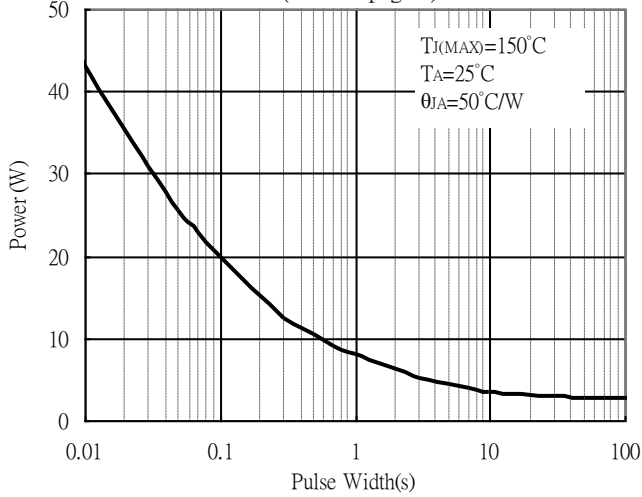
Maximum Drain Current vs Junction Temperature



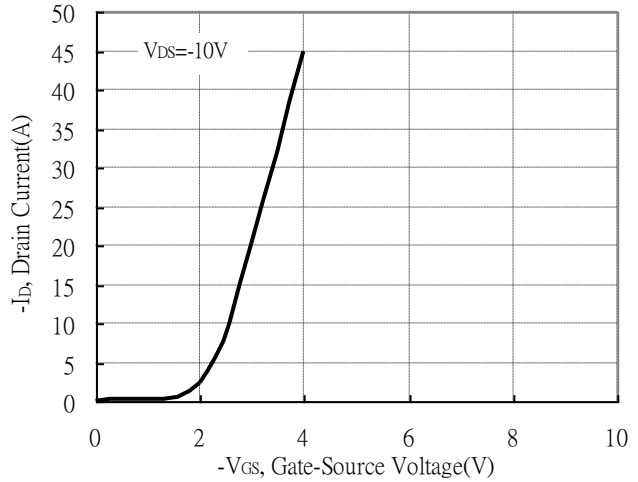


### Typical Characteristics(Cont.)

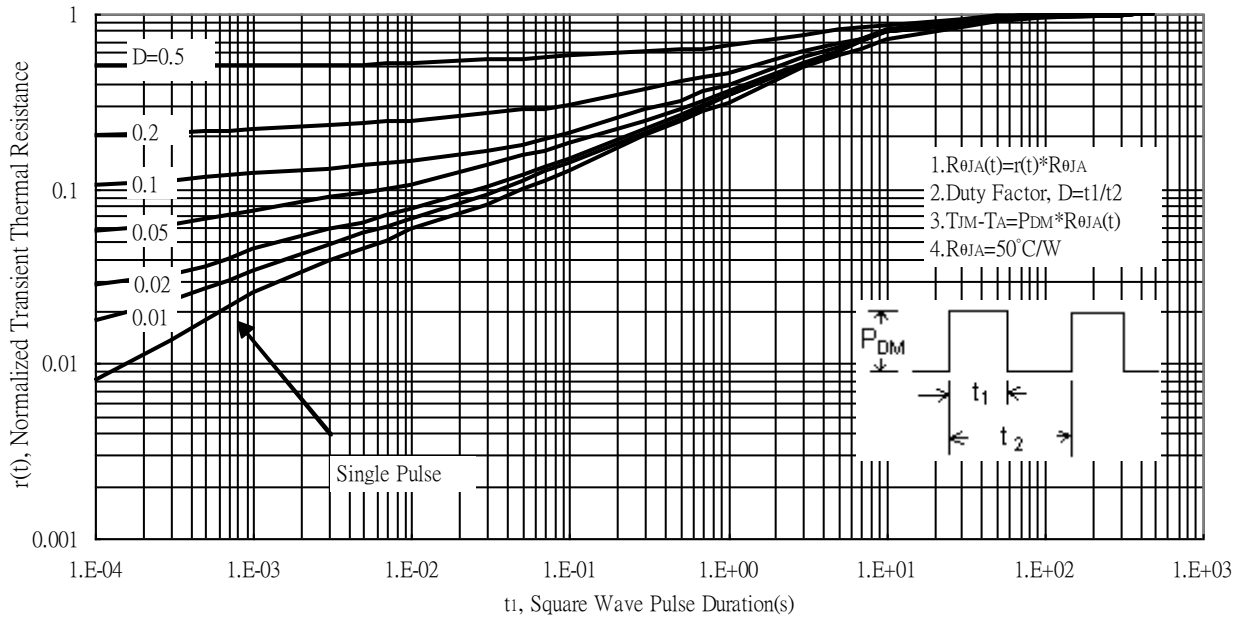
Single Pulse Power Rating, Junction to Ambient  
(Note on page 2)



Typical Transfer Characteristics



Transient Thermal Response Curves



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

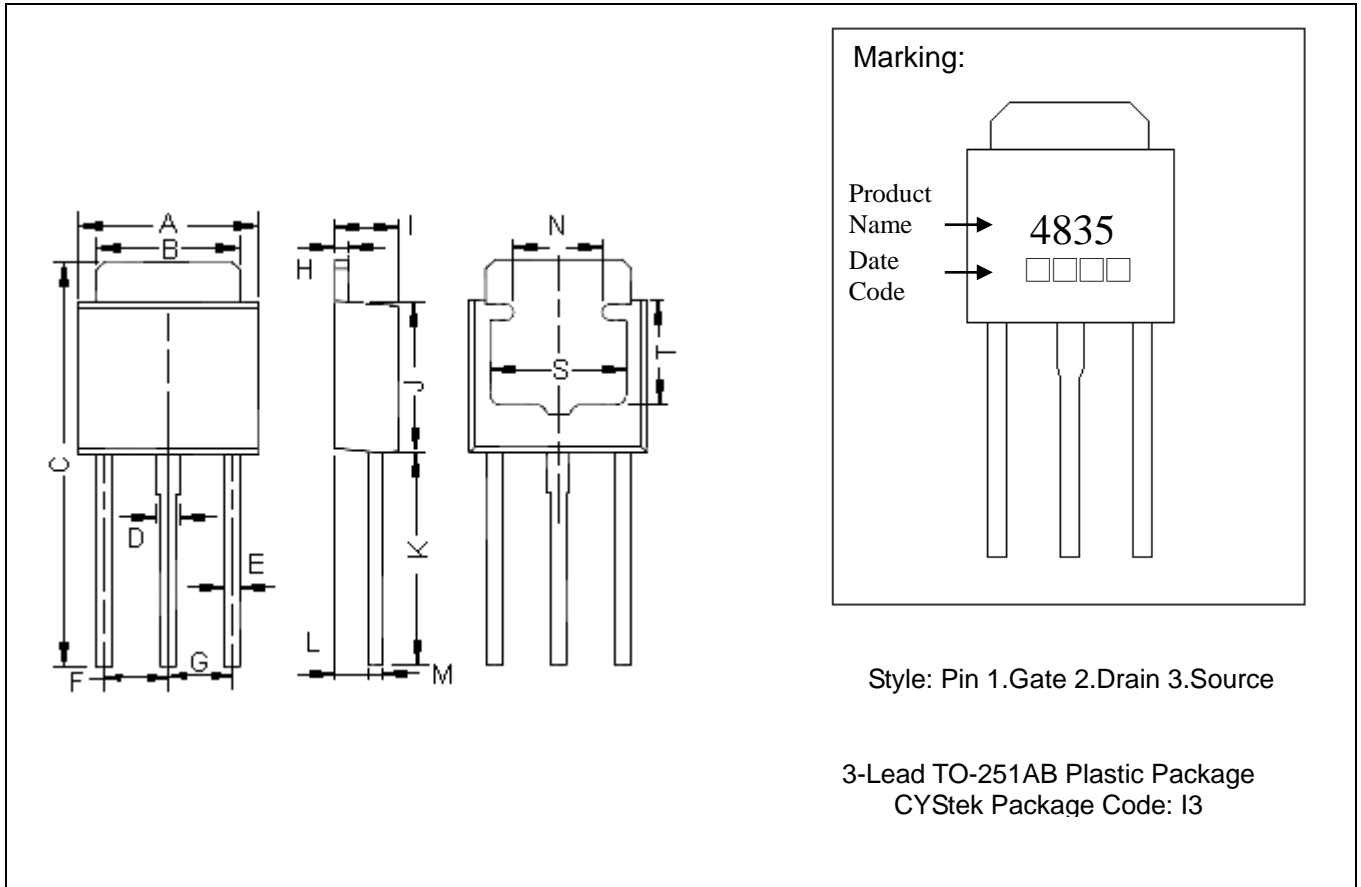
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**TO-251AB Dimension**



**Marking:**

Product Name → 4835  
 Date Code → □□□□

Style: Pin 1.Gate 2.Drain 3.Source

3-Lead TO-251AB Plastic Package  
 CYStek Package Code: I3

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.2500	0.2618	6.35	6.65	I	0.0866	0.0945	2.20	2.40
B	0.2047	0.2126	5.20	5.40	J	0.2126	0.2244	5.40	5.70
C	0.5709	0.5866	14.50	14.90	K	0.2992	0.3071	7.60	7.80
D	0.0276	0.0354	0.70	0.90	L	0.0453	0.0492	1.15	1.25
E	0.0199	0.0276	0.50	0.70	M	0.0169	0.0228	0.43	0.58
F	0.0886	0.0925	2.25	2.35	N	0.1181	REF	3.00	REF
G	0.0886	0.0925	2.25	2.35	S	0.1969	REF	5.00	REF
H	0.0169	0.0228	0.43	0.58	T	0.1496	REF	3.80	REF

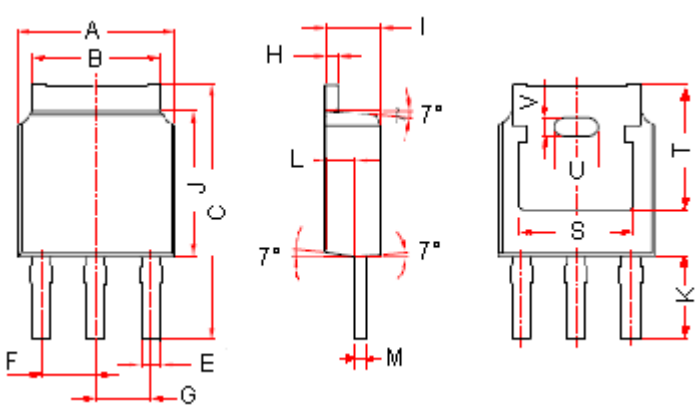
**Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

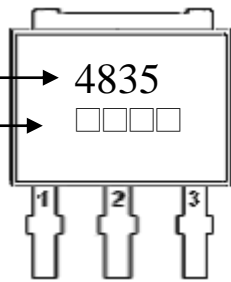


**TO-251S Dimension**



The diagram shows three views of the TO-251S package: a top view with dimensions A, B, C, E, F, G, J, K; a side view with dimensions H, I, L, M, and 7° lead angle; and a bottom view with dimensions S, T, U, V, X, Y.

**Marking :**



Device Name → 4835  
 Date Code → □□□□

1 2 3

3-Lead TO-251S Plastic Package  
 CYStek Package Code: I3

Style : Pin 1. Gate 2. Drain 3. Source

\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.2559	0.2638	6.50	6.70	J	0.2362	0.2441	6.00	6.20
B	0.2020	0.2126	5.13	5.46	K	0.1299	0.1457	3.30	3.70
C	0.4094	0.4331	10.40	11.00	L	0.0358	0.0437	0.91	1.11
E	0.0280	0.0319	0.71	0.81	M	0.0181	0.0220	0.46	0.56
F	0.0858	0.0941	2.18	2.39	S	0.1902	REF	4.83	REF
G	0.0858	0.0941	2.18	2.39	T	0.2106	REF	5.35	REF
H	0.0181	0.0220	0.46	0.56	U	0.0701	REF	1.78	REF
I	0.0902	0.0937	2.29	2.38	V	0.0299	REF	0.76	REF

**Notes:** 1. Controlling dimension: inch.  
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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