

N-Channel Enhancement Mode MOSFET

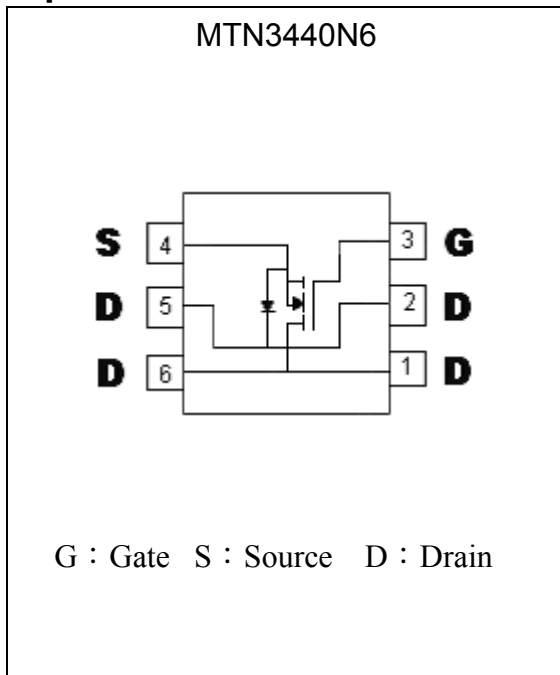
MTN3440N6

BV_{DSS}		150V
$I_D@V_{GS}=10V, T_A=25^\circ C$		1.7A
$I_D@V_{GS}=10V, T_A=70^\circ C$		1.4A
$I_D@V_{GS}=10V, T_C=25^\circ C$		2.2A
$I_D@V_{GS}=10V, T_C=70^\circ C$		1.8A
$R_{DSON(TYP)}$	$V_{GS}=10V, I_D=1.5A$	245mΩ
	$V_{GS}=6V, I_D=1.5A$	270mΩ

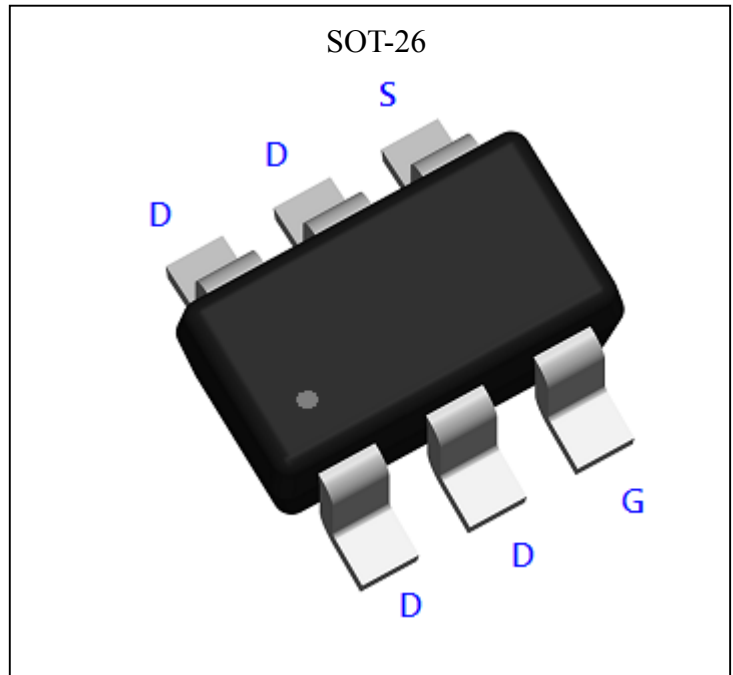
Features

- Simple drive requirement
- Low on-resistance
- Small package outline
- Pb-free lead plating package

Equivalent Circuit

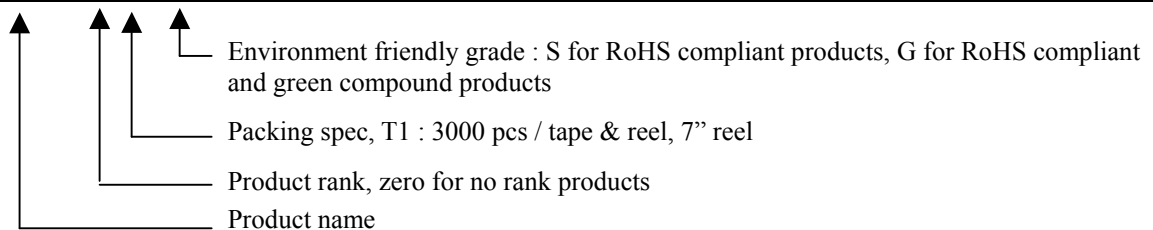


Outline



Ordering Information

Device	Package	Shipping
MTN3440N6-0-T1-G	SOT-26 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	150	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current	T _C =25 °C	I _D	2.2	A
	T _C =70 °C		1.8	
	T _A =25 °C (Note 1)		1.7	
	T _A =70 °C (Note 1)		1.4	
Pulsed Drain Current (Note 2, 3)		I _{DM}	8	
Total Power Dissipation	T _C =25 °C	P _D	3.2	W
	T _C =70 °C		2.1	
	T _A =25 °C		2	
	T _A =70 °C		1.25	
Operating Junction Temperature and Storage Temperature Range		T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	39	°C/W
Thermal Resistance, Junction-to-ambient, max (Note 1)	R _{θJA}	62.5	

Note : 1.Surface mounted on 1 in² copper pad of FR-4 board, t≤5 sec. 156°C/W when mounted on minimum copper pad.
 2.Pulse width limited by maximum junction temperature.
 3.Pulse Width ≤300μs, Duty Cycle≤2%

Electrical Characteristics (Ta=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	150	-	-	V	V _{GS} =0, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.1	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	2	3.3	4	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0
I _{DSS}	-	-	100		V _{DS} =120V, V _{GS} =0, T _j =25°C
	-	-	10	μA	V _{DS} =120V, V _{GS} =0, T _j =55°C
*R _{DS(ON)}	-	245	320	mΩ	I _D =1.5A, V _{GS} =10V
	-	270	350		I _D =1.5A, V _{GS} =6V
*G _{FS}	-	2.5	-	S	V _{DS} =15V, I _D =1A
Dynamic					
C _{iss}	-	283	-	pF	V _{DS} =30V, V _{GS} =0, f=1MHz
C _{oss}	-	30	-		
C _{rss}	-	12	-		

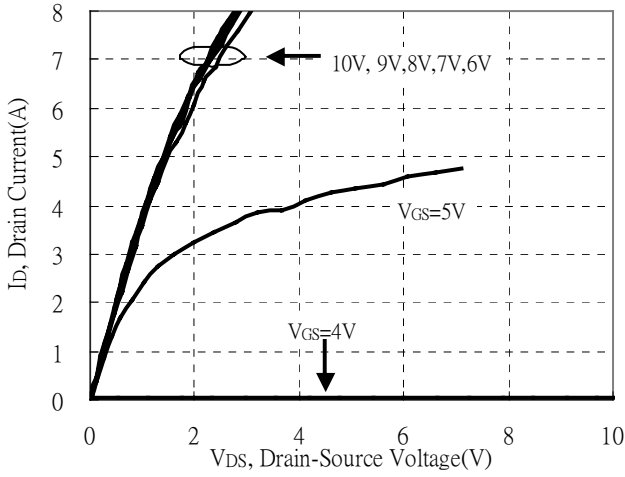


t _{d(ON)}	-	12	-	ns	V _{DS} =75V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r	-	16	-		
t _{d(OFF)}	-	32	-		
t _f	-	17	-		
Q _g	-	7.5	-	nC	V _{DS} =75V, I _D =1.7A, V _{GS} =10V,
Q _{gs}	-	1.4	-		
Q _{gd}	-	2	-		
Source-Drain Diode					
*I _S	-	-	1.7	A	
*I _{SM}	-	-	5		
*V _{SD}	-	0.78	1.2	V	I _S =1.7A, V _{GS} =0V
*t _{rr}	-	45	-	ns	I _S =1.7A, V _{GS} =0V, dI/dt=100A/μs
Q _{rr}	-	16	-	nC	

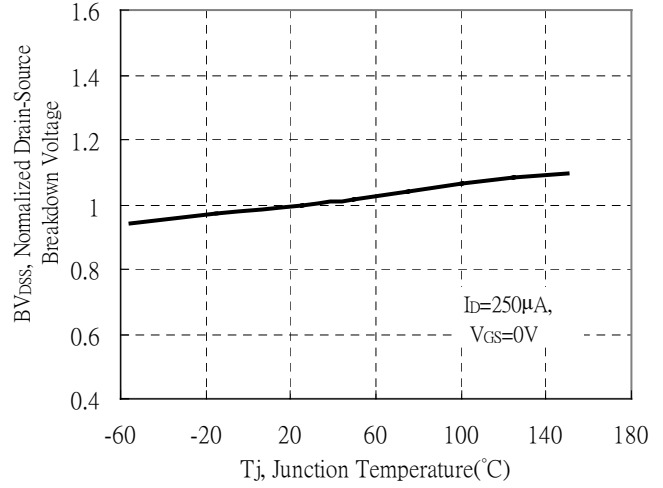
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

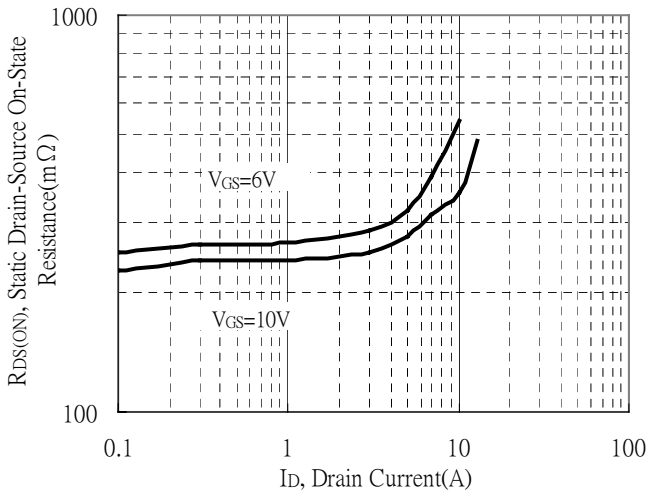
Typical Output Characteristics



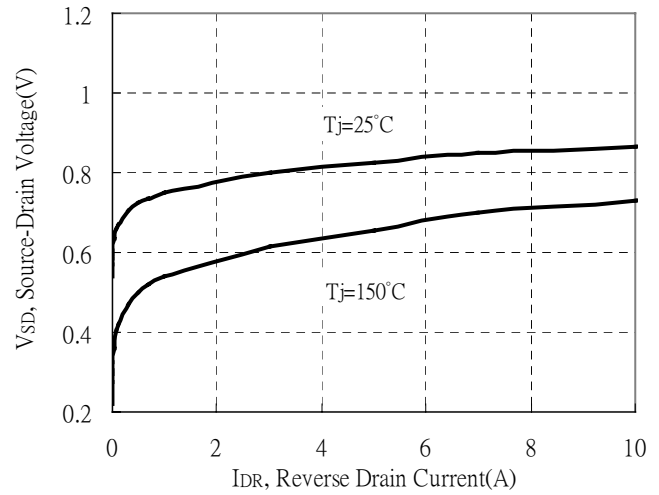
Brekdown Voltage vs Ambient Temperature



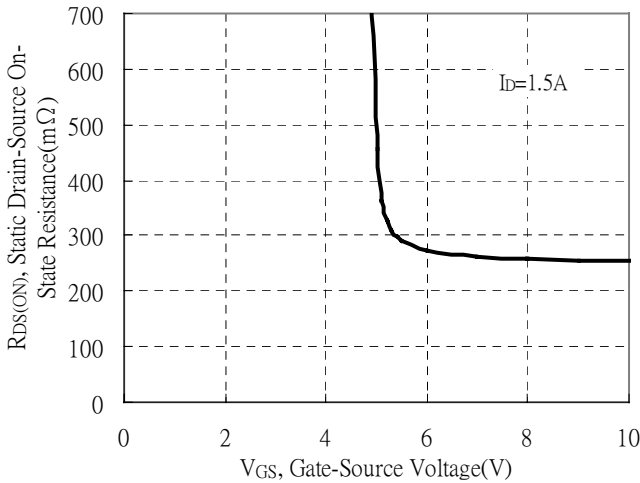
Static Drain-Source On-State resistance vs Drain Current



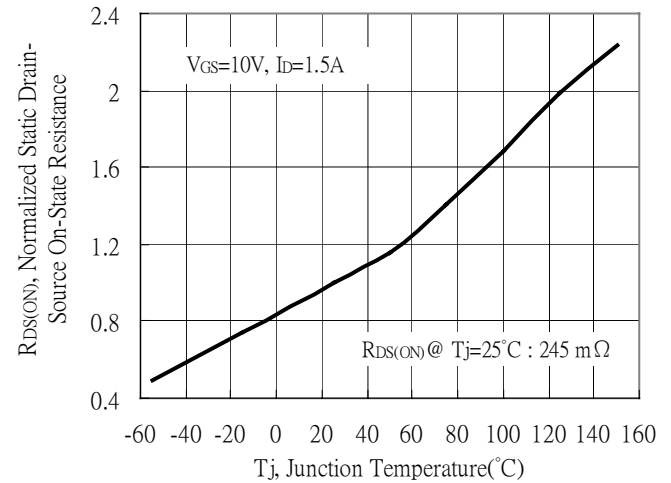
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

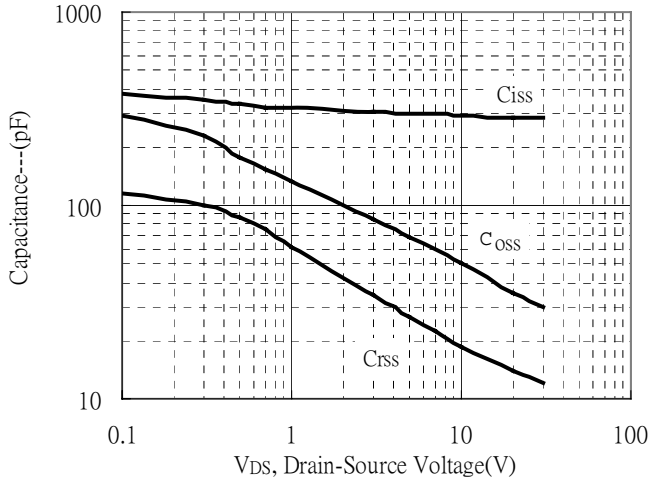


Drain-Source On-State Resistance vs Junction Temperature

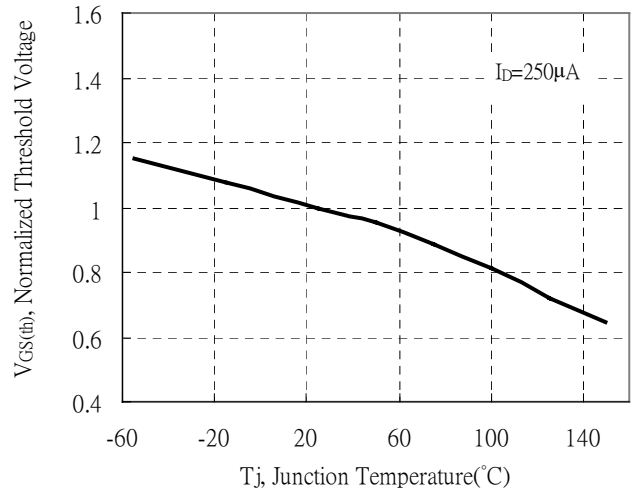


Typical Characteristics(Cont.)

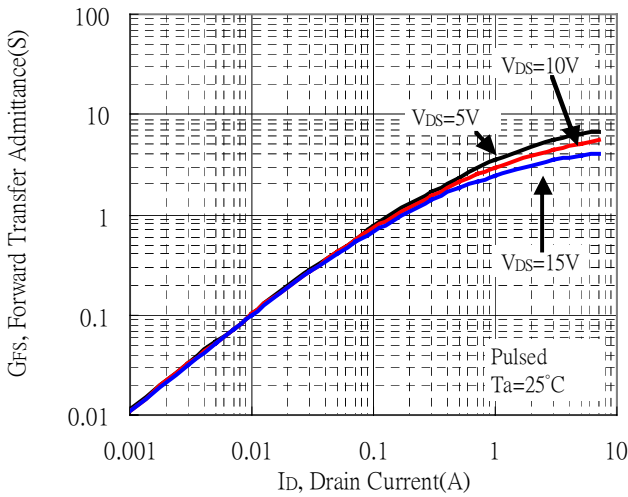
Capacitance vs Drain-to-Source Voltage



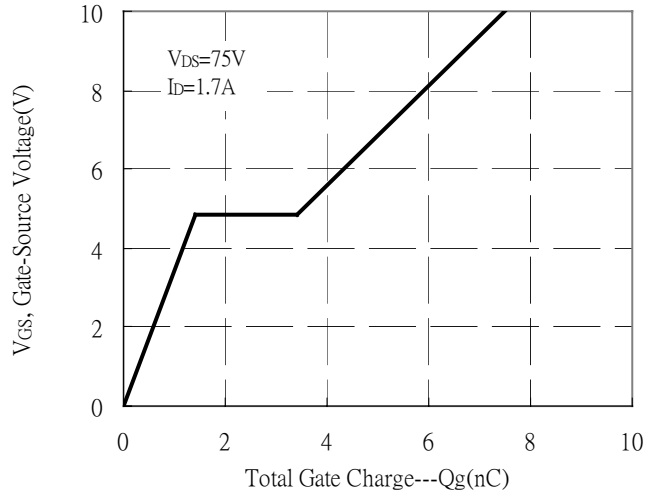
Threshold Voltage vs Junction Temperature



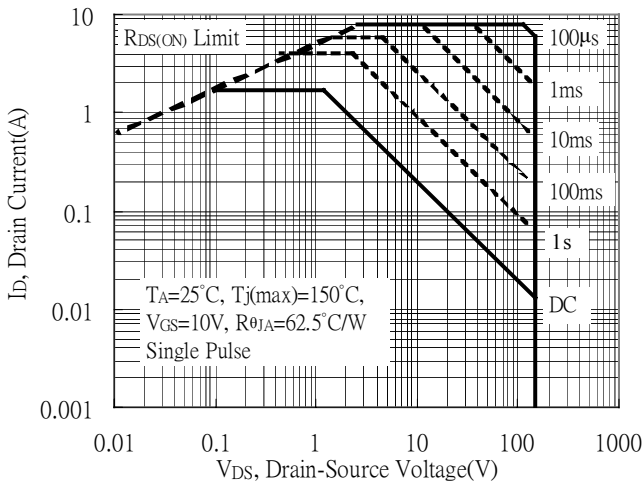
Forward Transfer Admittance vs Drain Current



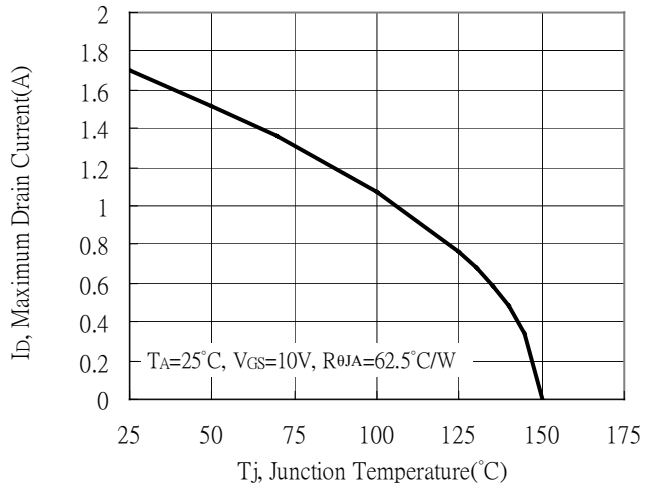
Gate Charge Characteristics



Maximum Safe Operating Area

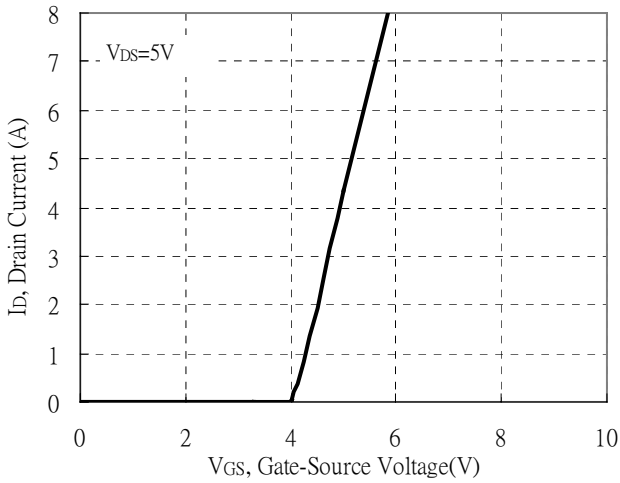


Maximum Drain Current vs Junction Temperature

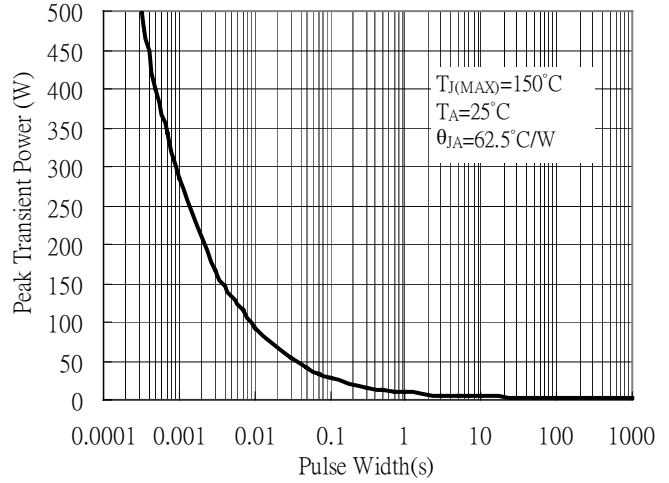


Typical Characteristics(Cont.)

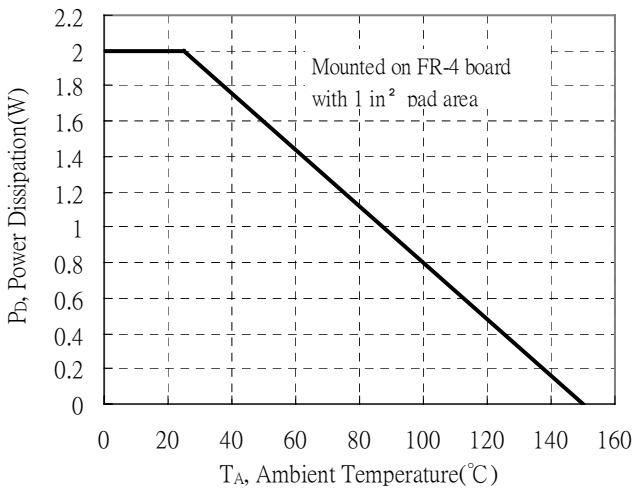
Typical Transfer Characteristics



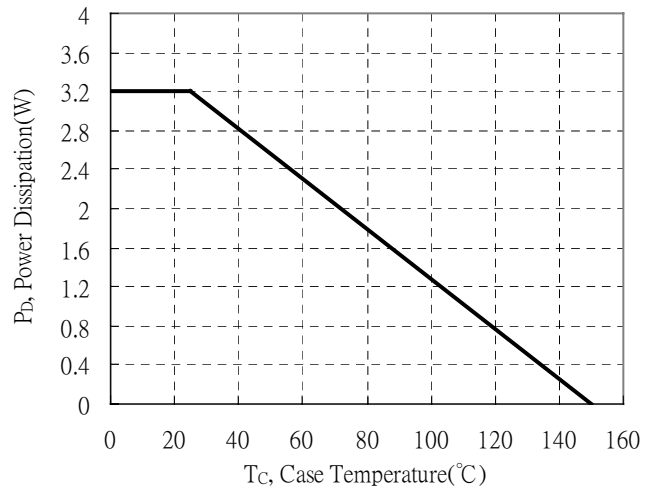
Single Pulse Maximum Power Dissipation



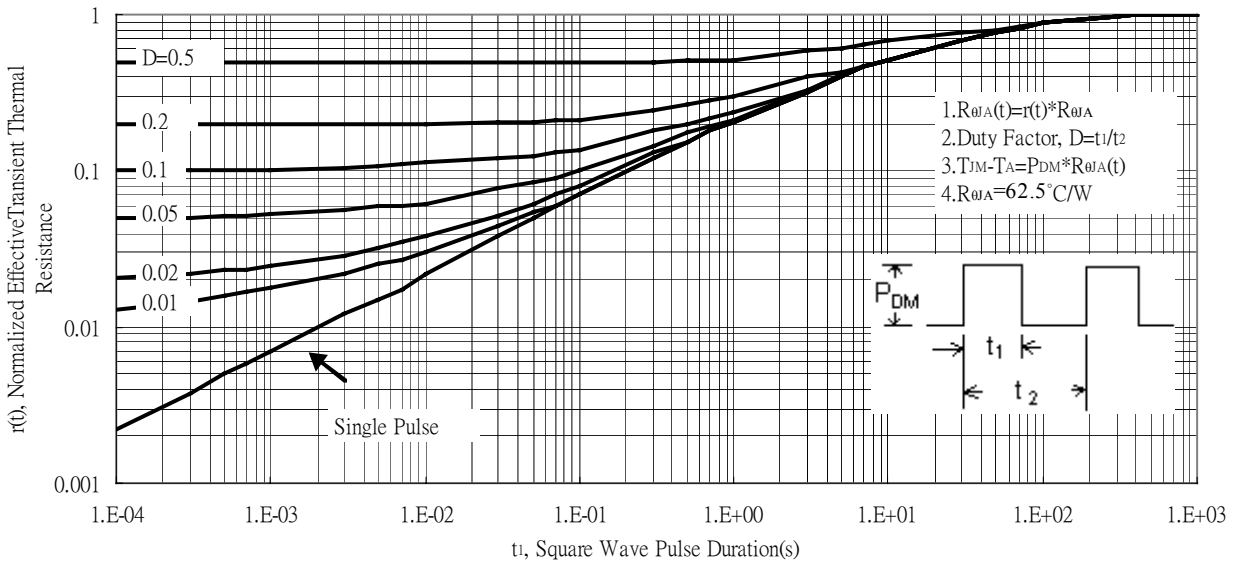
Power Derating Curve



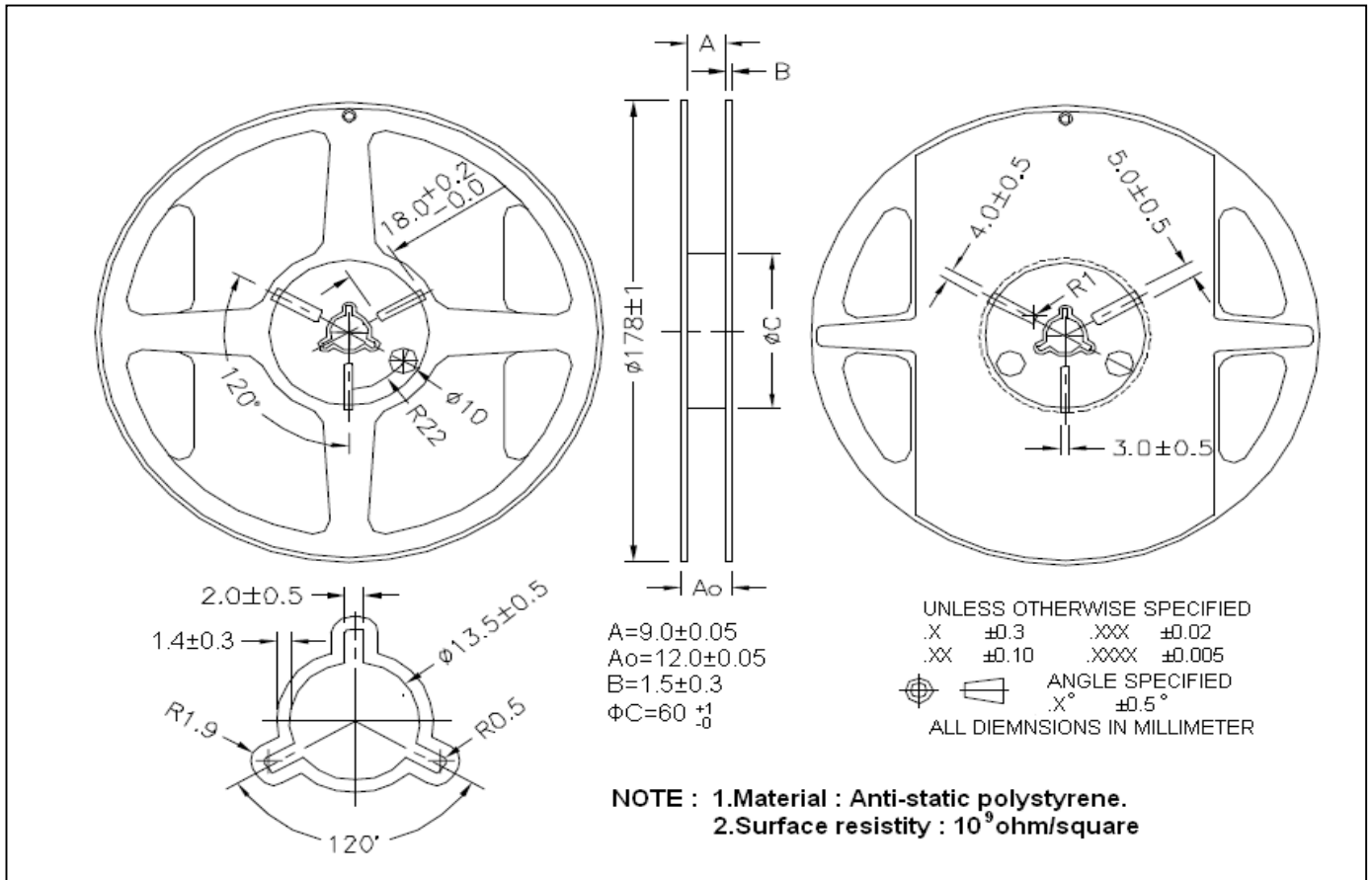
Power Derating Curve



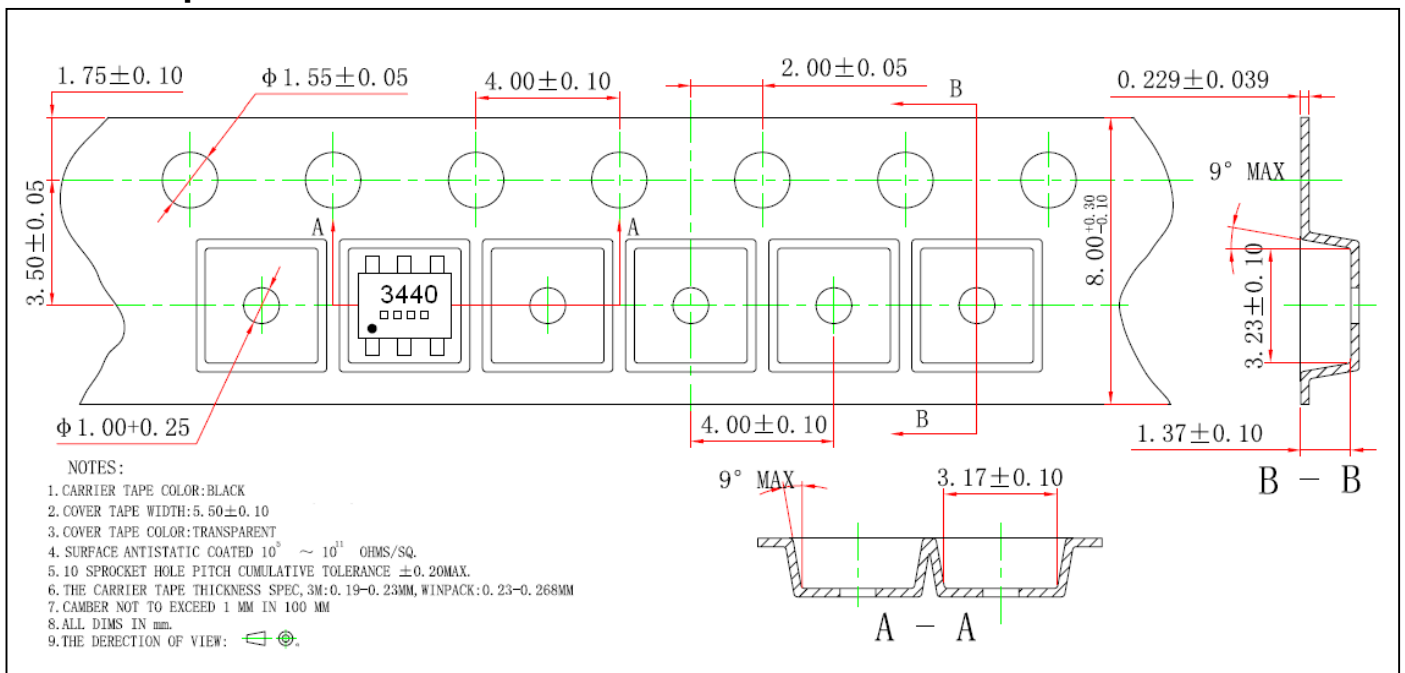
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

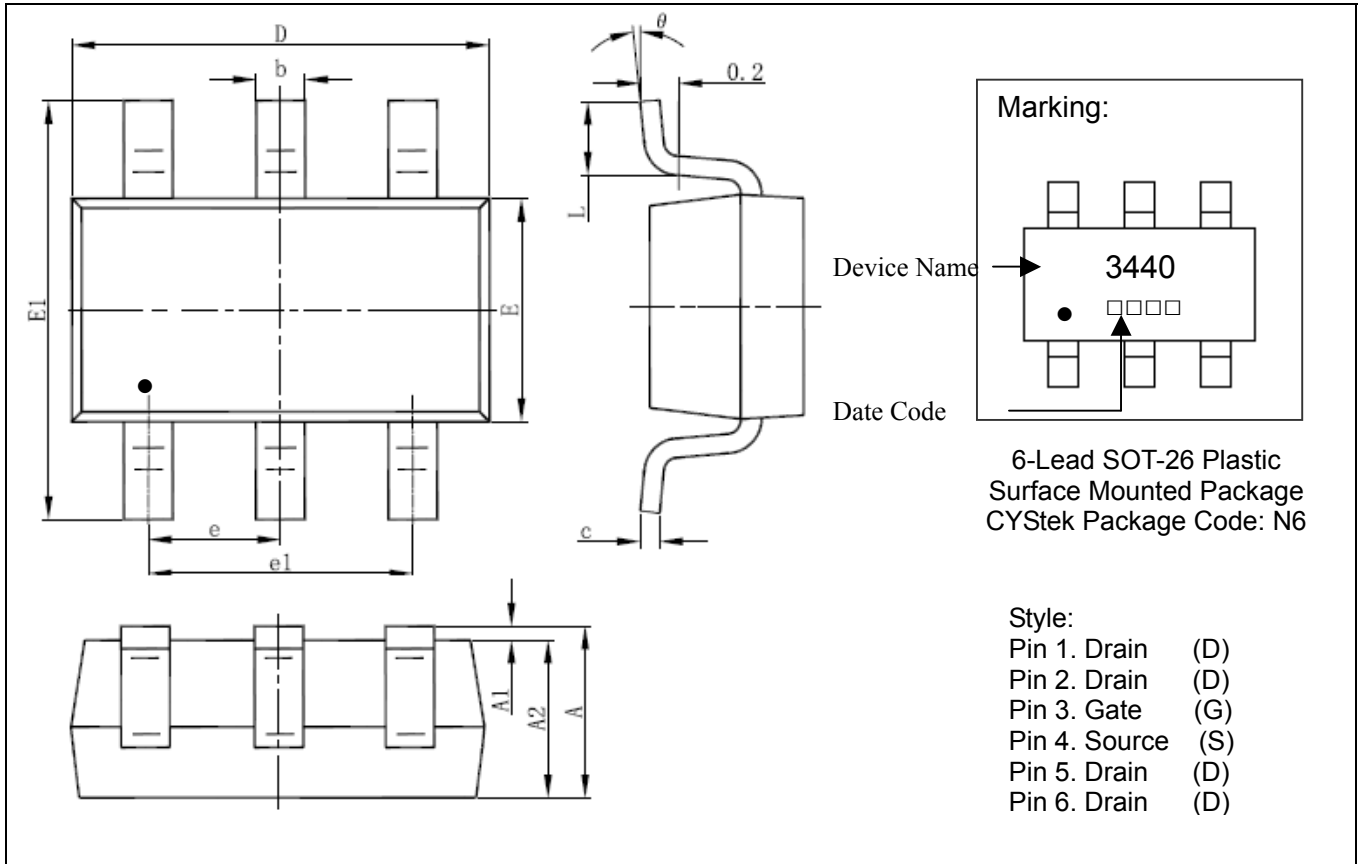
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note :1. All temperatures refer to topside of the package, measured on the package body surface.
 2.For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.

SOT-26 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049	E	1.500	1.700	0.059	0.067
A1	0.000	0.100	0.000	0.004	E1	2.650	2.950	0.104	0.116
A2	1.050	1.150	0.041	0.045	e	0.950 (BSC)		0.037 (BSC)	
b	0.300	0.500	0.012	0.020	e1	1.800	2.000	0.071	0.079
c	0.100	0.200	0.004	0.008	L	0.300	0.600	0.012	0.024
D	2.820	3.020	0.111	0.119	θ	0°	8°	0°	8°

Notes : 1. Controlling dimension : millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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