

20V N-CHANNEL Enhancement Mode MOSFET

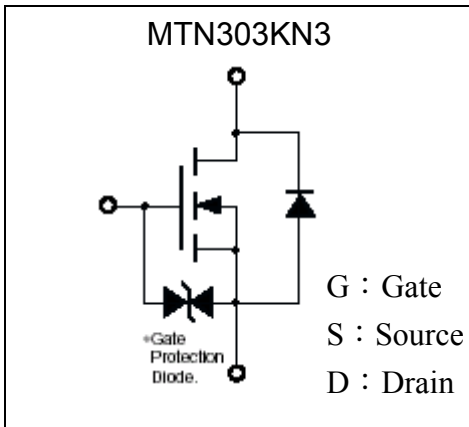
MTN303KN3

BV _{DSS}	20V
I _D	850mA
R _{DS(on)} @V _{GS} =4.5V, I _D =600mA	300mΩ (typ)
R _{DS(on)} @V _{GS} =2.5V, I _D =400mA	450mΩ (typ)
R _{DS(on)} @V _{GS} =1.8V, I _D =350mA	870mΩ (typ)

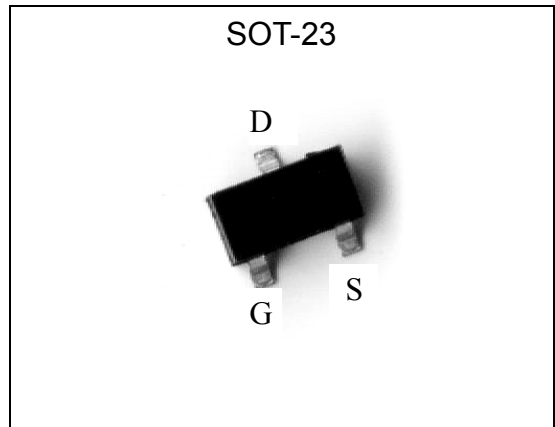
Features

- Simple drive requirement
- Small package outline
- Pb-free package

Symbol

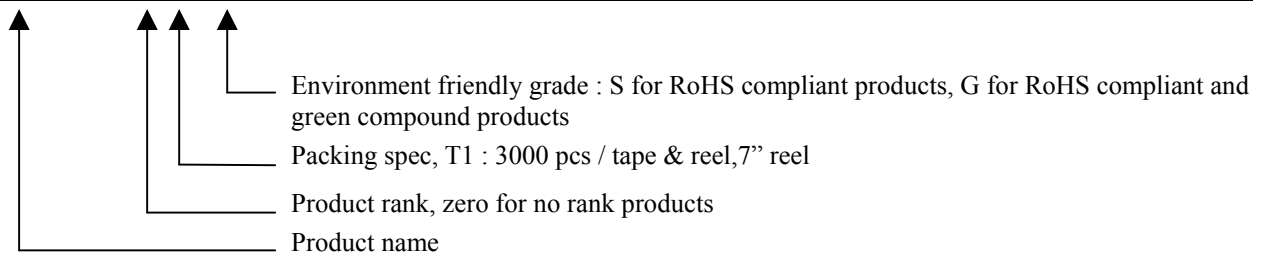


Outline



Ordering Information

Device	Package	Shipping
MTN303KN3-0-T1-G	SOT-23 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±8	V
Continuous Drain Current @ TA=25°C, V _{GS} =4.5V	I _D	850	mA
Continuous Drain Current @ TA=70°C, V _{GS} =4.5V		680	mA
Pulsed Drain Current (Notes 1, 2)	I _{DM}	3.5	A
Maximum Power Dissipation@ TA=25°C	P _D	0.35	W
Linear Derating Factor		0.003	W/°C
ESD susceptibility		2000 (Note 3)	V
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Note : 1. Pulse width limited by maximum junction temperature.
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%.
 3. Human body model, 1.5kΩ in series with 100pF

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient(PCB mounted)	R _{th,ja}	360	°C/W

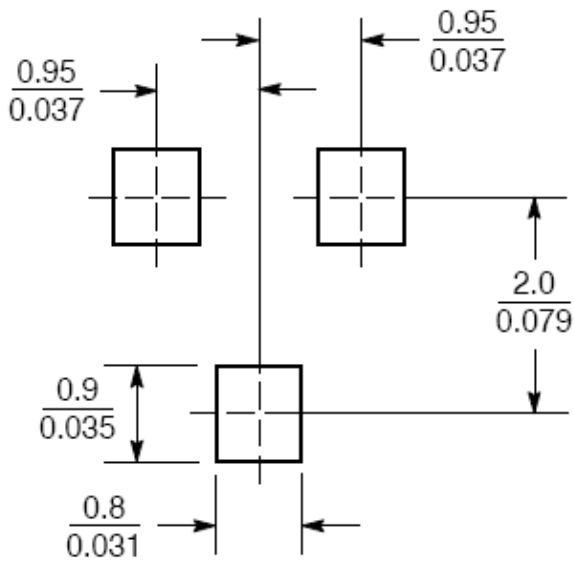
Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	20	-	-	V	V _{GS} =0, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.02	-	V/°C	Reference to 25°C, I _D =1mA
V _{GS(th)}	0.5	0.92	1.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±10	μA	V _{GS} =±8V, V _{DS} =0
I _{DSS}	-	-	1		V _{DS} =20V, V _{GS} =0
	-	-	10		V _{DS} =16V, V _{GS} =0 (T _j =70°C)
*R _{DS(ON)}	-	300	400	mΩ	V _{GS} =4.5V, I _D =600mA
	-	450	600		V _{GS} =2.5V, I _D =400mA
	-	870	1200		V _{GS} =1.8V, I _D =350mA
*G _{FS}	-	1.4	-	S	V _{DS} =5V, I _D =600mA
Dynamic					
C _{iss}	-	60	-	pF	V _{DS} =10V, V _{GS} =0, f=1MHz
C _{oss}	-	14	-		
C _{rss}	-	9	-		
t _{d(ON)}	-	4	-	ns	V _{DS} =10V, I _D =600mA, V _{GS} =10V R _G =3.3Ω, R _D =16.7Ω
t _r	-	10	-		
t _{d(OFF)}	-	15	-		
t _f	-	2	-		

Qg	-	1.3	-	nC	V _{DS} =16V, I _D =600mA, V _{GS} =4.5V
Qgs	-	0.3	-		
Qgd	-	0.5	-		
Source-Drain Diode					
*V _{SD}	-	0.81	1.2	V	V _{GS} =0V, I _S =600mA

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

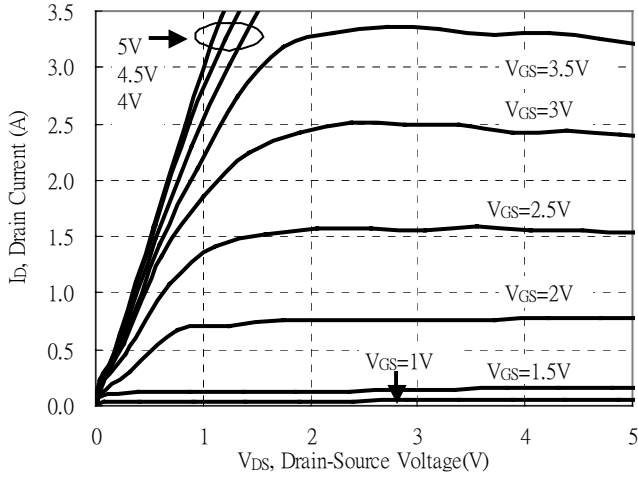
Recommended Soldering Footprint



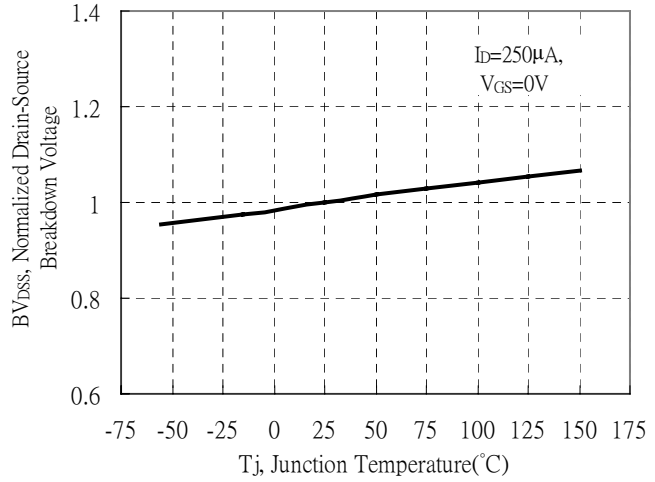
Unit : $\frac{\text{mm}}{\text{inches}}$

Typical Characteristics

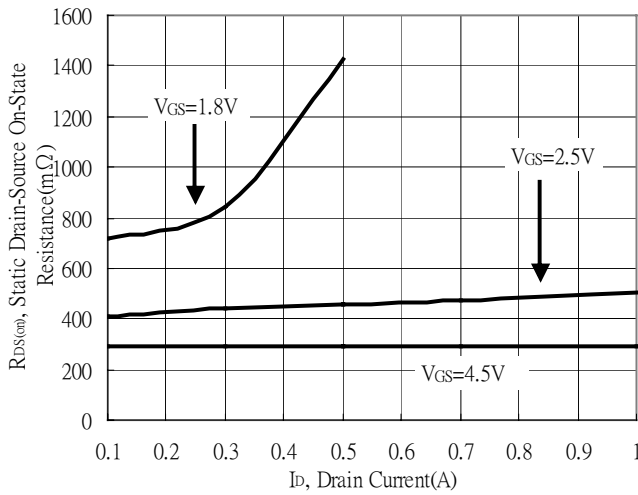
Typical Output Characteristics



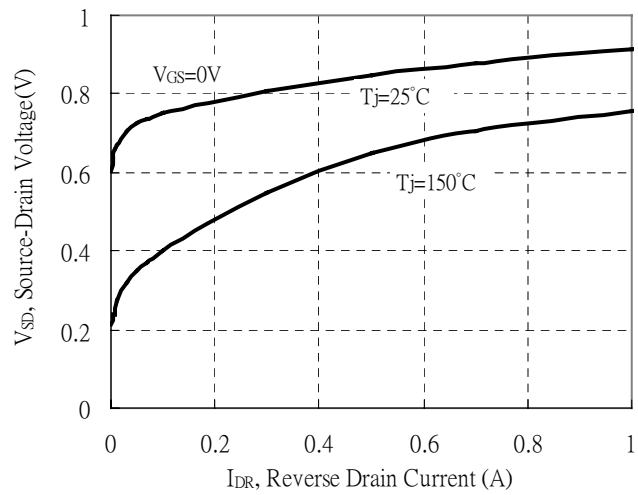
Breakdown Voltage vs Ambient Temperature



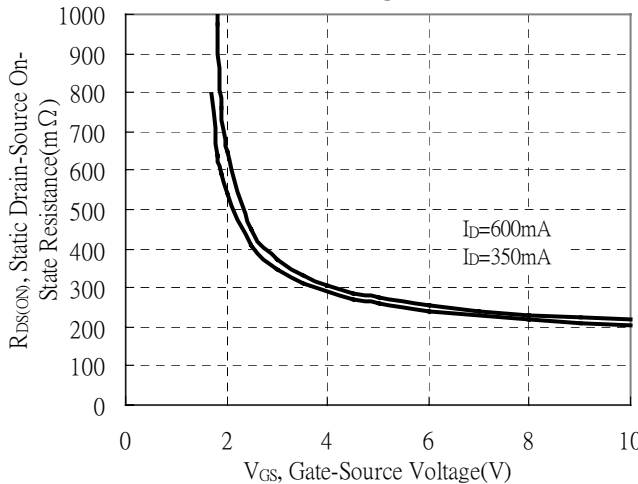
Static Drain-Source On-State resistance vs Drain Current



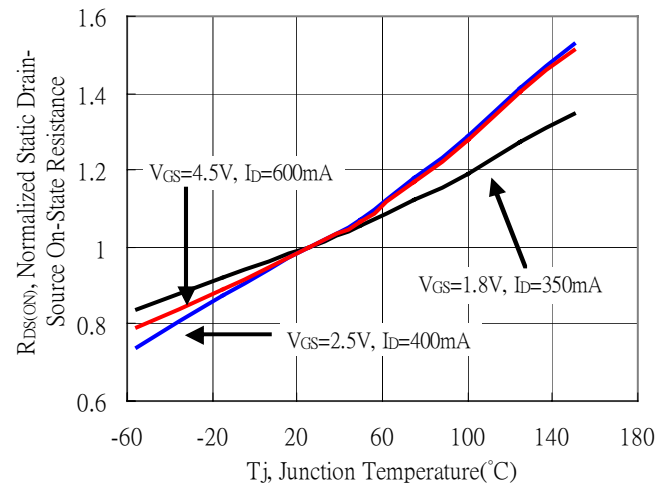
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



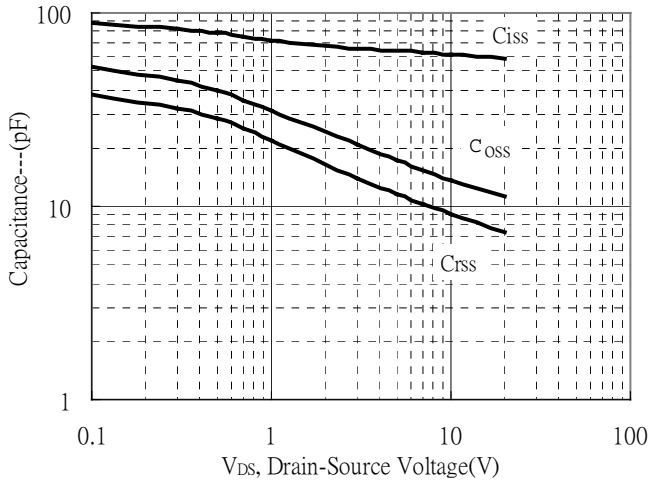
Drain-Source On-State Resistance vs Junction Temperature



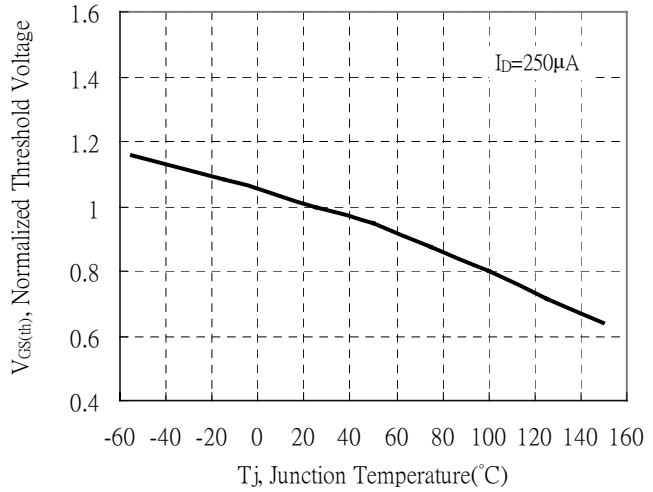


Typical Characteristics(Cont.)

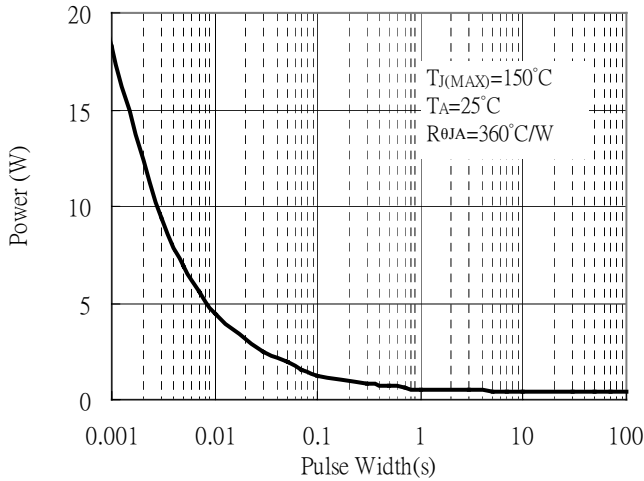
Capacitance vs Drain-to-Source Voltage



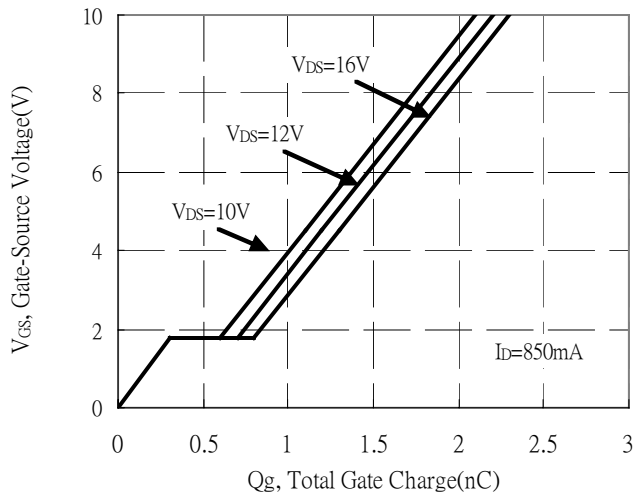
Threshold Voltage vs Junction Temperature



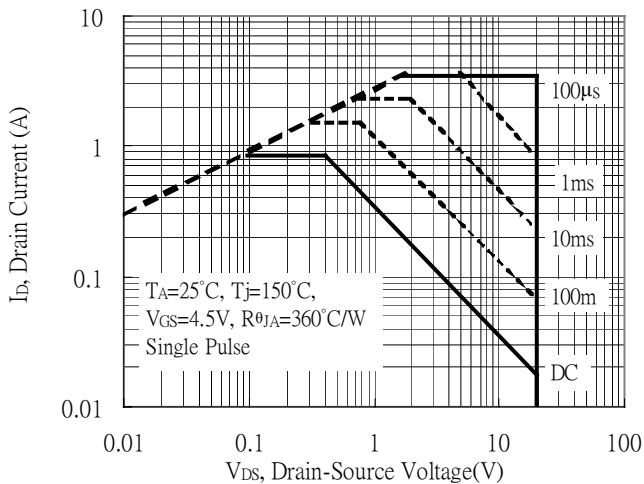
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



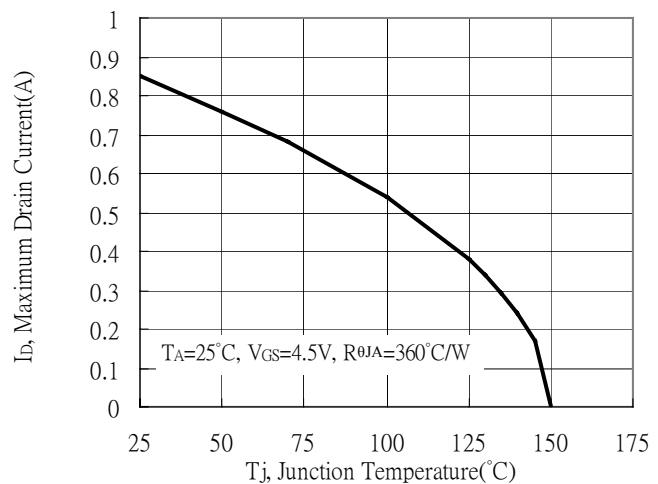
Gate Charge Characteristics



Maximum Safe Operating Area

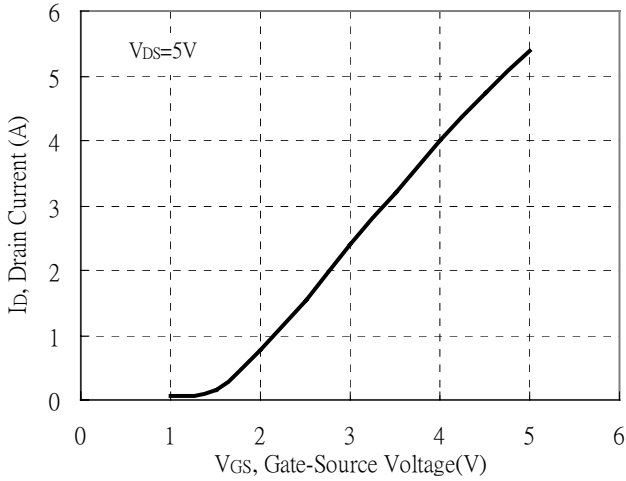


Maximum Drain Current vs Junction Temperature

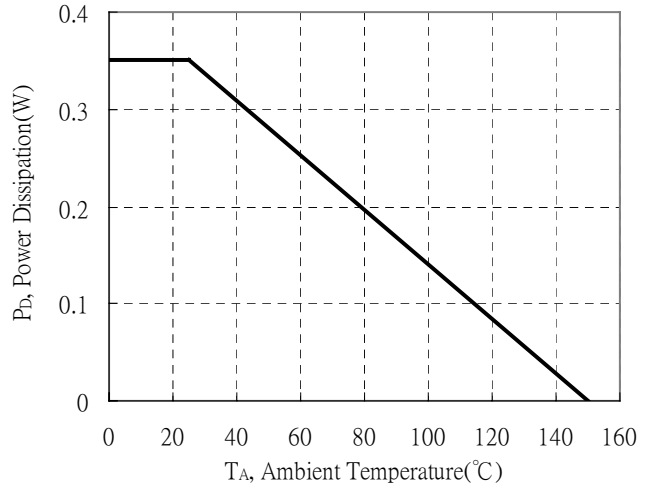


Typical Characteristics(Cont.)

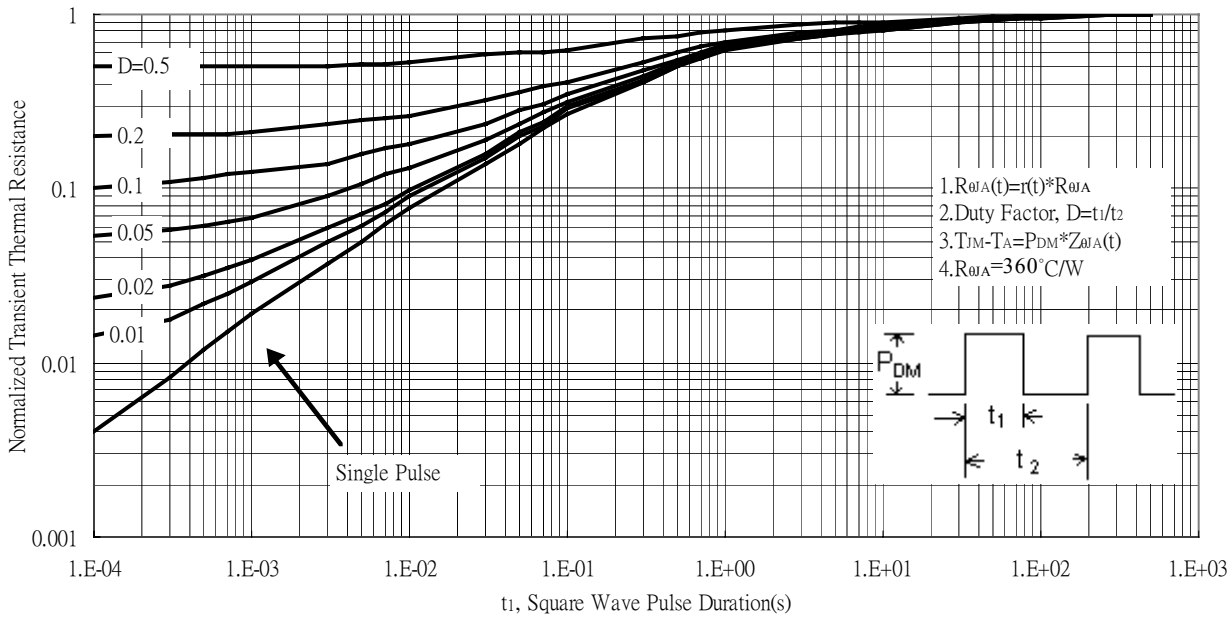
Typical Transfer Characteristics



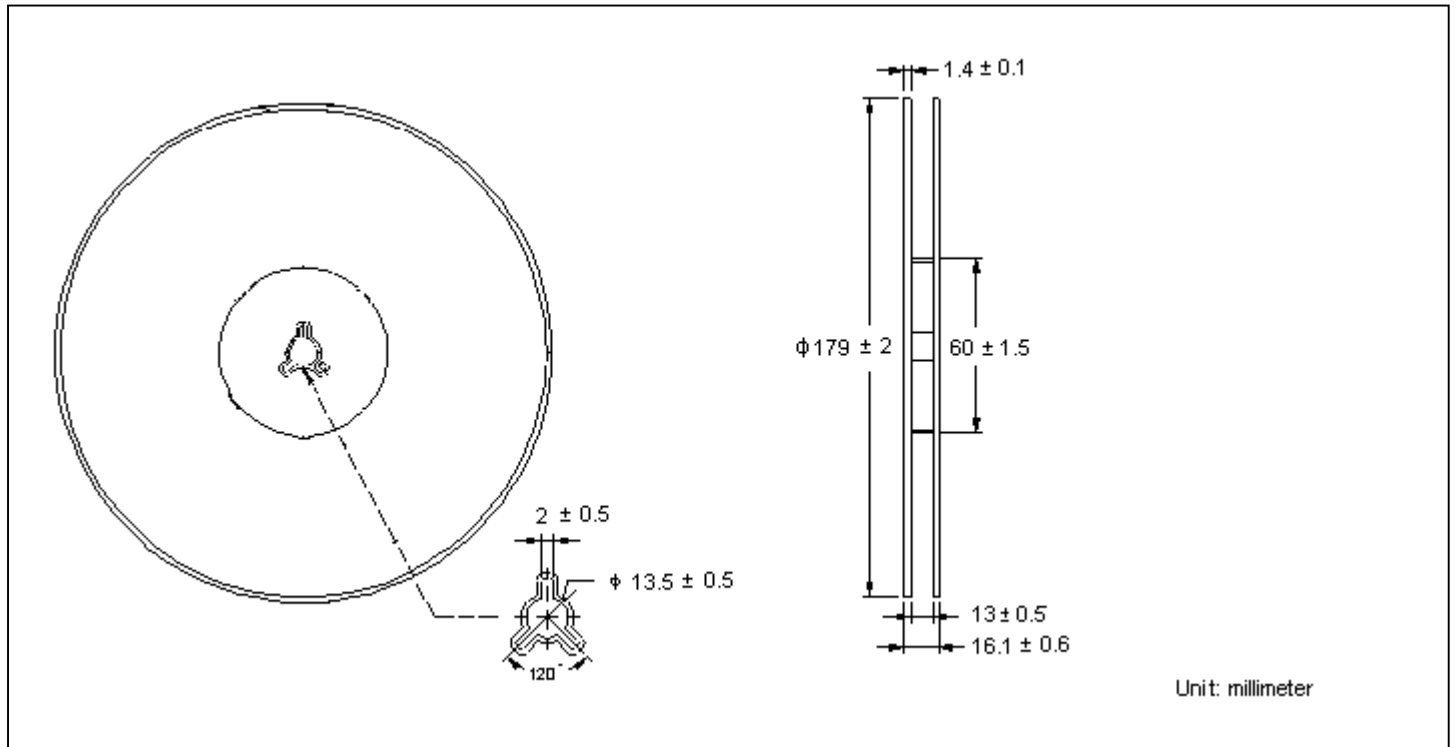
Power Derating Curve



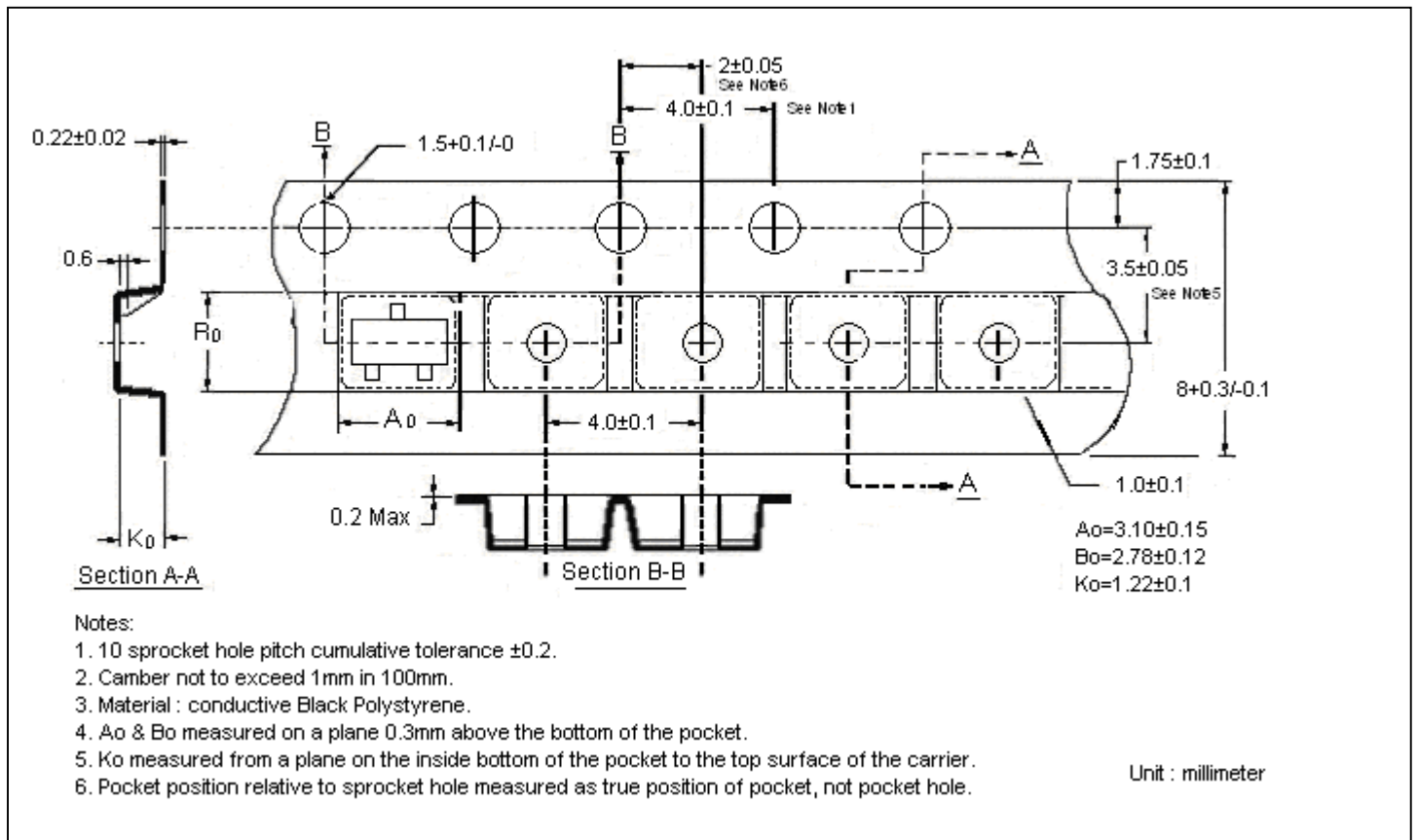
Transient Thermal Response Curves



Reel Dimension



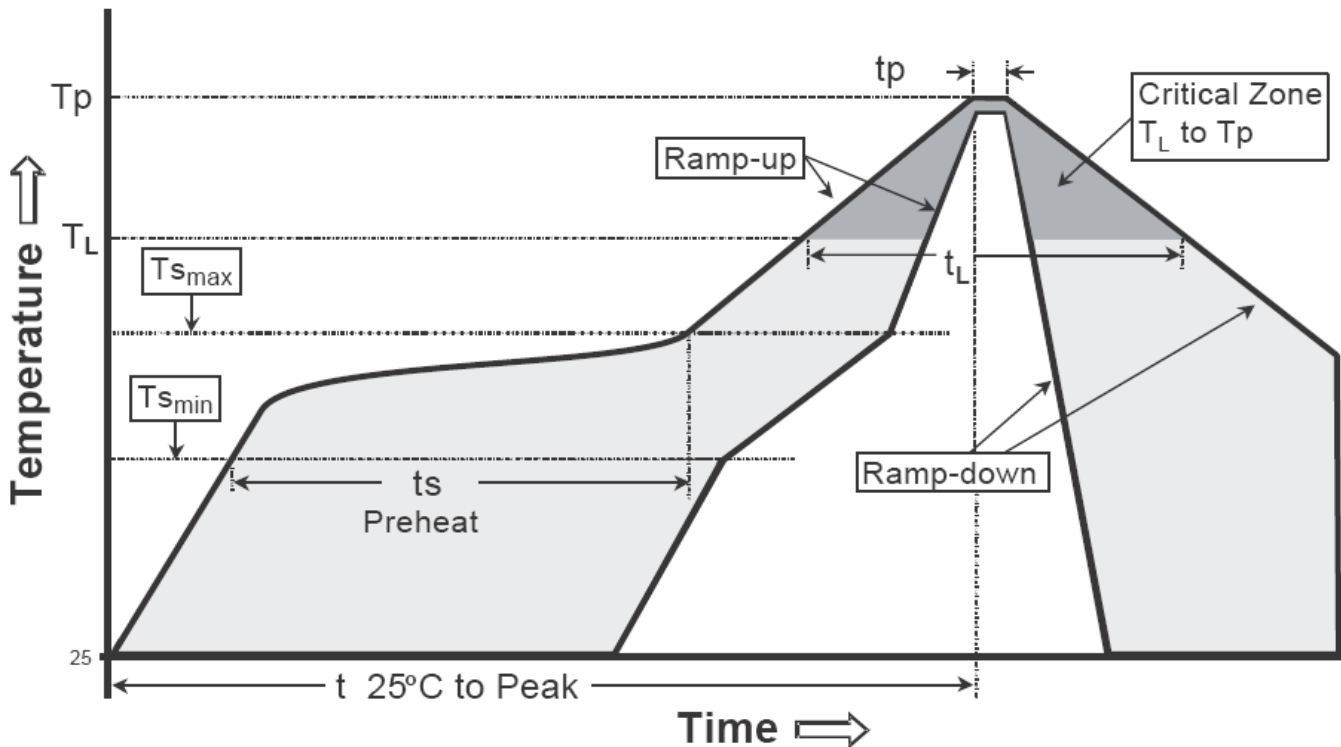
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

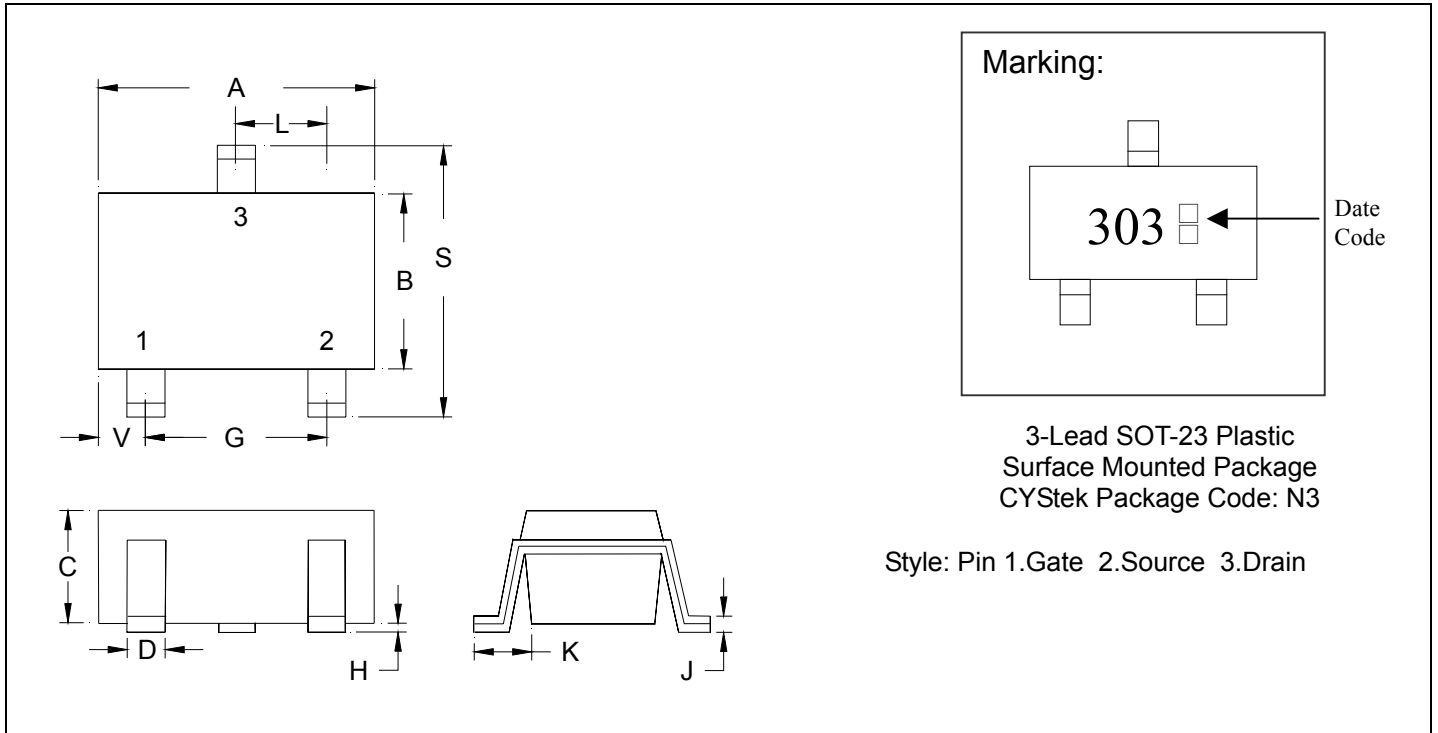
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(T _{s min}) -Temperature Max(T _{s max}) -Time(t _{s min} to t _{s max})	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-23 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1102	0.1204	2.80	3.04	J	0.0032	0.0079	0.08	0.20
B	0.0472	0.0669	1.20	1.70	K	0.0118	0.0266	0.30	0.67
C	0.0335	0.0512	0.89	1.30	L	0.0335	0.0453	0.85	1.15
D	0.0118	0.0197	0.30	0.50	S	0.0830	0.1161	2.10	2.95
G	0.0669	0.0910	1.70	2.30	V	0.0098	0.0256	0.25	0.65
H	0.0000	0.0040	0.00	0.10					

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.