

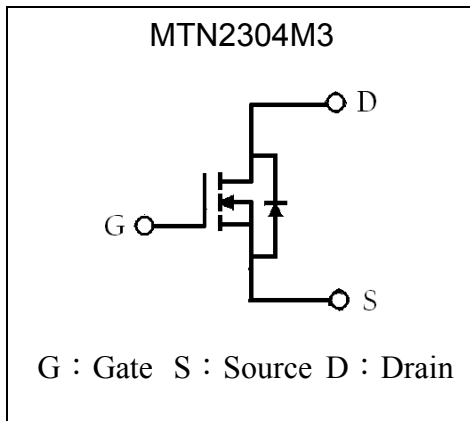
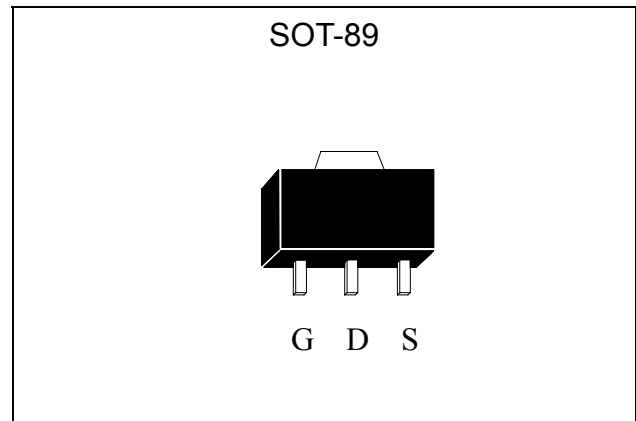
30V N-CHANNEL Enhancement Mode MOSFET

MTN2304M3

BV_{DSS}		30V
I_D		6A
$R_{DS(on)(TYP)}$	$V_{GS}=10V, I_D=5A$	20m Ω
	$V_{GS}=4.5V, I_D=4A$	28m Ω

Features

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Pb-free lead plating and halogen-free package

Symbol

Outline

Absolute Maximum Ratings ($T_A=25^\circ C$)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current @ $V_{GS}=10V, T_A=25^\circ C$	I_D	6	A
Continuous Drain Current @ $V_{GS}=10V, T_A=70^\circ C$	I_D	4.8	A
Pulsed Drain Current	I_{DM}	20 *1	A
Total Power Dissipation ($T_A=25^\circ C$)	P_d	1.2 *2	W
Linear Derating Factor		0.016	W/°C
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55~+150	°C

Note : *1. Pulse width limited by safe operating area.

 *2. Surface mounted on 1 in² copper pad of FR-4 board; 270 °C/W when mounted on min. copper pad.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient (Note)	$R_{th,ja}$	104	°C/W

 Note : Surface mounted on 1 in² copper pad of FR-4 board; 270 °C/W when mounted on min. copper pad.



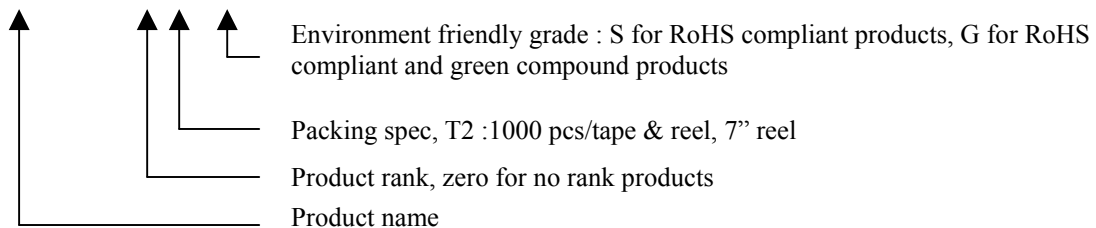
Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	30	-	-	V	V _{GS} =0, I _D =250μA
V _{GS(th)}	1.0	1.8	2.5	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0
I _{DSS}	-	-	1	μA	V _{DS} =30V, V _{GS} =0
I _{DSS}	-	-	10	μA	V _{DS} =24V, V _{GS} =0 (Tj=150°C)
*R _{DS(ON)}	-	20	26	mΩ	I _D =5A, V _{GS} =10V
	-	28	40		I _D =4A, V _{GS} =4.5V
Dynamic					
C _{iSS}	-	745	-	pF	V _{DS} =25V, V _{GS} =0, f=1MHz
C _{oSS}	-	51	-		
C _{rSS}	-	58	-		
*t _{d(ON)}	-	6	-	ns	V _{DS} =15V, I _D =1A, V _{GS} =10V, R _G =6Ω
*t _r	-	10	-		
*t _{d(OFF)}	-	24	-		
*t _f	-	5	-		
*Q _g	-	16	-	nC	V _{DS} =15V, I _D =5A, V _{GS} =10V
*Q _{gs}	-	3	-		
*Q _{gd}	-	5	-		
Source-Drain Diode					
*V _{SD}	-	-	1.3	V	V _{GS} =0V, I _S =2A
*I _S	-	-	4	A	V _D =V _{GS} =0V, V _S =1.3V
*I _{SM}	-	-	20	A	
t _{rr}	-	29	-	ns	I _F =5A, dI _F /dt=100A/μs
Q _{rr}	-	10	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

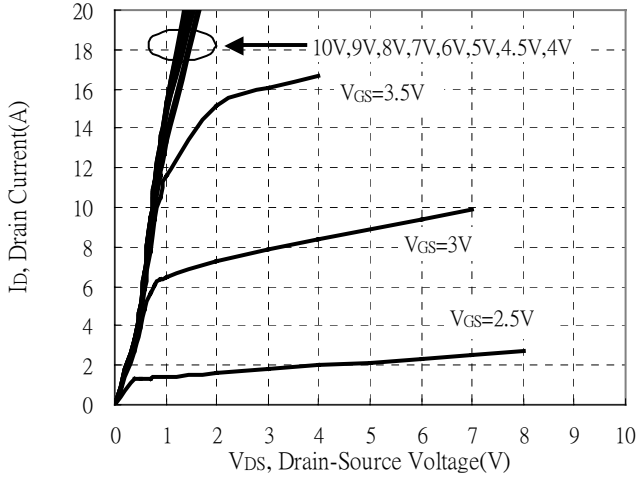
Ordering Information

Device	Package	Shipping
MTN2304M3-0-T2-G	SOT-89 (Pb-free lead plating & halogen-free package)	1000 pcs / Tape & Reel

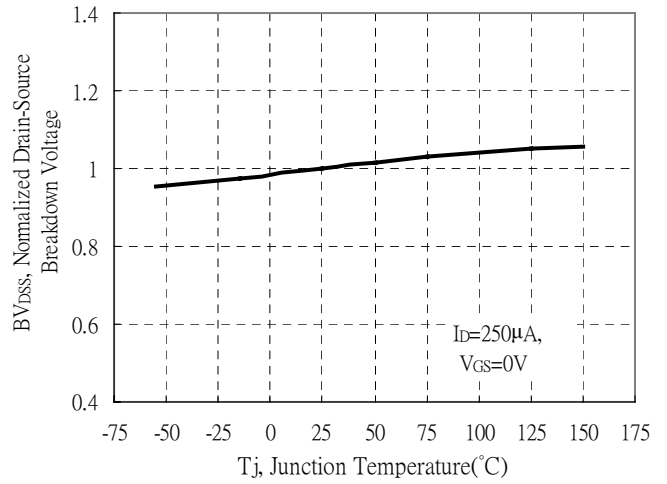


Typical Characteristics

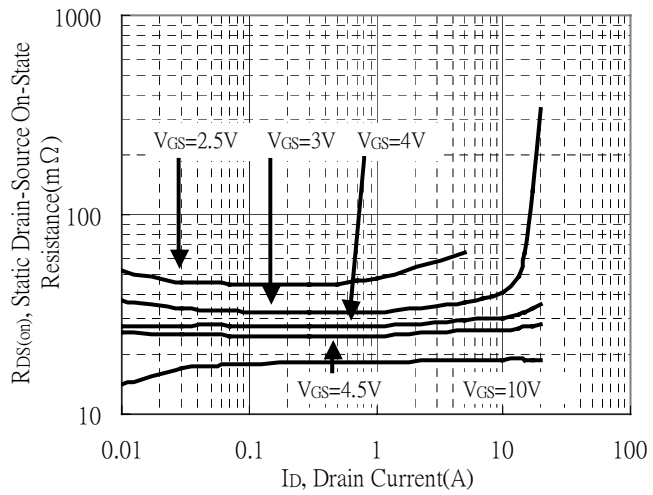
Typical Output Characteristics



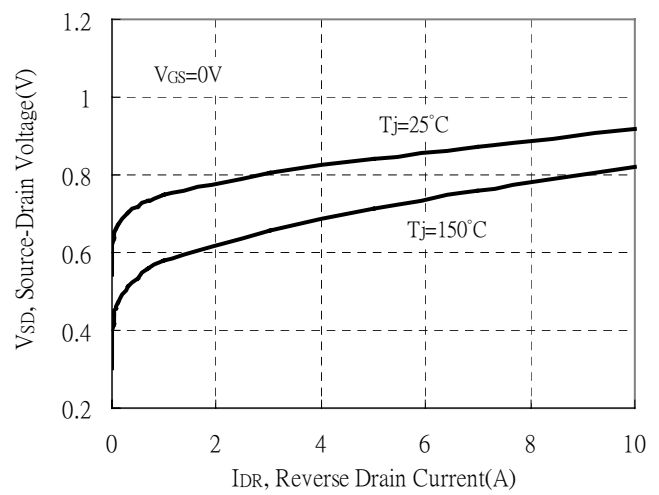
Brekdown Voltage vs Ambient Temperature



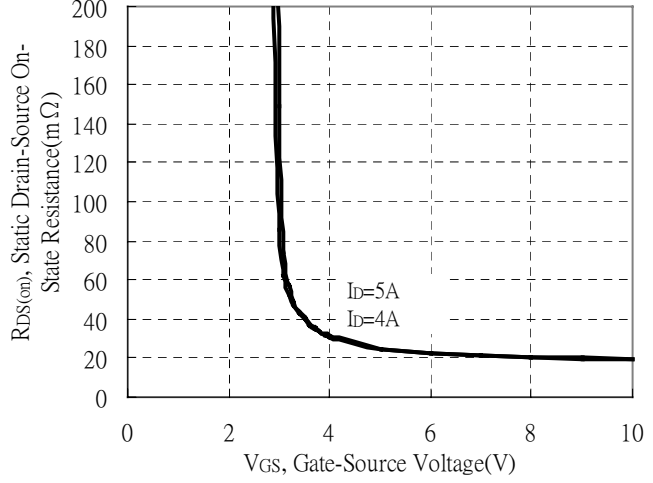
Static Drain-Source On-State resistance vs Drain Current



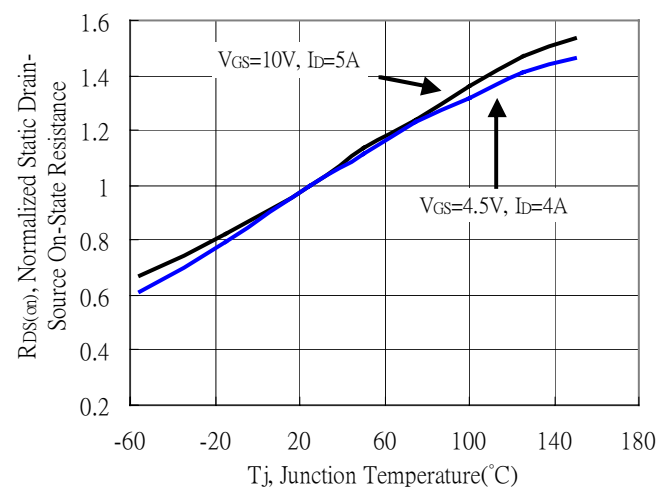
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

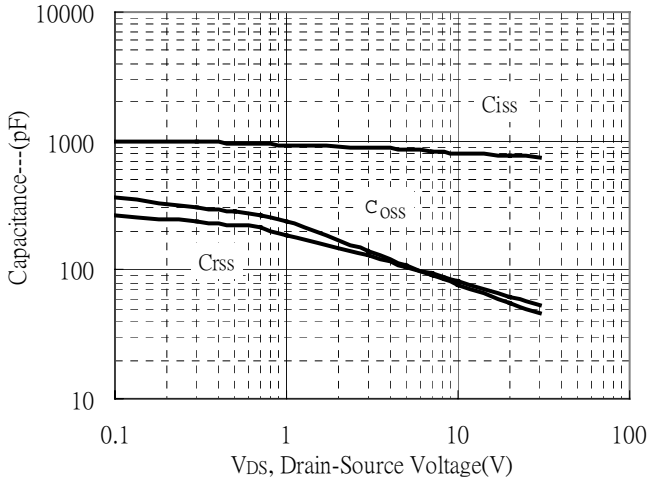


Drain-Source On-State Resistance vs Junction Temperature

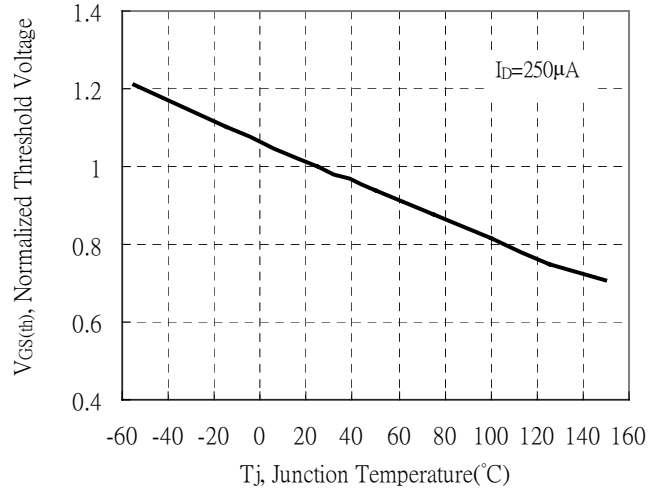


Typical Characteristics(Cont.)

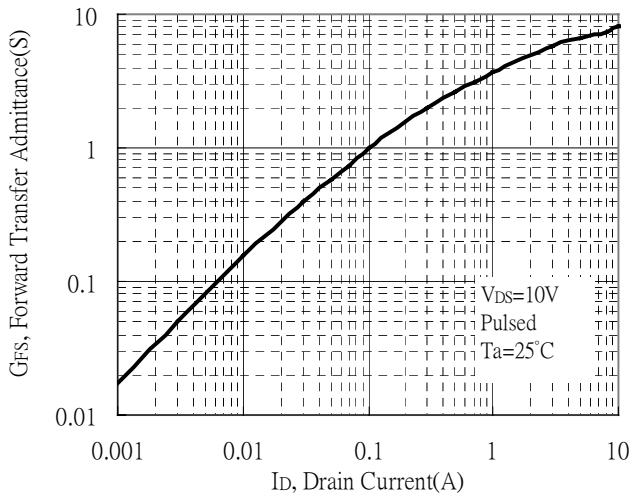
Capacitance vs Drain-to-Source Voltage



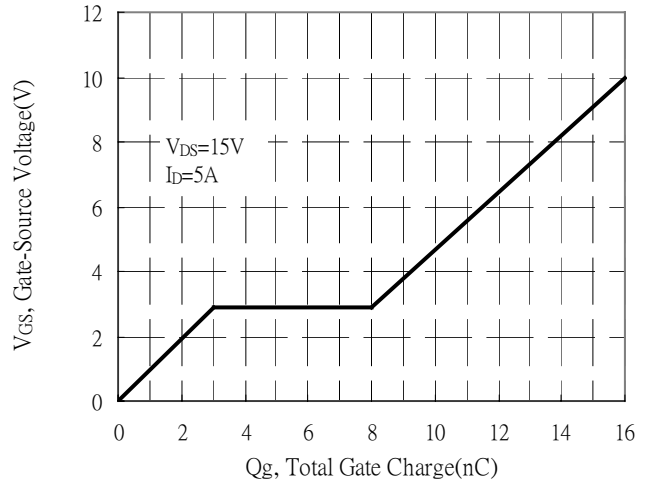
Threshold Voltage vs Junction Temperature



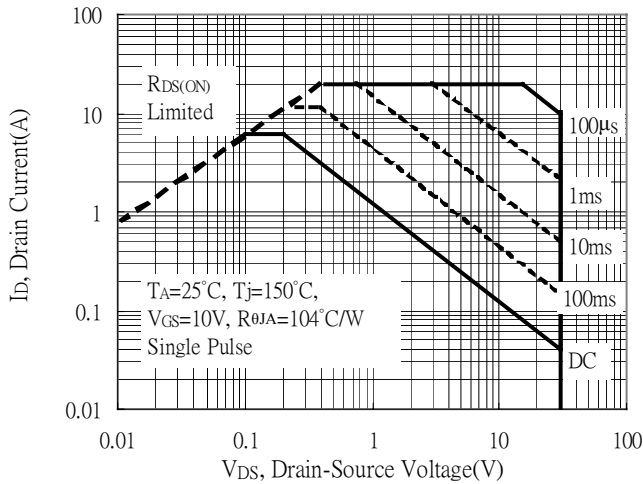
Forward Transfer Admittance vs Drain Current



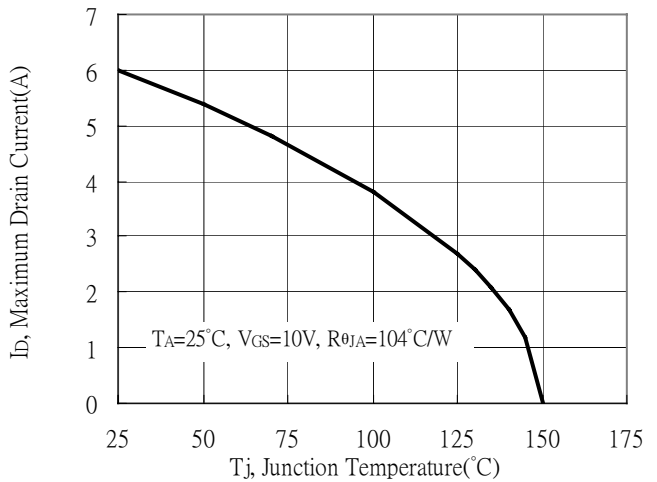
Gate Charge Characteristics



Maximum Safe Operating Area

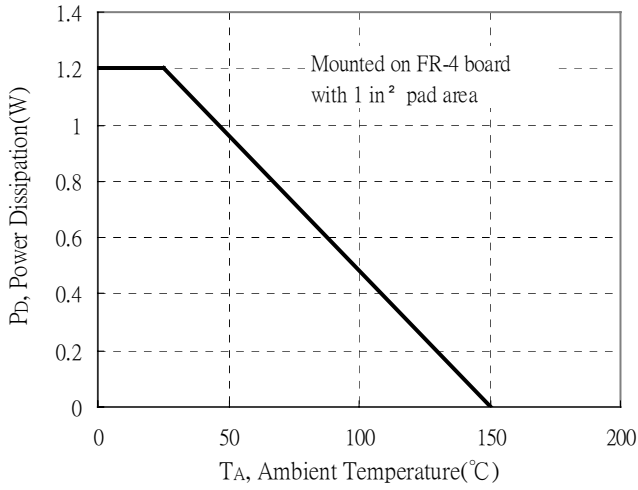


Maximum Drain Current vs Junction Temperature

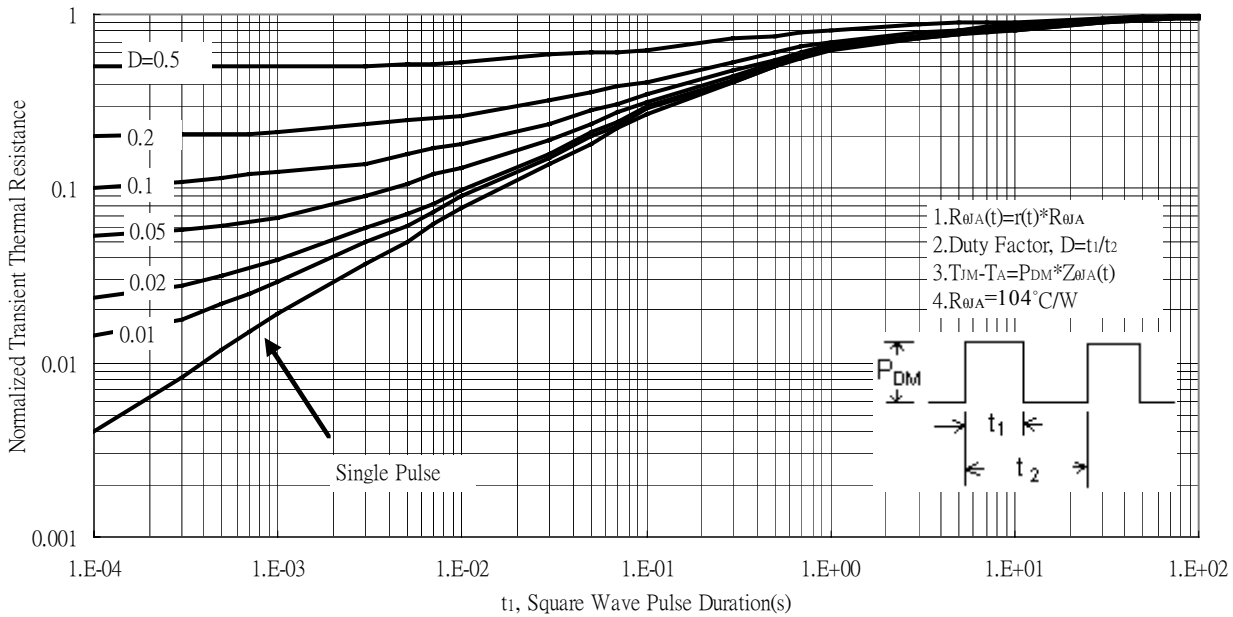


Typical Characteristics(Cont.)

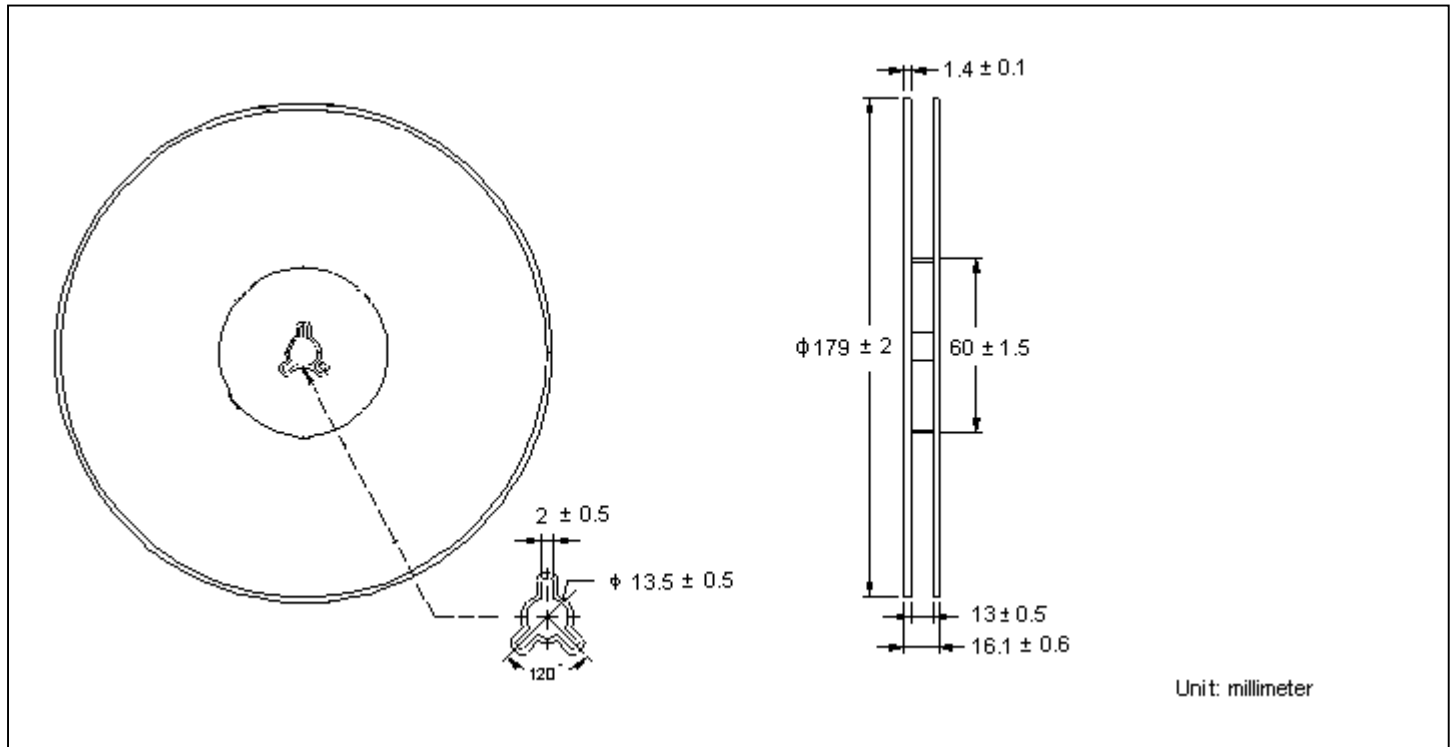
Power Derating Curve



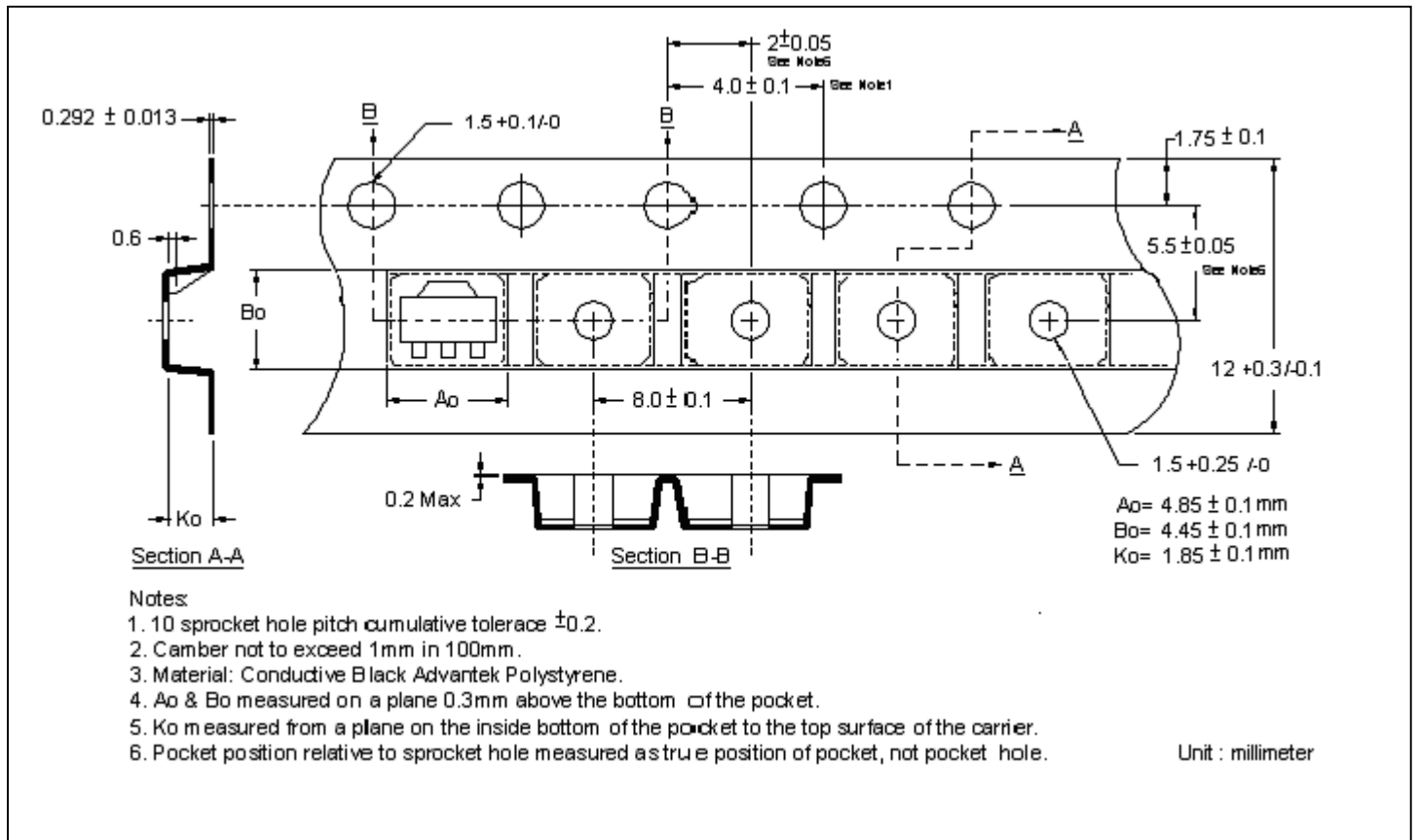
Transient Thermal Response Curves



Reel Dimension

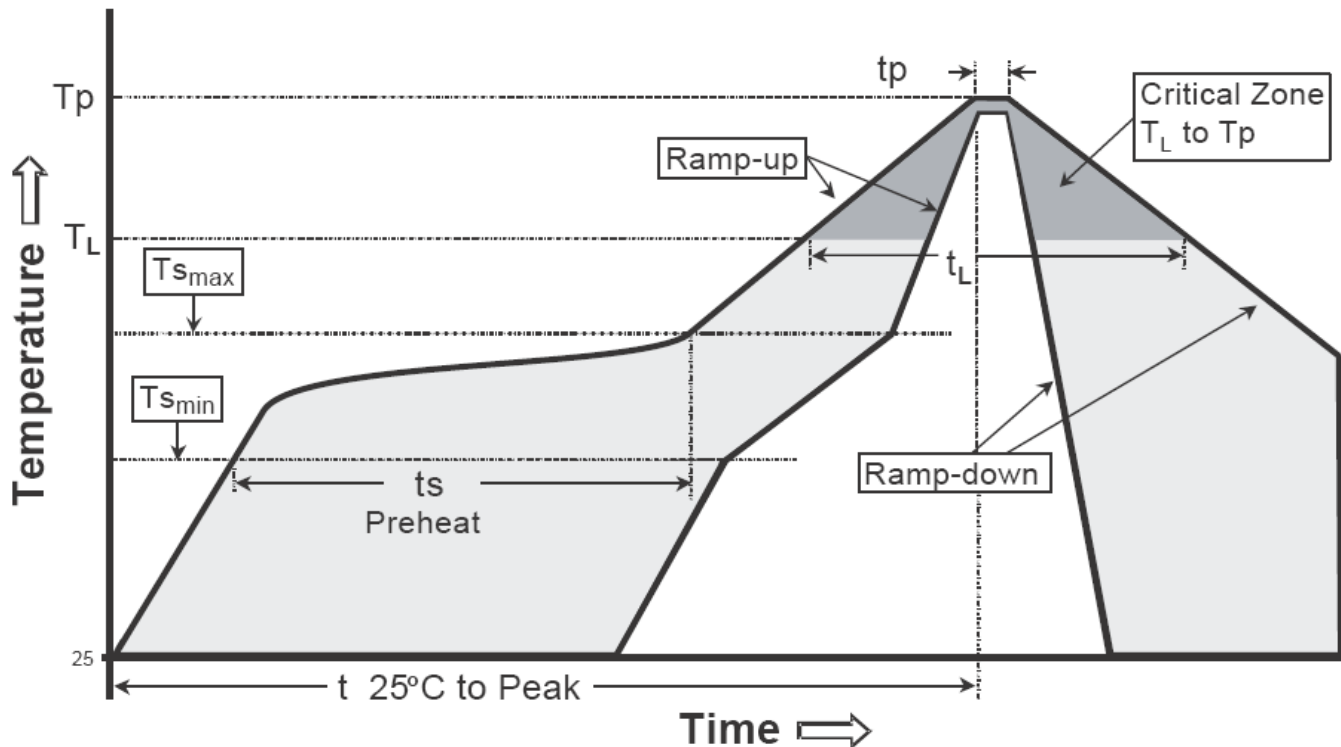


Carrier Tape Dimension



Recommended wave soldering condition

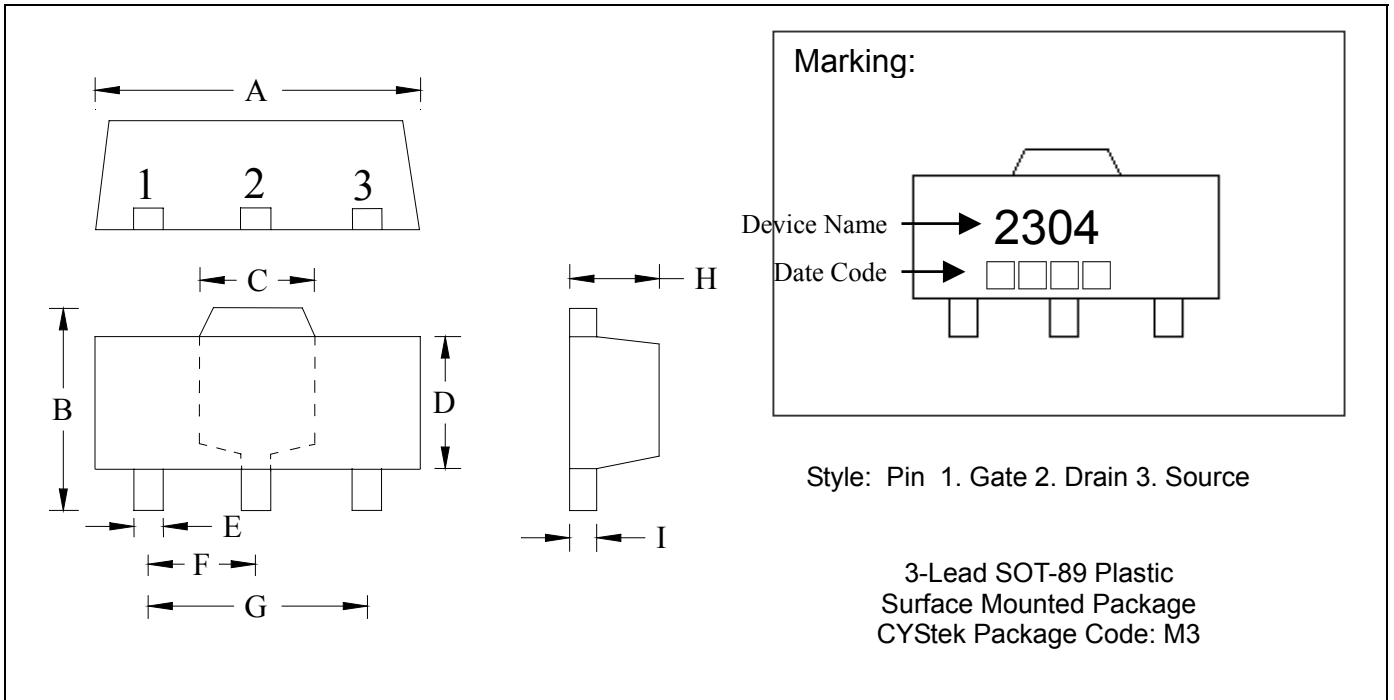
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-89 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1732	0.1811	4.40	4.60	F	0.0591	TYP	1.50	TYP
B	0.1551	0.1673	3.94	4.25	G	0.1181	TYP	3.00	TYP
C	0.0610	REF	1.55	REF	H	0.0551	0.0630	1.40	1.60
D	0.0906	0.1024	2.30	2.60	I	0.0138	0.0173	0.35	0.44
E	0.0126	0.0205	0.32	0.52					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.