

50V N-CHANNEL Enhancement Mode MOSFET

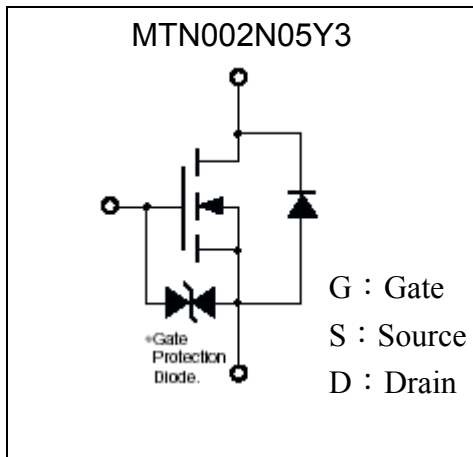
MTN002N05Y3

BV _{DSS}	50V
I _D @ T _A =25°C, V _{GS} =4.5V	250mA
R _{DS(on)} @V _{GS} =4.5V, I _D =220mA	1.2 Ω (typ)
R _{DS(on)} @V _{GS} =2.5V, I _D =220mA	1.7 Ω (typ)
R _{DS(on)} @V _{GS} =1.8V, I _D =100mA	3.6 Ω (typ)

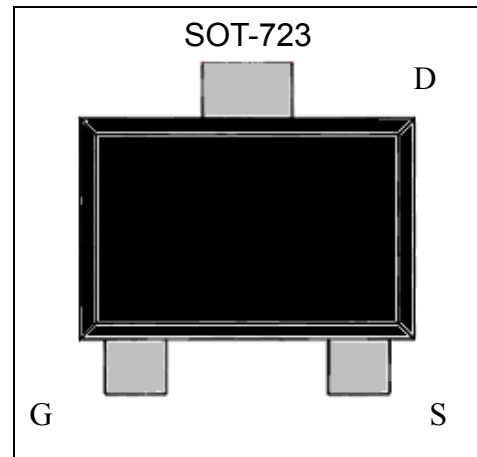
Features

- Simple drive requirement
- Small package outline
- **ESD protected gate**
- Pb-free lead plating and halogen-free package

Symbol

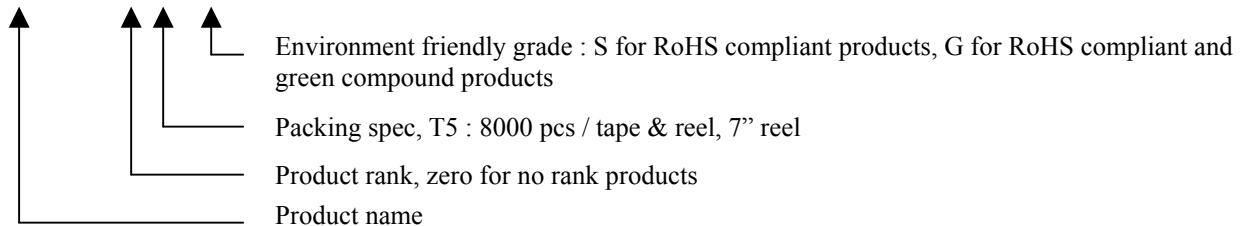


Outline



Ordering Information

Device	Package	Shipping
MTN002N05Y3-0-T5-G	SOT-723 (Pb-free lead plating and halogen-free package)	8000 pcs / tape & reel





Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	50	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current @ T _A =25°C, V _{GS} =4.5V (Note 3)		I _D	250	mA
Continuous Drain Current @ T _A =85°C, V _{GS} =4.5V (Note 3)			180	
Pulsed Drain Current (Notes 1, 2)		I _{DM}	1	A
Maximum Power Dissipation (Note 3)	T _A =25°C	P _D	150	mW
	T _A =85°C		80	
ESD susceptibility			1500 (Note 4)	V
Operating Junction and Storage Temperature		T _j , T _{stg}	-55~+150	°C

- Note : 1. Pulse width limited by maximum junction temperature.
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%.
 3. Surface mounted on FR-4 board.
 4. Human body model, 1.5kΩ in series with 100pF.

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient(PCB mounted)	R _{th,ja}	833	°C/W

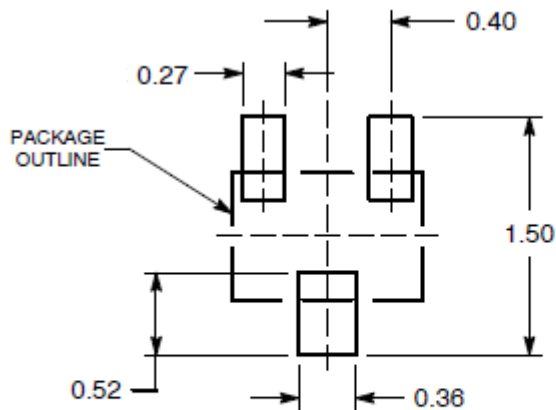
Electrical Characteristics (Tj=25°C, unless otherwise noted)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	50	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	0.5	1.1	1.5		V _{DS} =10V, I _D =1mA
I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =50V, V _{GS} =0V
	-	-	10		V _{DS} =40V, V _{GS} =0V (T _j =70°C)
*R _{DSON}	-	1.2	1.6	Ω	V _{GS} =4.5V, I _D =220mA
	-	1.7	2.2		V _{GS} =2.5V, I _D =220mA
	-	3.6	5		V _{GS} =1.8V, I _D =100mA
*G _{FS}	0.4	0.6	-	S	V _{DS} =10V, I _D =220mA
Dynamic					
C _{iss}	-	43	-	pF	V _{DS} =10V, V _{GS} =0V, f=1MHz
C _{oss}	-	7.2	-		
C _{rss}	-	4	-		

$t_{d(ON)}$	-	4	-	ns	$V_{DS}=30V, I_D=100mA, V_{GS}=4.5V$ $R_G=10\Omega$
t_r	-	7	-		
$t_{d(OFF)}$	-	15	-		
t_f	-	15	-		
Q_g	-	0.76	-	nC	$V_{DS}=30V, I_D=250mA, V_{GS}=4.5V$
Q_{gs}	-	0.085	-		
Q_{gd}	-	0.26	-		
Source-Drain Diode					
$*V_{SD}$	-	0.79	1.2	V	$V_{GS}=0V, I_S=200mA$

*Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

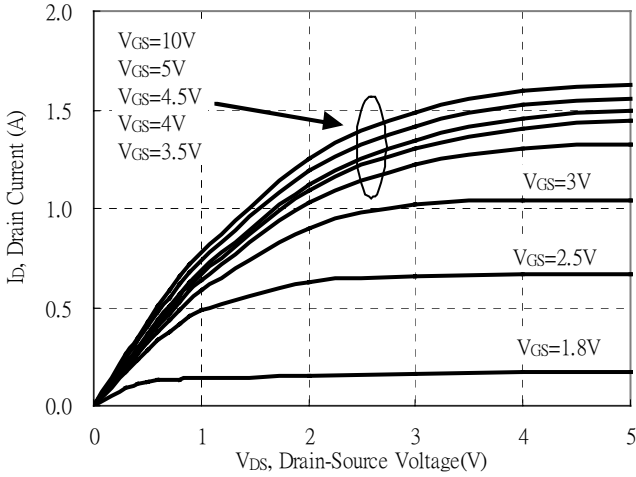
Recommended Soldering Footprint



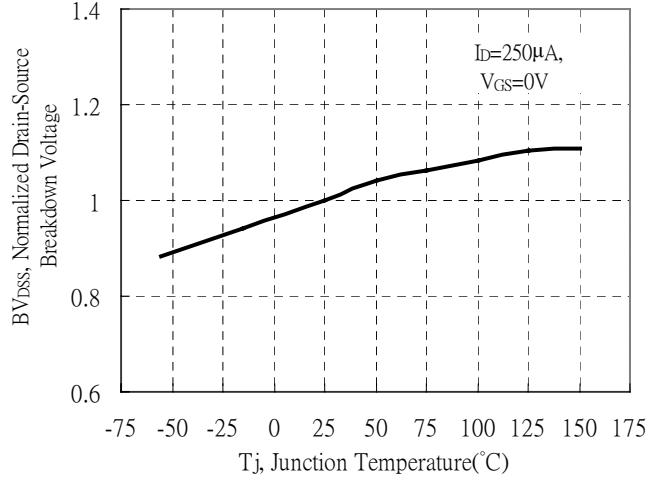
DIMENSIONS: MILLIMETERS

Typical Characteristics

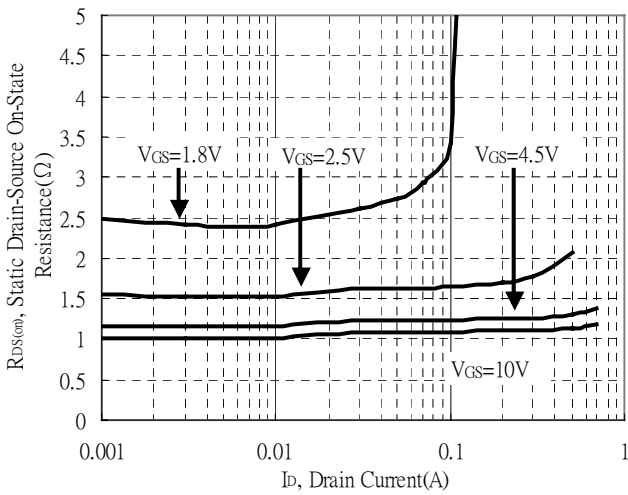
Typical Output Characteristics



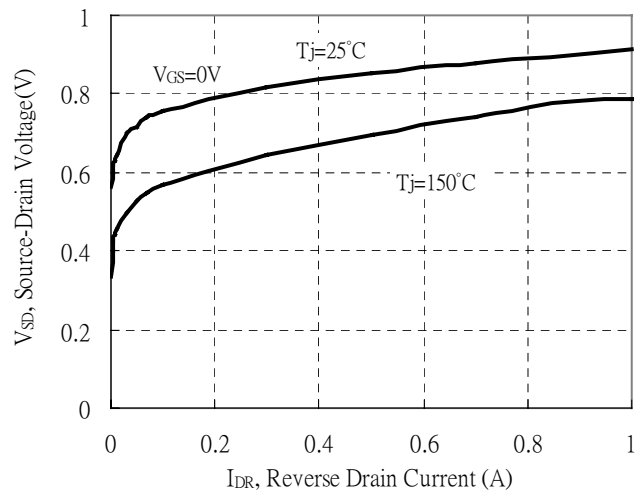
Breakdown Voltage vs Ambient Temperature



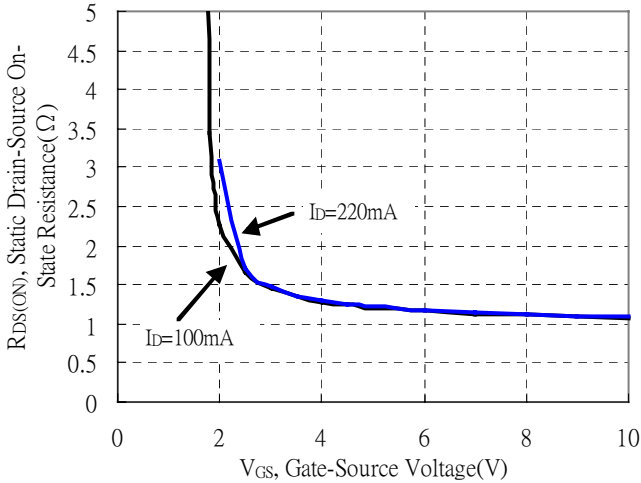
Static Drain-Source On-State resistance vs Drain Current



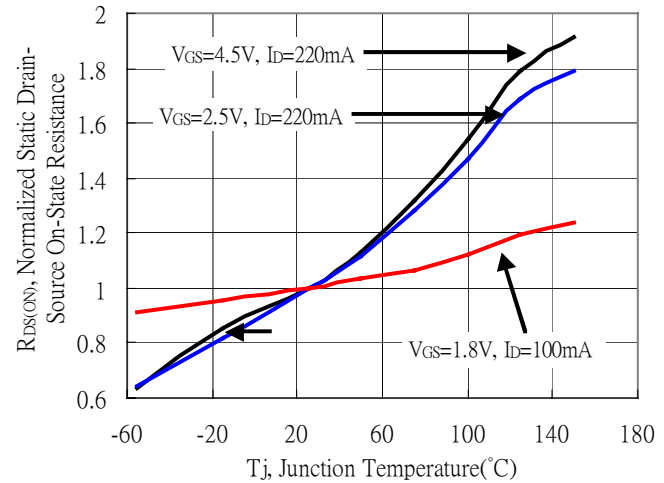
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

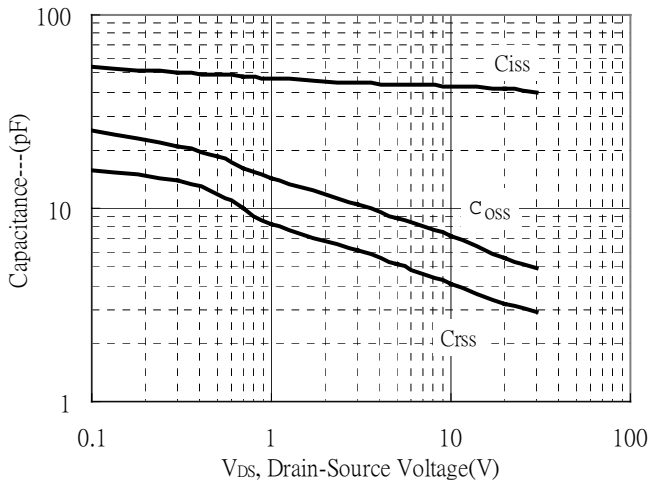


Drain-Source On-State Resistance vs Junction Temperature

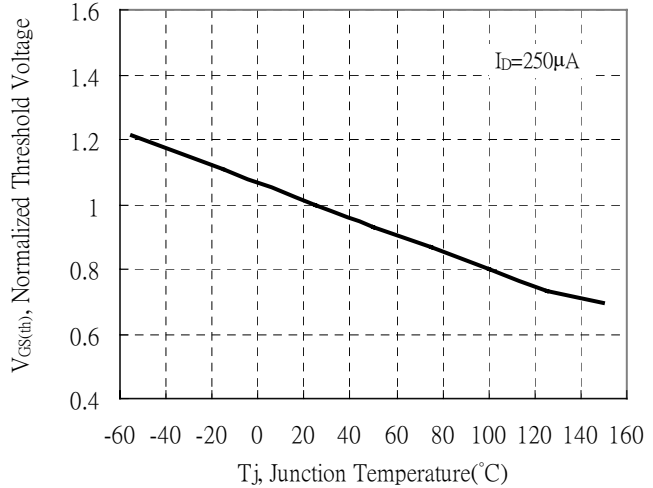


Typical Characteristics(Cont.)

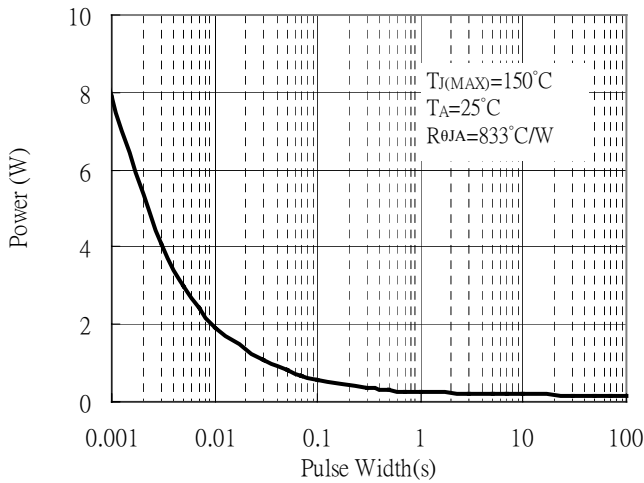
Capacitance vs Drain-to-Source Voltage



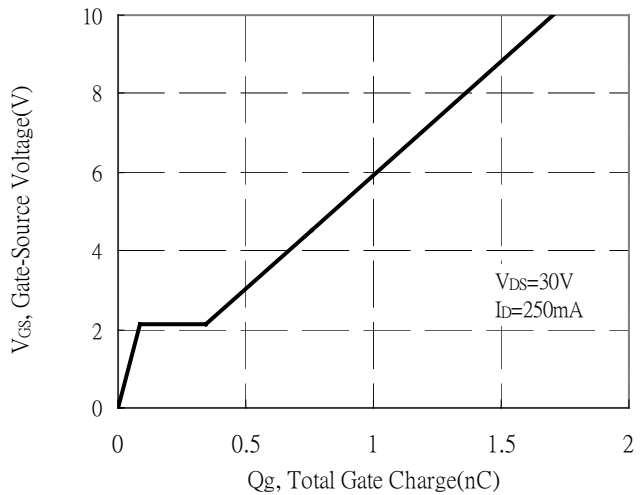
Threshold Voltage vs Junction Temperature



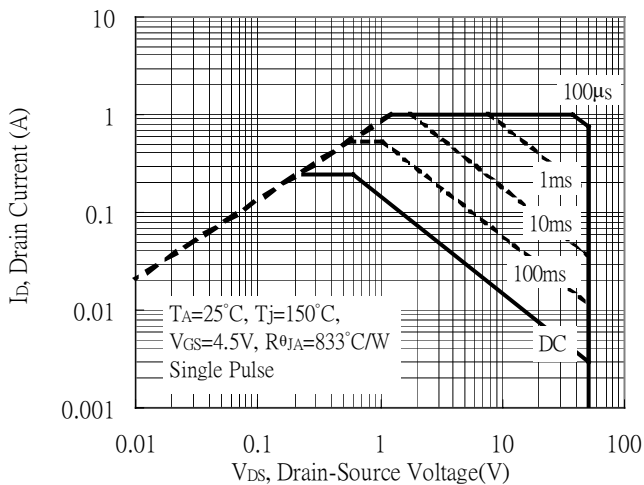
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



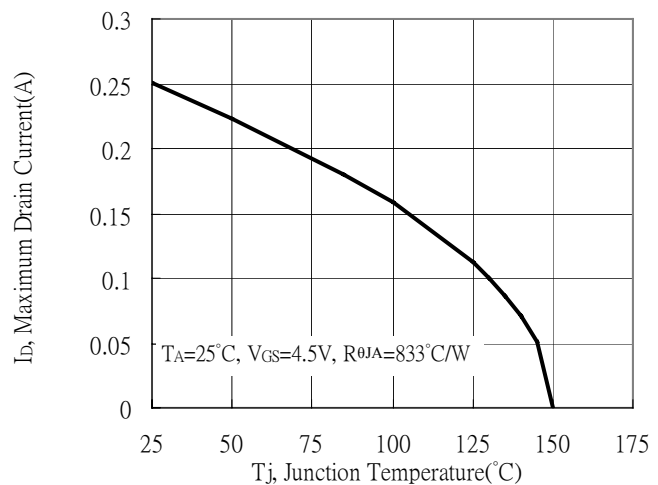
Gate Charge Characteristics



Maximum Safe Operating Area

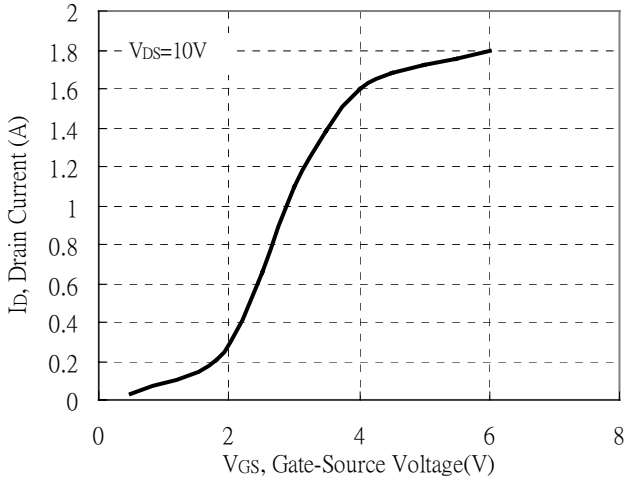


Maximum Drain Current vs Junction Temperature

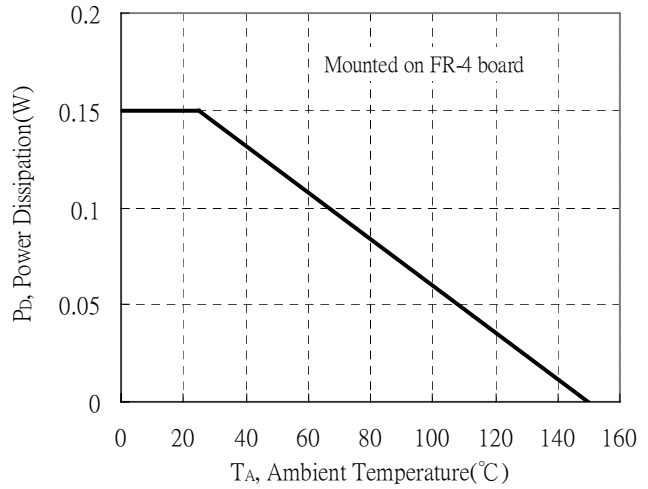


Typical Characteristics(Cont.)

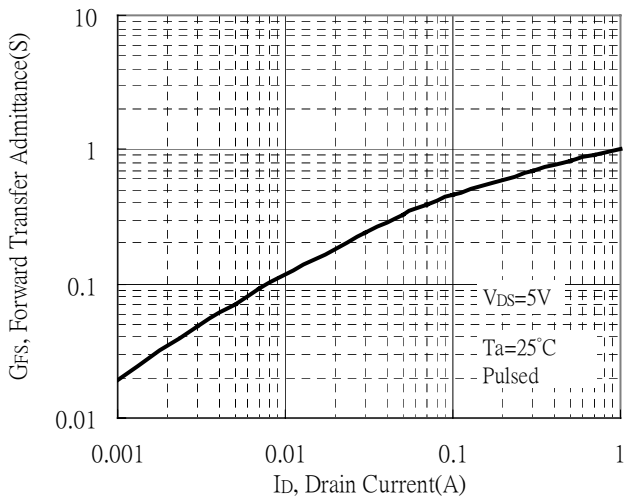
Typical Transfer Characteristics



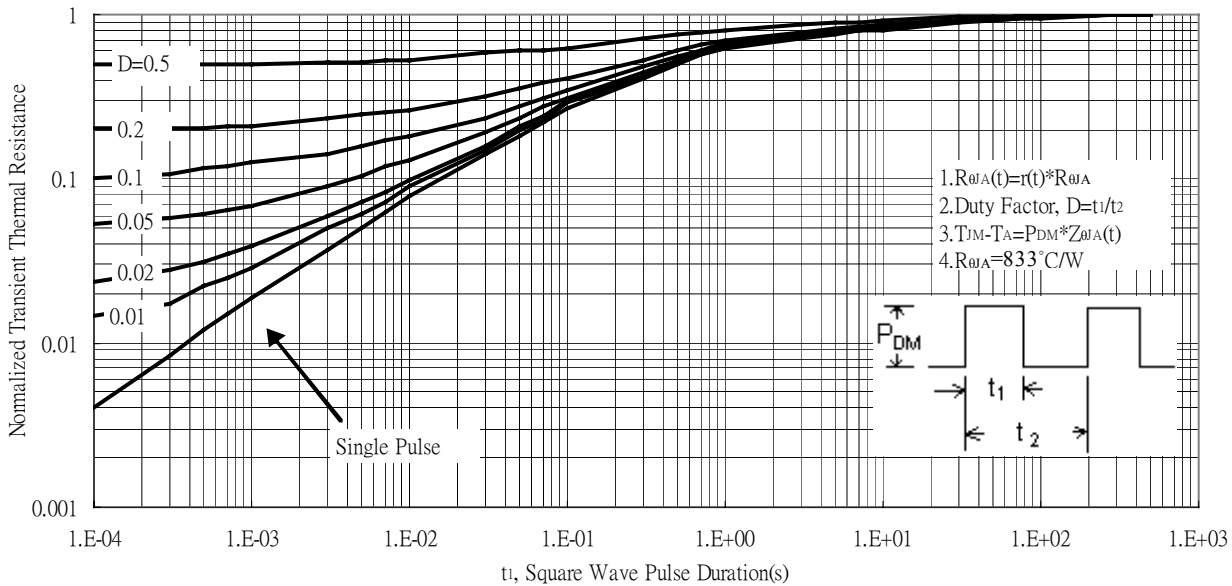
Power Derating Curve



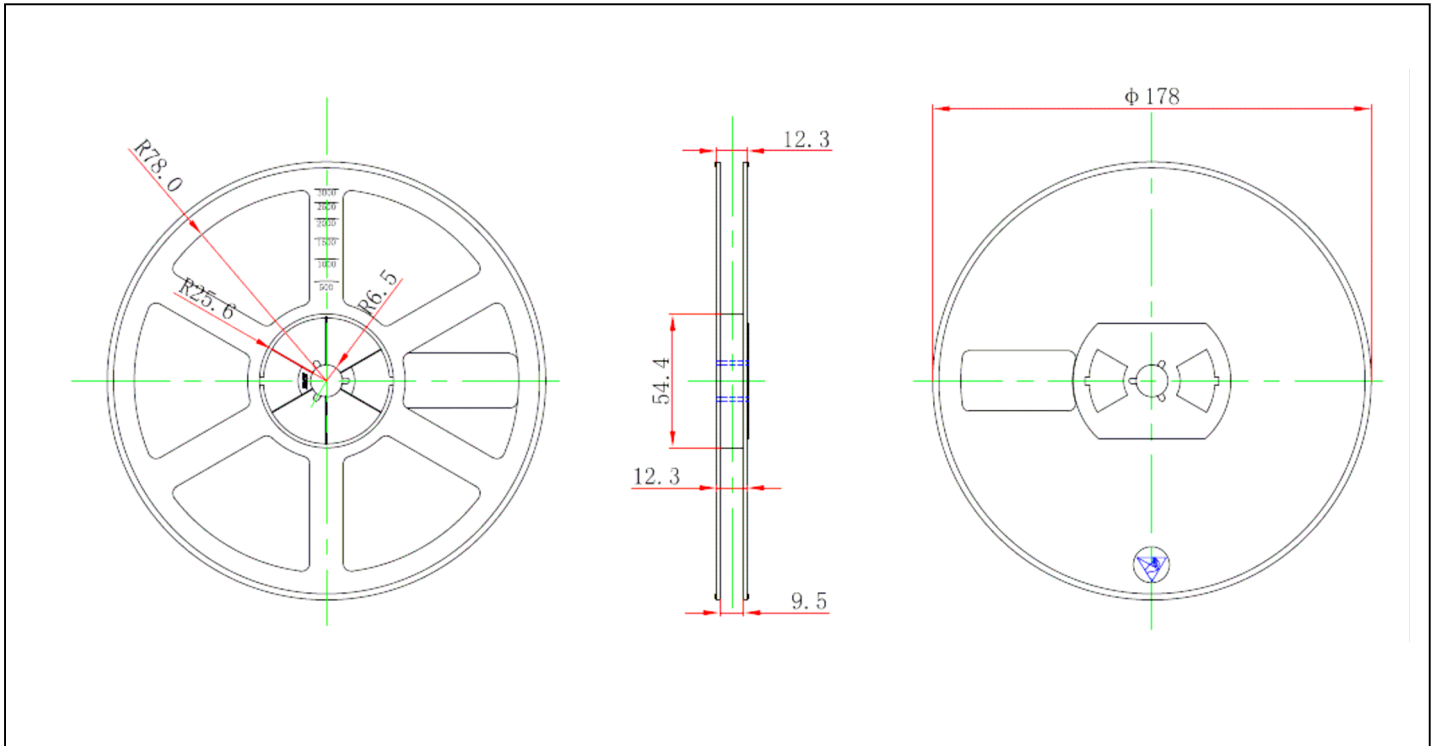
Forward Transfer Admittance vs Drain Current



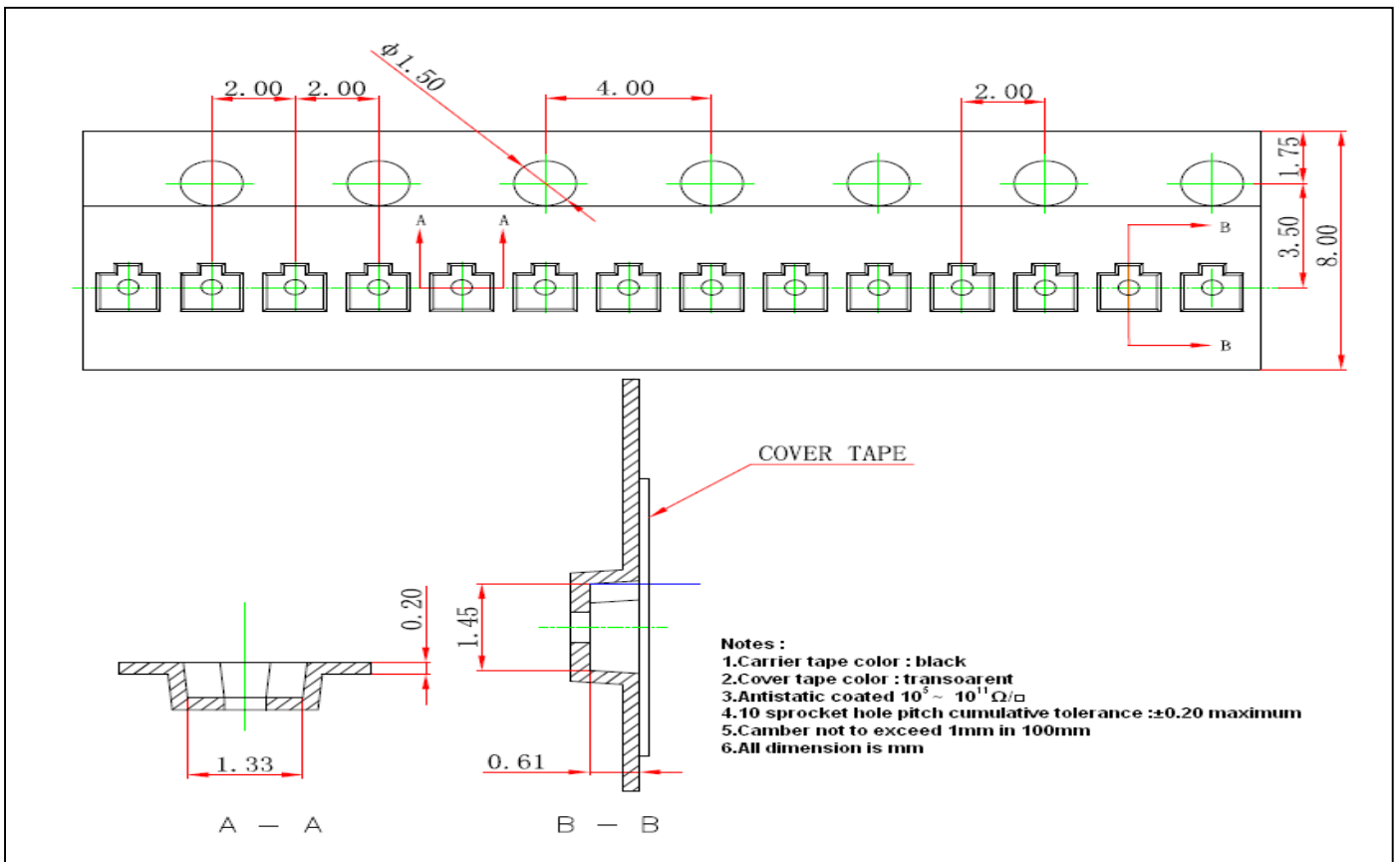
Transient Thermal Response Curves



Reel Dimension



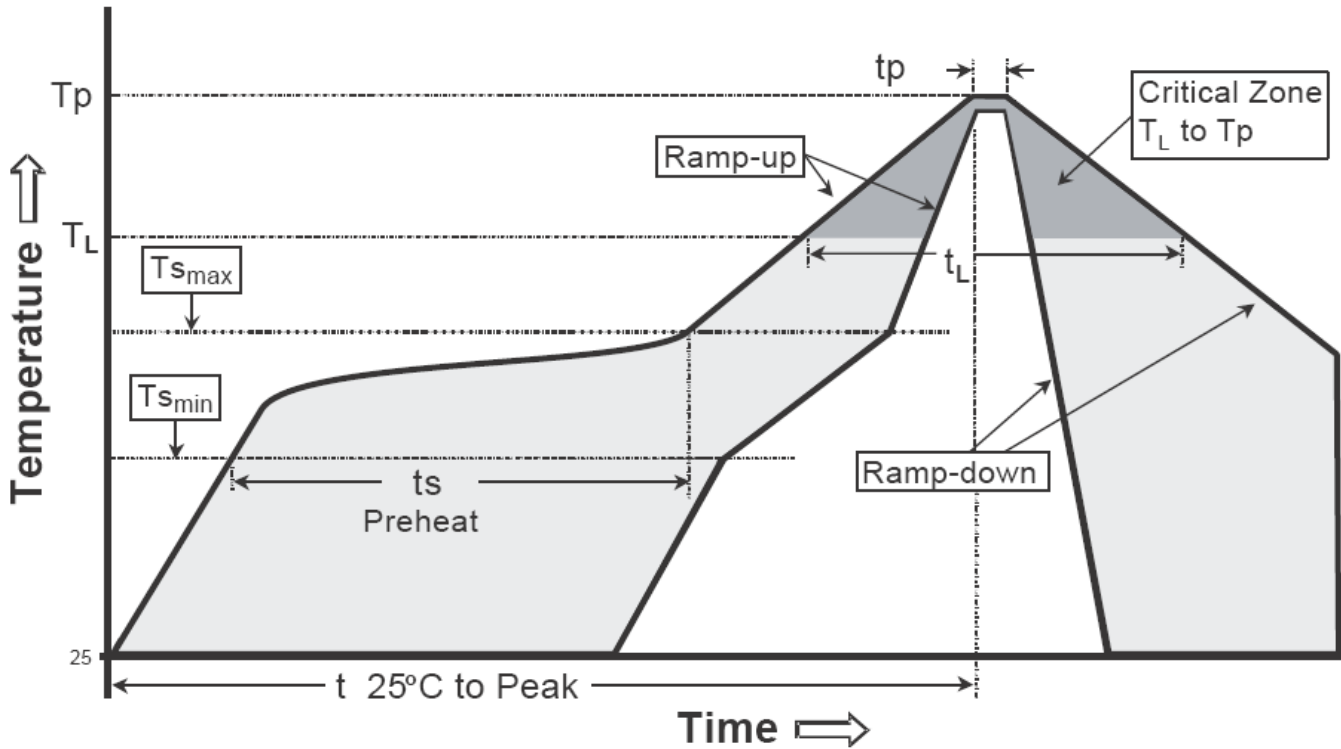
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

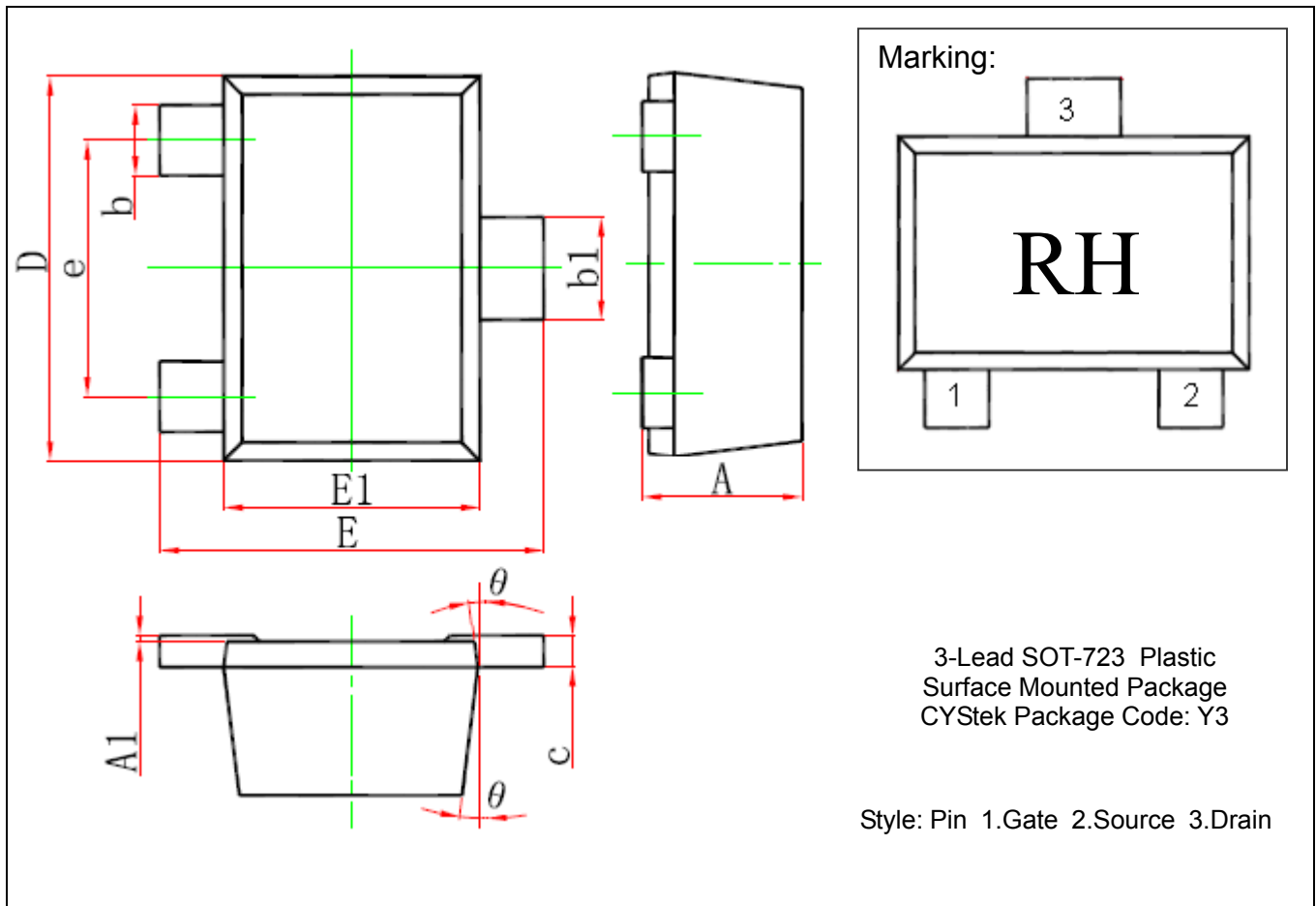
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOT-723 Dimension



*Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.000	0.500	0.000	0.020	D	1.150	1.250	0.045	0.049
A1	0.000	0.050	0.000	0.002	E	1.150	1.250	0.045	0.049
b	0.170	0.270	0.007	0.011	E1	0.750	0.850	0.030	0.033
b1	0.270	0.370	0.011	0.015	e	0.800*		0.031*	
c	0.000	0.150	0.000	0.006	θ	7° REF		7° REF	

- Notes:**
- 1.Controlling dimension: millimeters.
 - 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 - 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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