

N-Channel Enhancement Mode Power MOSFET

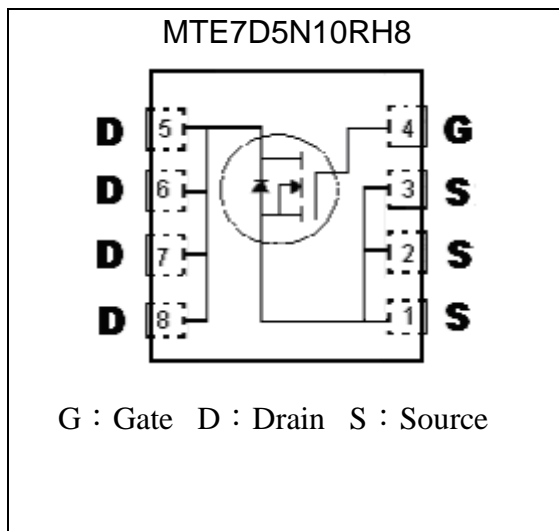
MTE7D5N10RH8

Features

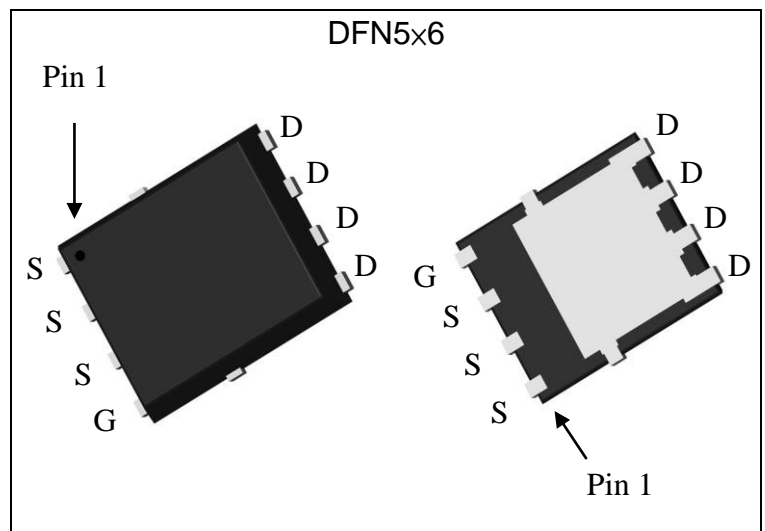
- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

BV_{DSS}	100V
ID@VGS=10V, TC=25°C	55A
ID@VGS=10V, TA=25°C	11A
RDS(ON)@VGS=10V, ID=20A	8mΩ (typ)

Symbol

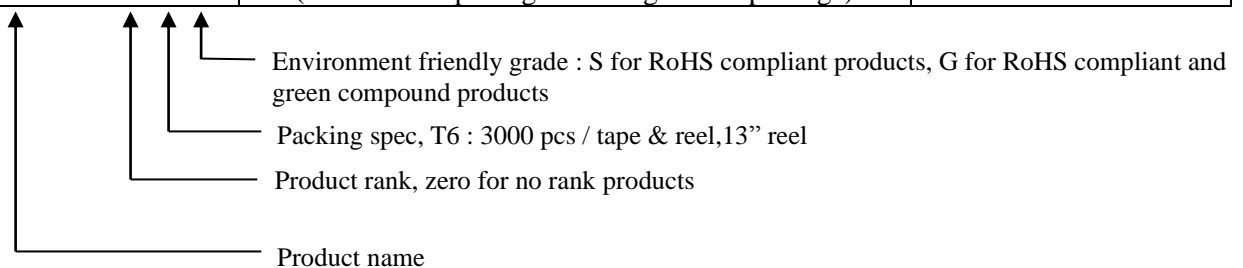


Outline



Ordering Information

Device	Package	Shipping
MTE7D5N10RH8-0-T6-G	DFN 5x6 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel



**Absolute Maximum Ratings (Ta=25°C)**

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	±20	
Continuous Drain Current @ T _C =25°C, V _{GS} =10V		I _D	55	A
Continuous Drain Current @ T _C =100°C, V _{GS} =10V			34.7	
Continuous Drain Current @ T _A =25°C, V _{GS} =10V			11 *2	
Continuous Drain Current @ T _A =70°C, V _{GS} =10V			8.8 *2	
Pulsed Drain Current		I _{DM}	220 *3	
Avalanche Current @ L=0.1mH		I _{AS}	31	
Avalanche Energy @ L=0.5mH, I _D =18A, V _{DD} =25V		E _{AS}	81 *5	mJ
Total Power Dissipation	T _C =25°C	P _D	67 *1	W
	T _C =100°C		26.8 *1	
	T _A =25°C		2.6 *2,4	
	T _A =70°C		1.7 *2,4	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{θJC}	1.86	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{θJA}	47.5 *4	

- Note : 1. The power dissipation P_D is based on T_{J(MAX)}=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The power dissipation P_{DSM} is based on R_{θJA} and the maximum allowed junction temperature of 150°C.
3. Ratings are based on low frequency and low duty cycles to keep initial T_J=25°C.
4. When mounted on 1 in² copper pad of FR-4 board ; 125°C/W when mounted on minimum copper pad.
5. 100% tested by conditions of L=0.5mH, I_{AS}=7A, V_{GS}=10V, V_{DD}=25V

Characteristics (T_J=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.06	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	2	-	4	V	V _{DS} =V _{GS} , I _D =250μA
*G _{FS}	-	27.7	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
I _{DSS}	-	-	1	μA	V _{DS} =80V, V _{GS} =0V
	-	-	5		V _{DS} =80V, V _{GS} =0V, T _j =55°C
*R _{DS(ON)}	-	8	10.5	mΩ	V _{GS} =10V, I _D =20A

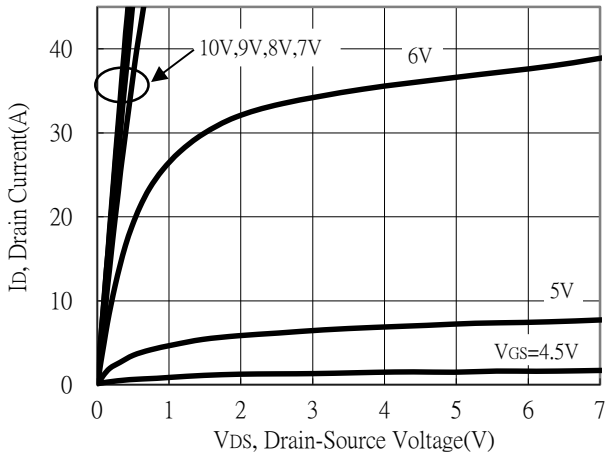


Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Dynamic					
*Qg	-	40.6	-	nC	V _{DS} =50V, I _D =20A, V _{GS} =10V
*Qgs	-	14.8	-		
*Qgd	-	8.4	-		
*t _{d(ON)}	-	28.6	-	ns	V _{DS} =50V, I _D =20A, V _{GS} =10V, R _{GS} =6Ω
*tr	-	20.2	-		
*t _{d(OFF)}	-	47.8	-		
*t _f	-	13.6	-		
C _{iss}	-	2808	-	pF	V _{GS} =0V, V _{DS} =50V, f=1MHz
C _{oss}	-	317	-		
C _{rss}	-	25.8	-		
R _g	-	0.94	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	55	A	
*I _{SM}	-	-	220		
*V _{SD}	-	0.84	1.2	V	I _S =20A, V _{GS} =0V
*trr	-	44	-	ns	V _{GS} =0V, I _F =20A, dI _F /dt=100A/μs
*Qrr	-	76	-	nC	

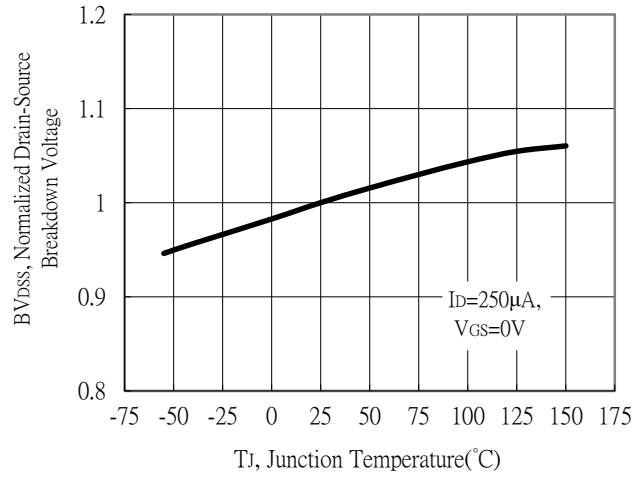
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

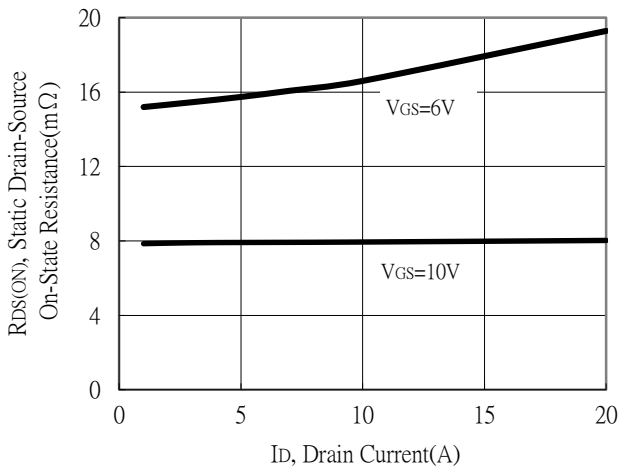
Typical Output Characteristics



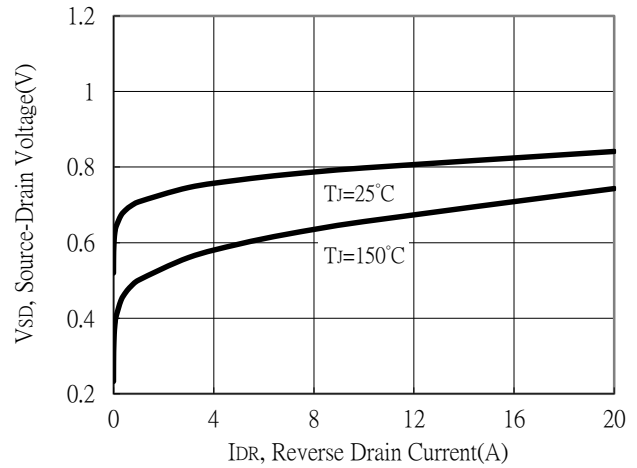
Breakdown Voltage vs Ambient Temperature



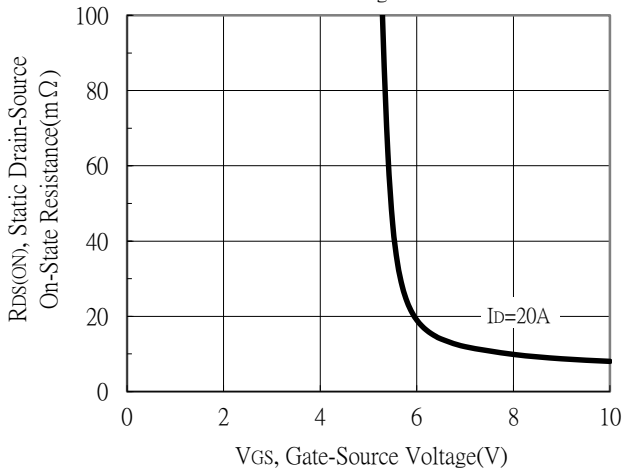
Static Drain-Source On-State resistance vs Drain Current



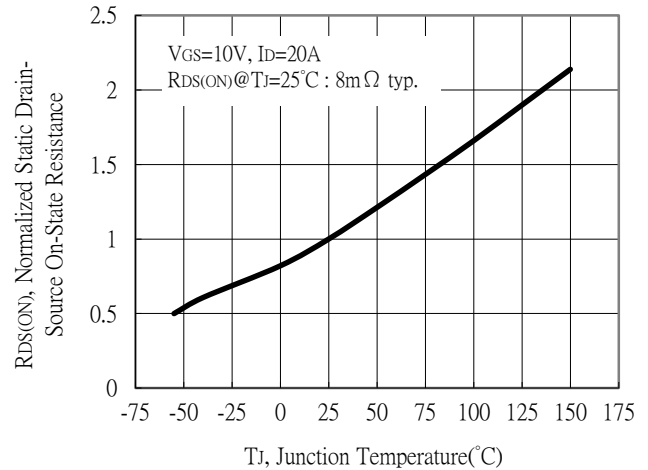
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

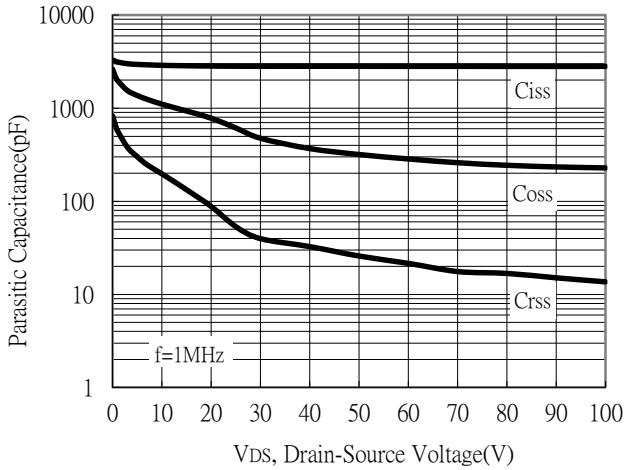


Drain-Source On-State Resistance vs Junction Temperature

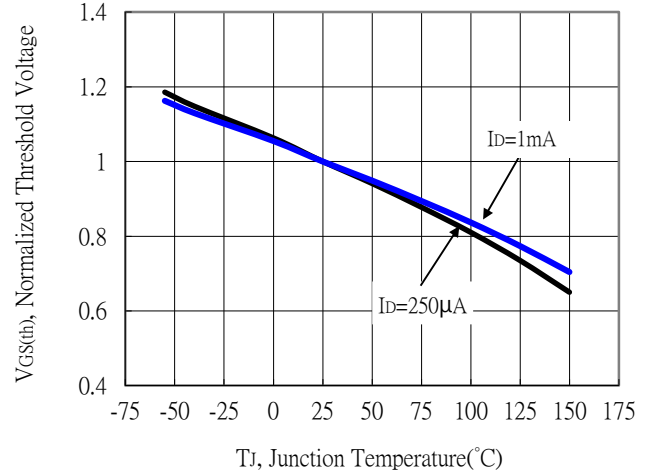


Typical Characteristics(Cont.)

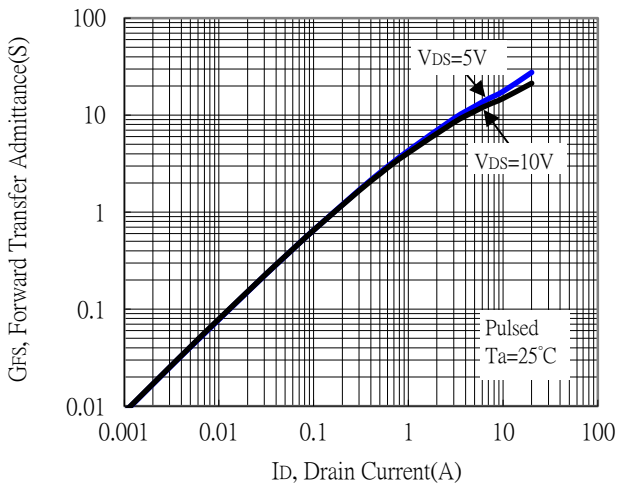
Capacitance vs Drain-to-Source Voltage



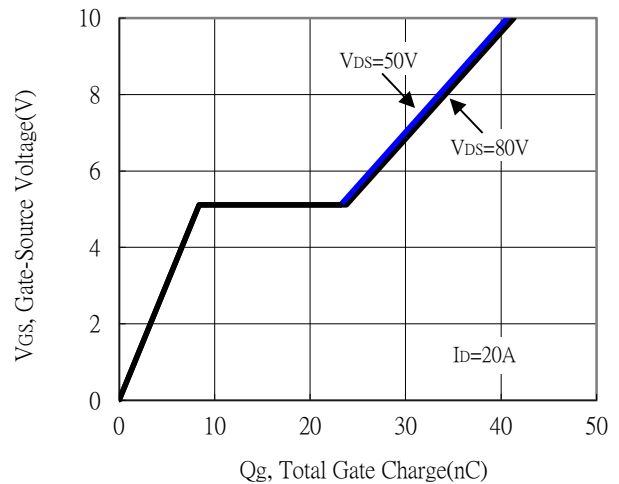
Threshold Voltage vs Junction Temperature



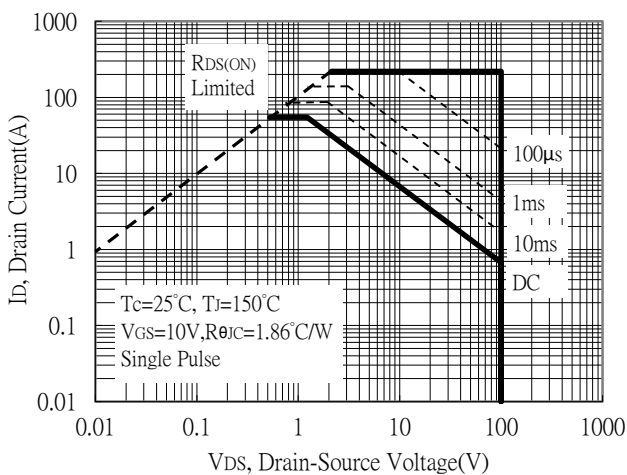
Forward Transfer Admittance vs Drain Current



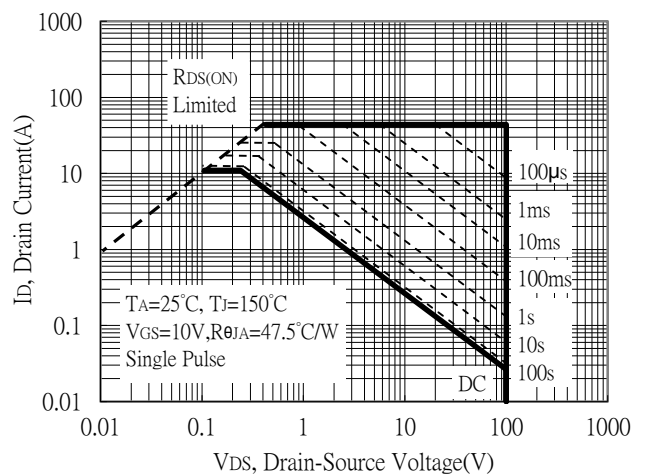
Gate Charge Characteristics



Maximum Safe Operating Area



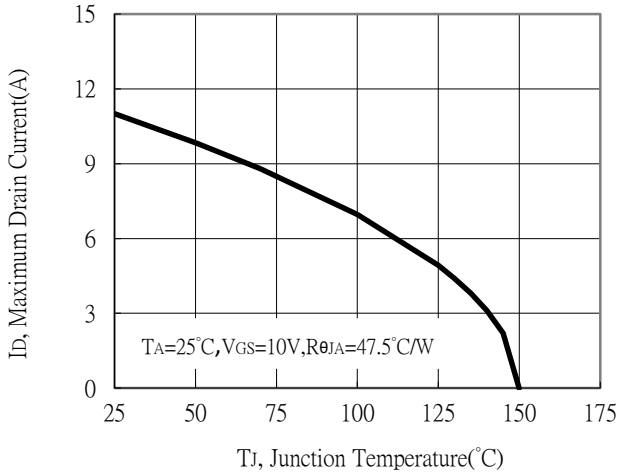
Maximum Safe Operating Area



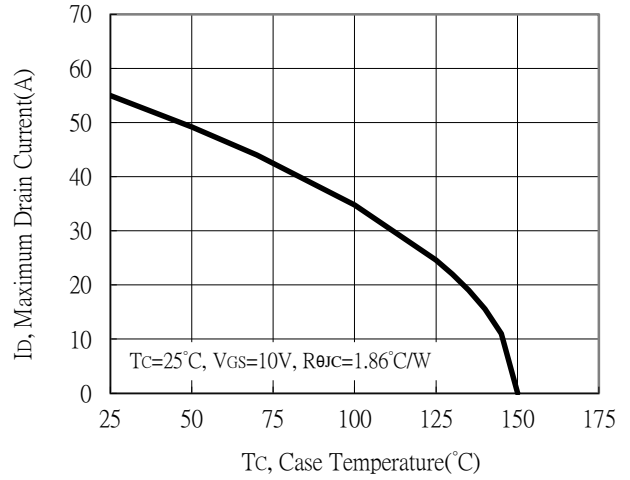


Typical Characteristics (Cont.)

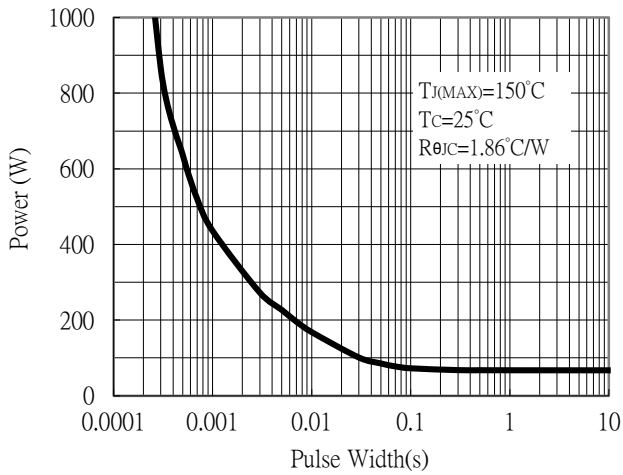
Maximum Drain Current vs Junction Temperature



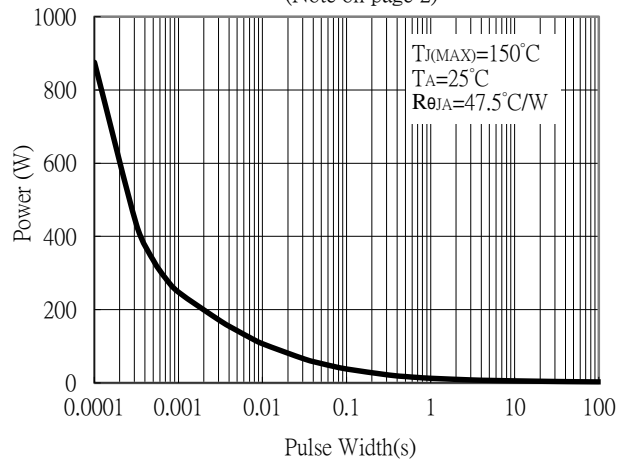
Maximum Drain Current vs Case Temperature



Single Pulse Maximum Power Dissipation

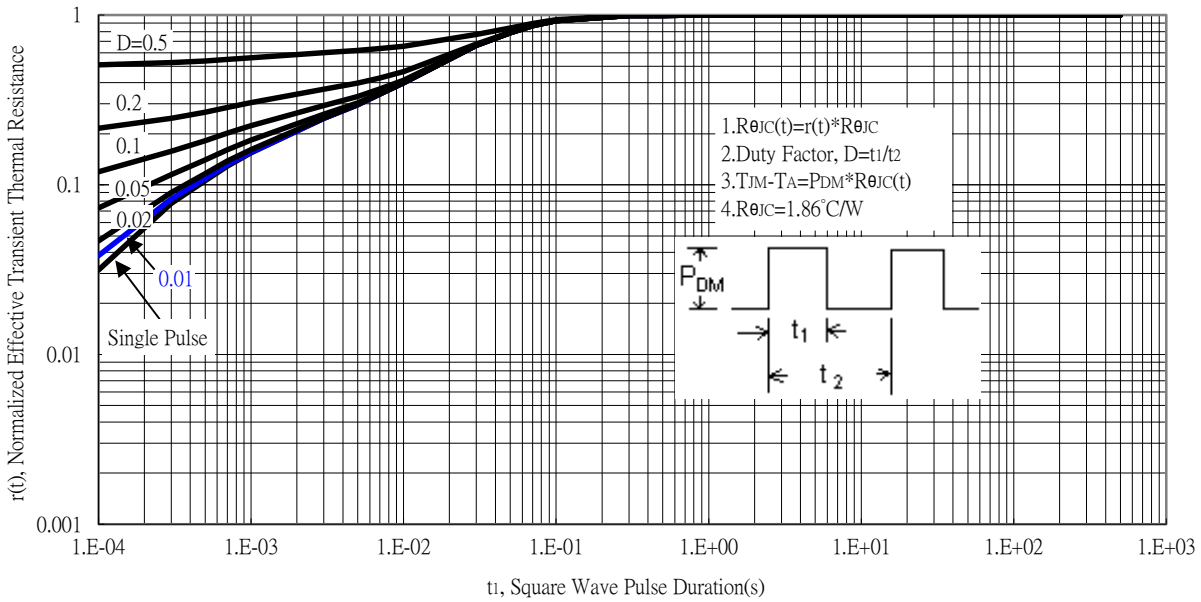


Single Pulse Power Rating, Junction to Ambient
(Note on page 2)

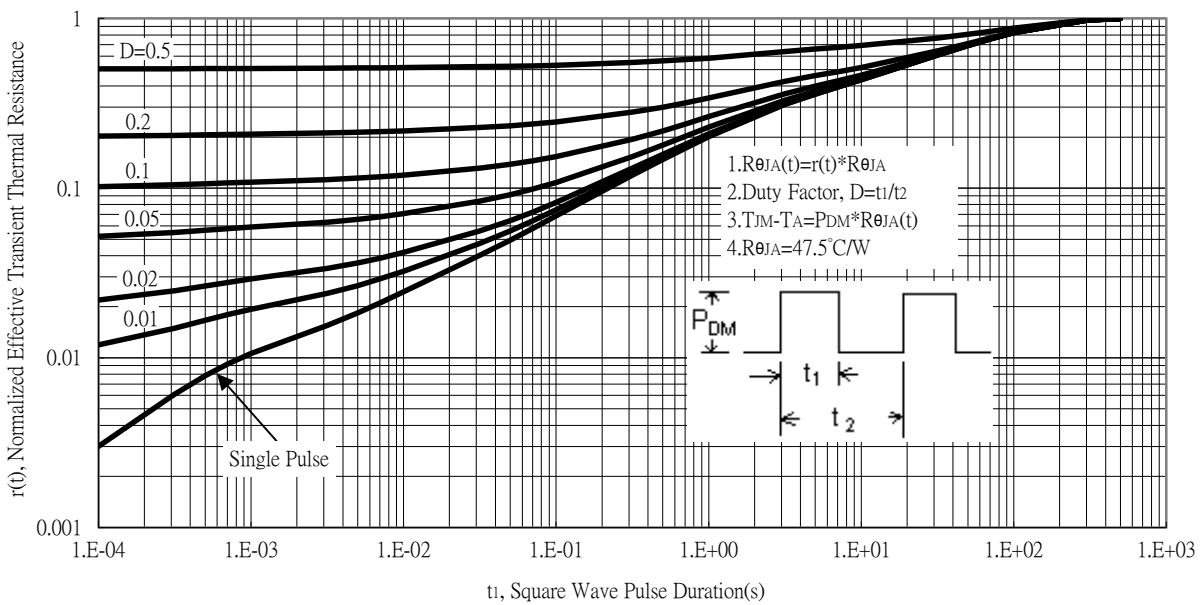


Typical Characteristics (Cont.)

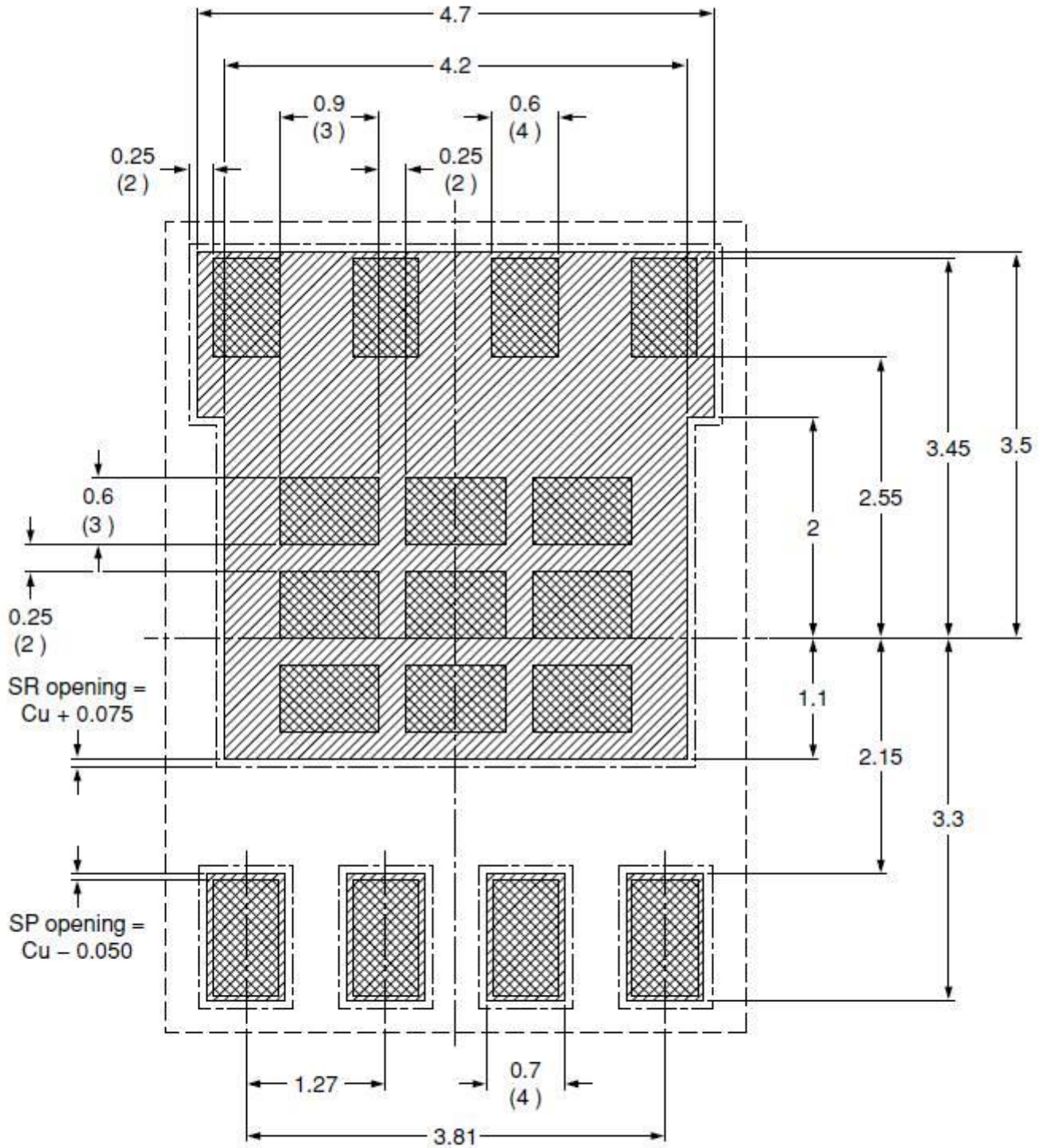
Transient Thermal Response Curves



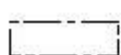



Transient Thermal Response Curves



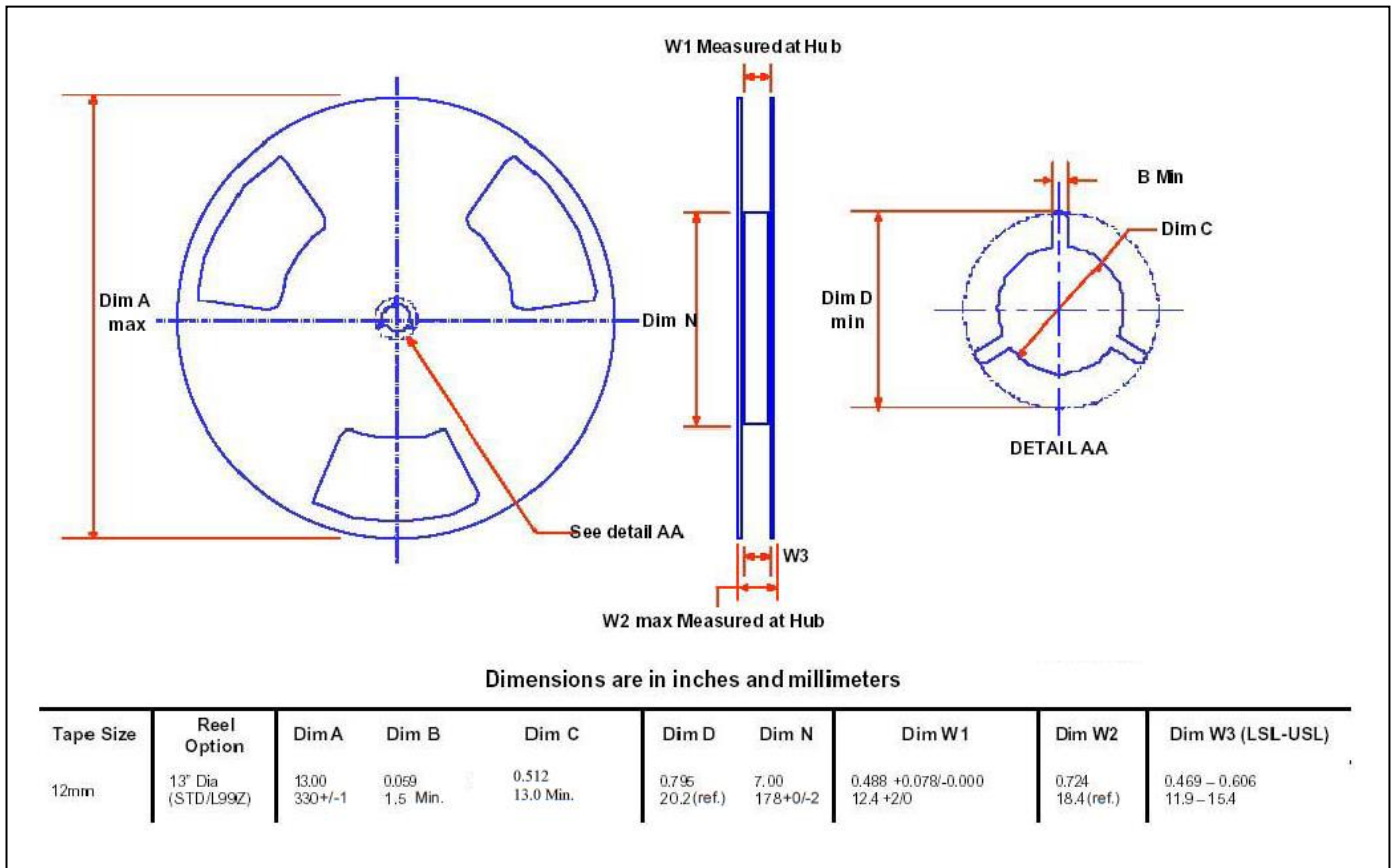
Recommended Soldering Footprint & Stencil Design



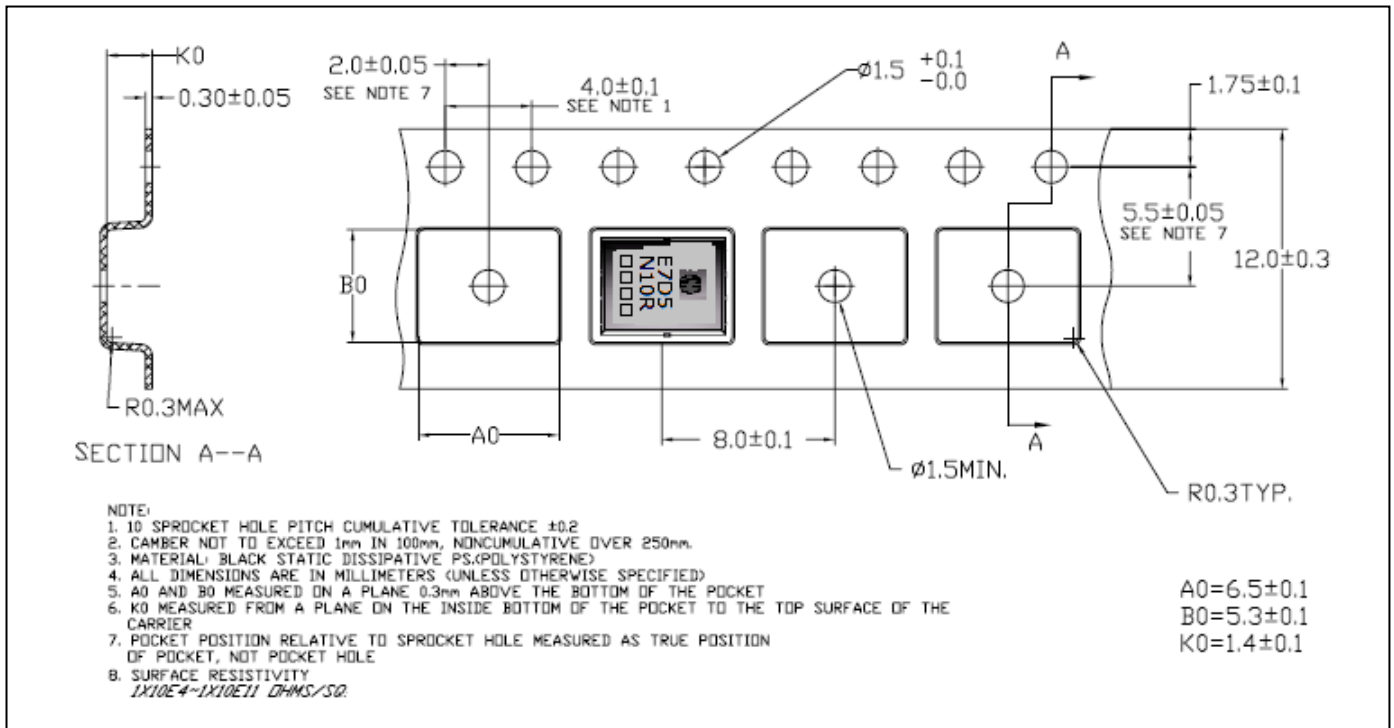
- | | |
|---|--|
|  solder lands |  solder paste
125 μm stencil |
|  solder resist |  occupied area |

unit : mm

Reel Dimension



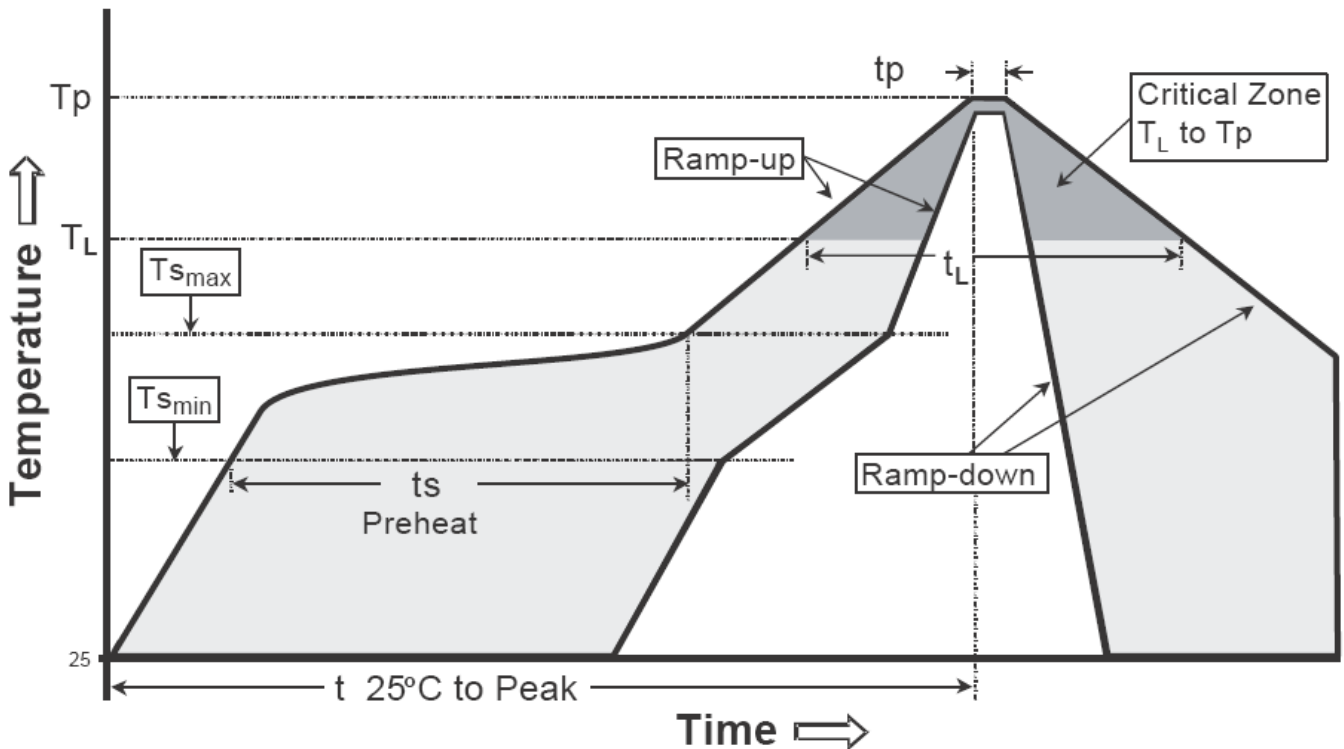
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

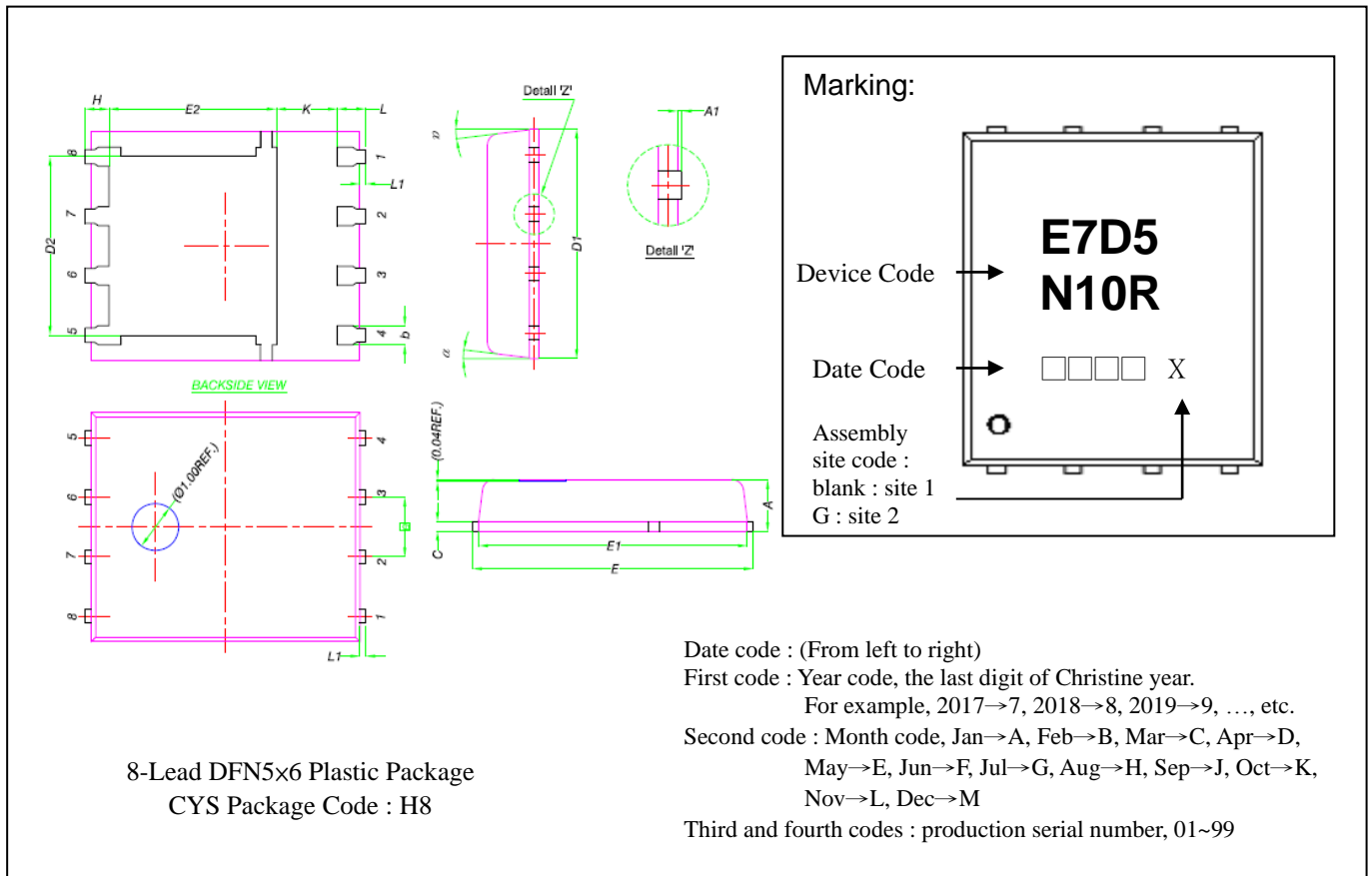
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN5x6 Dimension



8-Lead DFN5x6 Plastic Package
 CYS Package Code : H8

Marking:

Device Code → **E7D5**
 N10R

Date Code → □□□□ X

Assembly site code :
 blank : site 1
 G : site 2

Date code : (From left to right)
 First code : Year code, the last digit of Christine year.
 For example, 2017→7, 2018→8, 2019→9, ..., etc.
 Second code : Month code, Jan→A, Feb→B, Mar→C, Apr→D,
 May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K,
 Nov→L, Dec→M
 Third and fourth codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.90	1.10	0.035	0.043	E2	3.37	3.78	0.133	0.149
A1	0.00	0.05	0.000	0.002	e	1.27	BSC	0.050	BSC
b	0.33	0.51	0.013	0.020	H	0.41	0.73	0.016	0.029
C	0.20	0.30	0.008	0.012	K	1.10	-	0.043	-
D1	4.80	5.00	0.189	0.197	L	0.51	0.71	0.020	0.028
D2	3.67	4.11	0.144	0.162	L1	0.06	0.20	0.002	0.008
E	5.90	6.13	0.232	0.241	θ	8°	12°	8°	12°
E1	5.67	5.83	0.223	0.230					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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