

N-Channel Enhancement Mode Power MOSFET

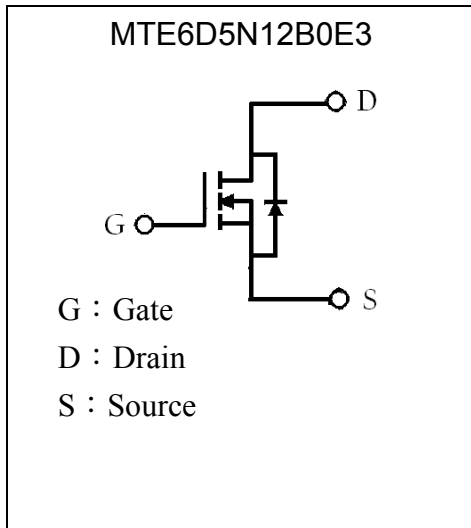
MTE6D5N12B0E3

BV_{DSS}	120V
$I_D @ V_{GS}=10V, T_C=25^\circ C$	129A
$R_{DS(on)(TYP)} @ V_{GS}=10V, I_D=50A$	7.9mΩ
$R_{DS(on)(TYP)} @ V_{GS}=7V, I_D=20A$	8.5mΩ

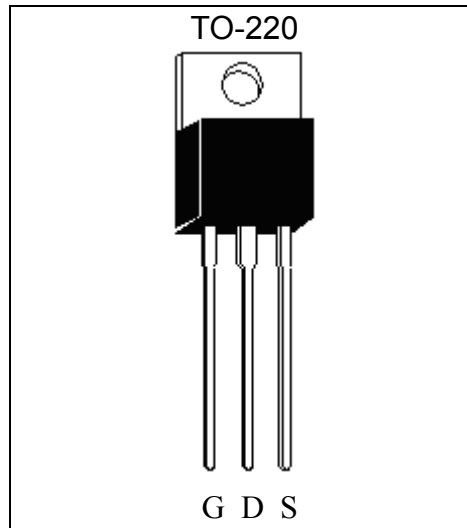
Features

- Low Gate Charge
- Simple Drive Requirement
- Repetitive Avalanche Rated
- Fast Switching Characteristic
- RoHS compliant package

Symbol

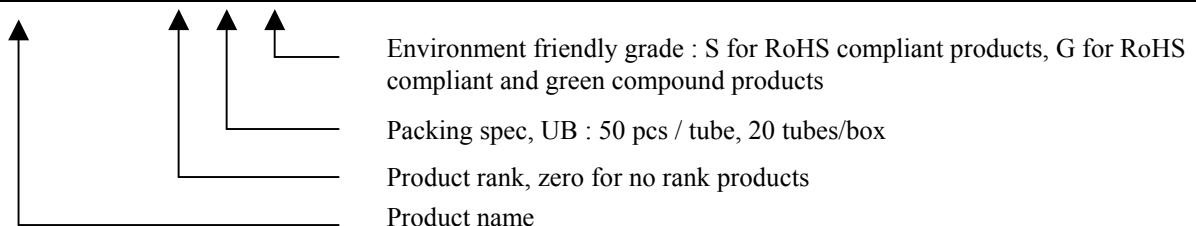


Outline



Ordering Information

Device	Package	Shipping
MTE6D5N12B0E3-0-UB-X	TO-220 (Pb-free lead plating package)	50 pcs/tube, 20 tubes/box, 4 boxes / carton



**Absolute Maximum Ratings** ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V_{DS}	120	V	
Gate-Source Voltage	V_{GS}	± 30		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (silicon limit)	I_D	129	A	
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (silicon limit)		91		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (package limit) (Note 1)		86		
Pulsed Drain Current (Note 3)	I_{DM}	400		
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 2)	I_{DSM}	10		
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 2)		8		
Avalanche Current (Note 3)	I_{AS}	25		
Avalanche Energy @ $L=1\text{mH}$, $I_D=20\text{A}$, $R_G=25\Omega$ (Note 2)	E_{AS}	400	mJ	
Repetitive Avalanche Energy @ $L=0.1\text{mH}$ (Note 3)	E_{AR}	33		
Power Dissipation	P_D	$T_C=25^{\circ}\text{C}$ (Note 1)	333	W
		$T_C=100^{\circ}\text{C}$ (Note 1)	167	
Power Dissipation	P_{DSM}	$T_A=25^{\circ}\text{C}$ (Note 2)	2	W
		$T_A=70^{\circ}\text{C}$ (Note 2)	1.3	
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+175	$^{\circ}\text{C}$	

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.45	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max, $t \leq 10\text{s}$ (Note 1)	$R_{th,j-a}$	15	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note 1)		62.5	$^{\circ}\text{C}/\text{W}$

- Note : 1. The power dissipation P_D is based on $T_{J(MAX)}=175^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=175^{\circ}\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^{\circ}\text{C}$.
4. The maximum current limited by package is 100A.
5. The static characteristics are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% maximum.
6. The $R_{\theta JA}$ is the sum of thermal resistance from junction to case $R_{\theta JC}$ and case to ambient.



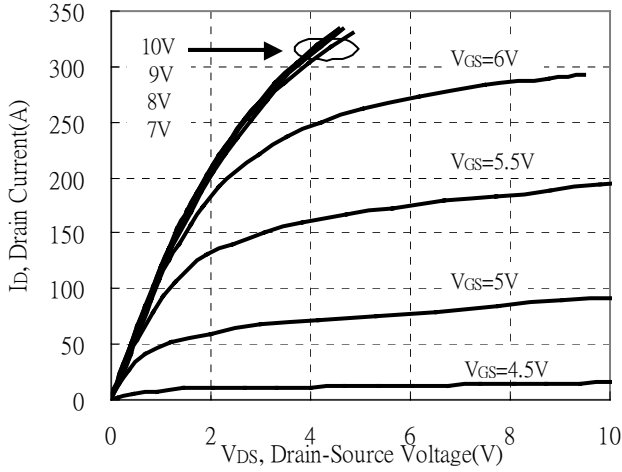
Characteristics (Tc=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	120	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2.0	2.9	4.0		V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	50	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±30V
I _{DSS}	-	-	1	μA	V _{DS} =100V, V _{GS} =0V
	-	-	25		V _{DS} =100V, V _{GS} =0V, T _j =125°C
*R _{DS(ON)}	-	7.9	9.6	mΩ	V _{GS} =10V, I _D =50A
	-	8.5	16		V _{GS} =7V, I _D =20A
Dynamic					
*Q _g	-	119	-	nC	I _D =100A, V _{DS} =60V, V _{GS} =10V
*Q _{gs}	-	29	-		
*Q _{gd}	-	44	-		
*t _{d(ON)}	-	35	-	ns	V _{DS} =60V, I _D =20A, V _{GS} =10V, R _G =3Ω
*t _r	-	39	-		
*t _{d(OFF)}	-	95	-		
*t _f	-	61	-		
C _{iss}	-	7068	-	pF	V _{GS} =0V, V _{DS} =30V, f=1MHz
C _{oss}	-	593	-		
C _{rss}	-	481	-		
R _g	-	1.9	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	100	A	
*V _{SD}	-	0.78	1	V	I _S =20A, V _{GS} =0V
*t _{rr}	-	36	-	ns	I _F =20A, V _{GS} =0V, dI/dt=100A/μs
*Q _{rr}	-	142	-	nC	

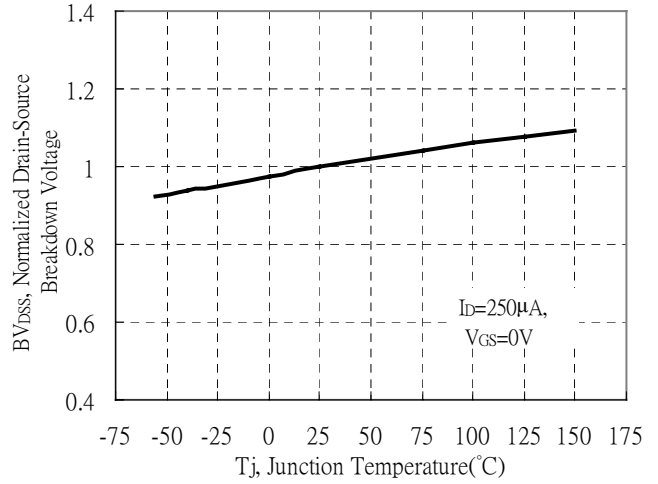
*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Typical Characteristics

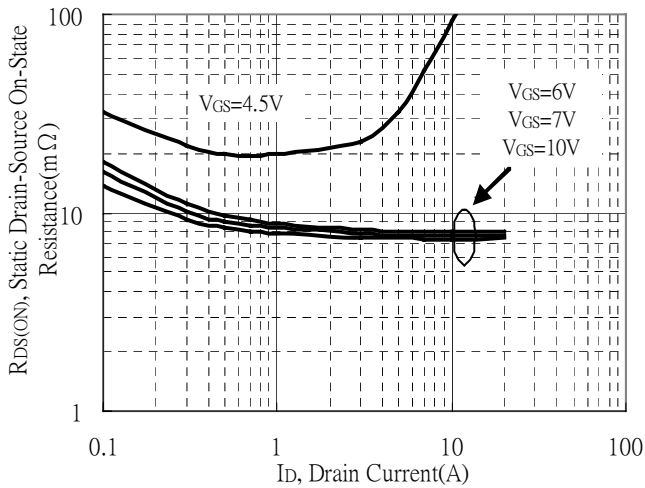
Typical Output Characteristics



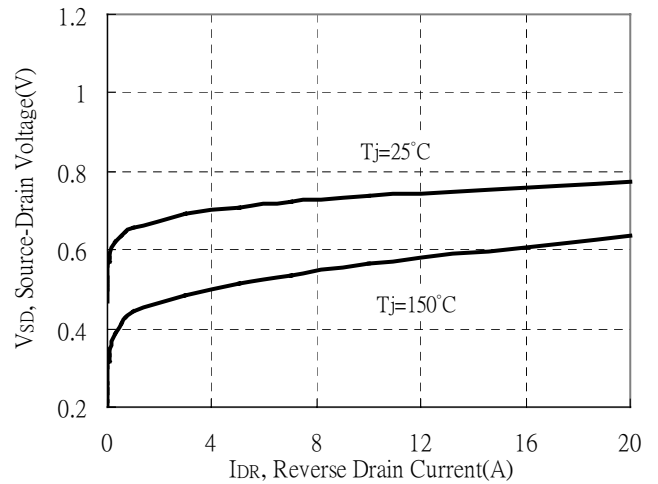
Brekdown Voltage vs Junction Temperature



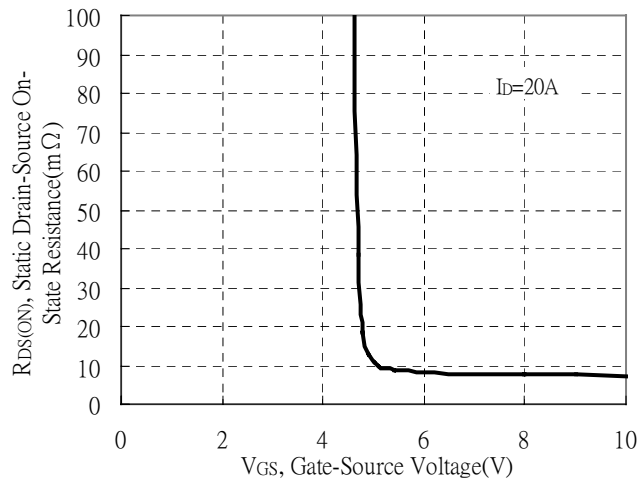
Static Drain-Source On-State resistance vs Drain Current



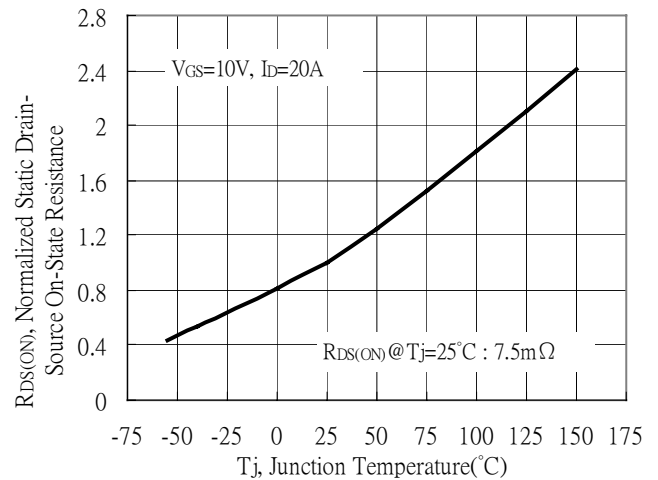
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



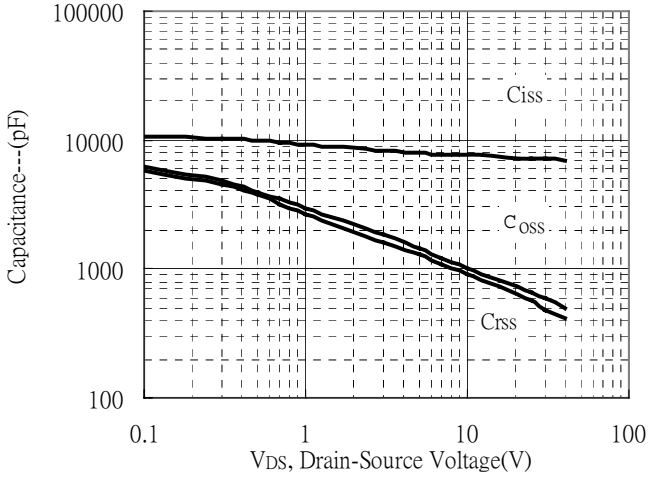
Drain-Source On-State Resistance vs Junction Temperature



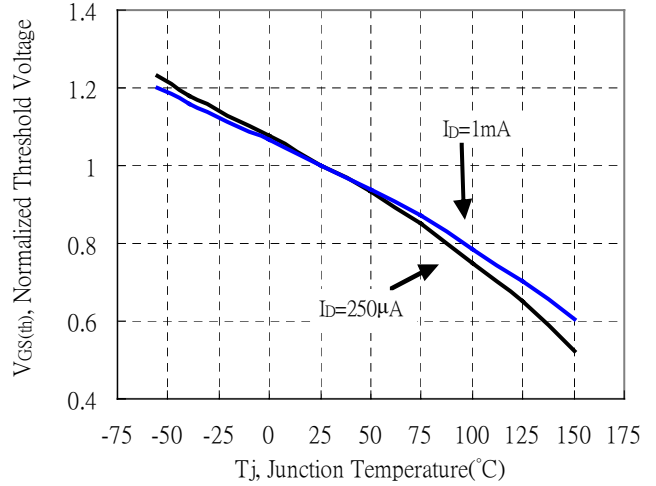


Typical Characteristics(Cont.)

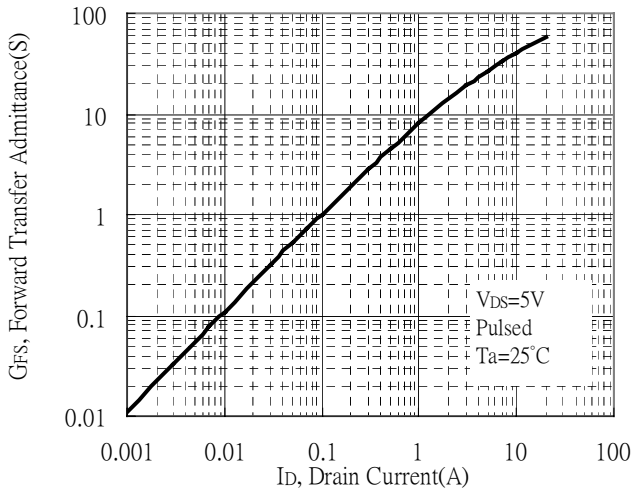
Capacitance vs Drain-to-Source Voltage



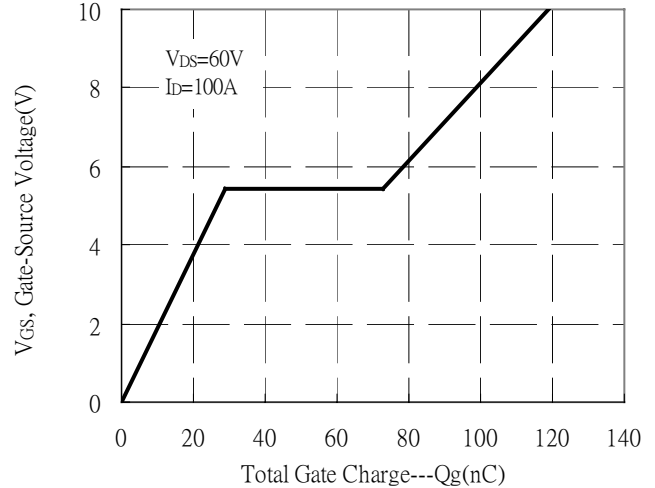
Threshold Voltage vs Junction Temperature



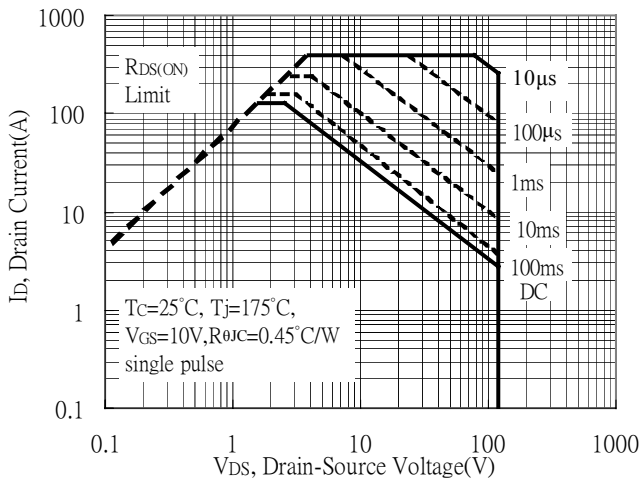
Forward Transfer Admittance vs Drain Current



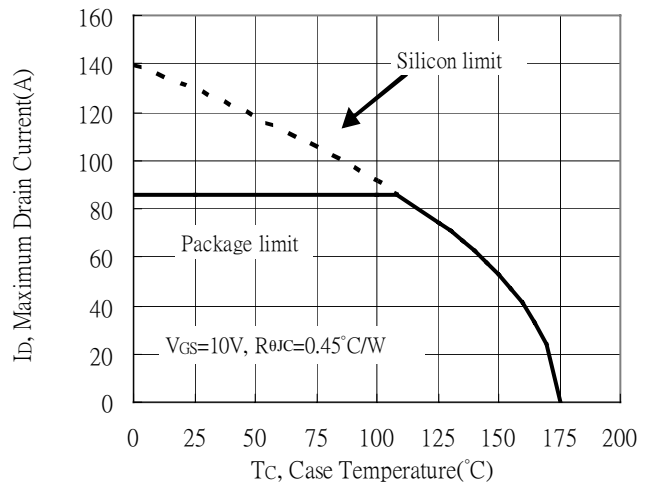
Gate Charge Characteristics



Maximum Safe Operating Area



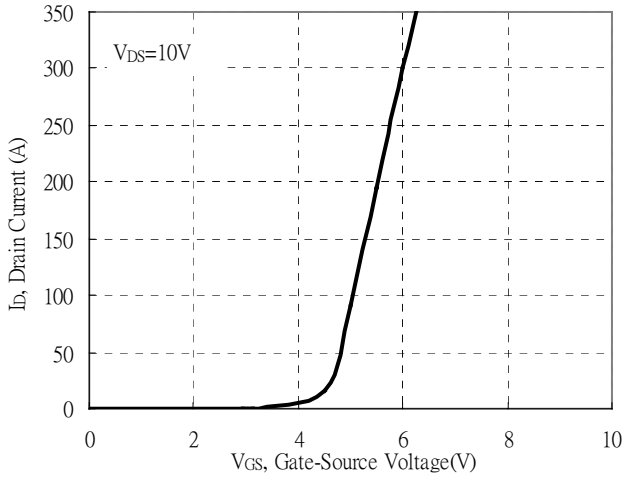
Maximum Drain Current vs Case Temperature



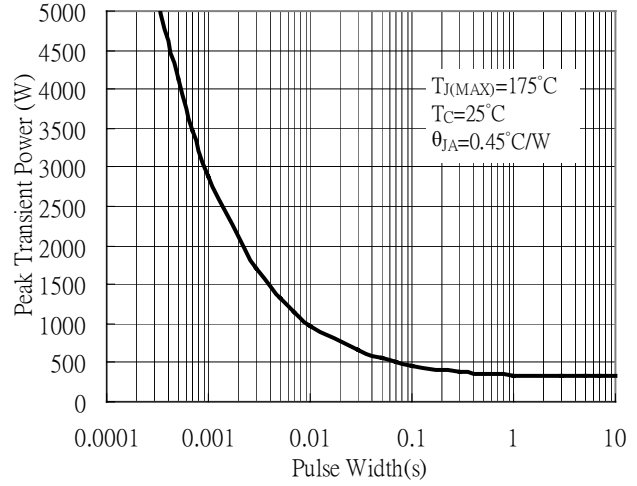


Typical Characteristics(Cont.)

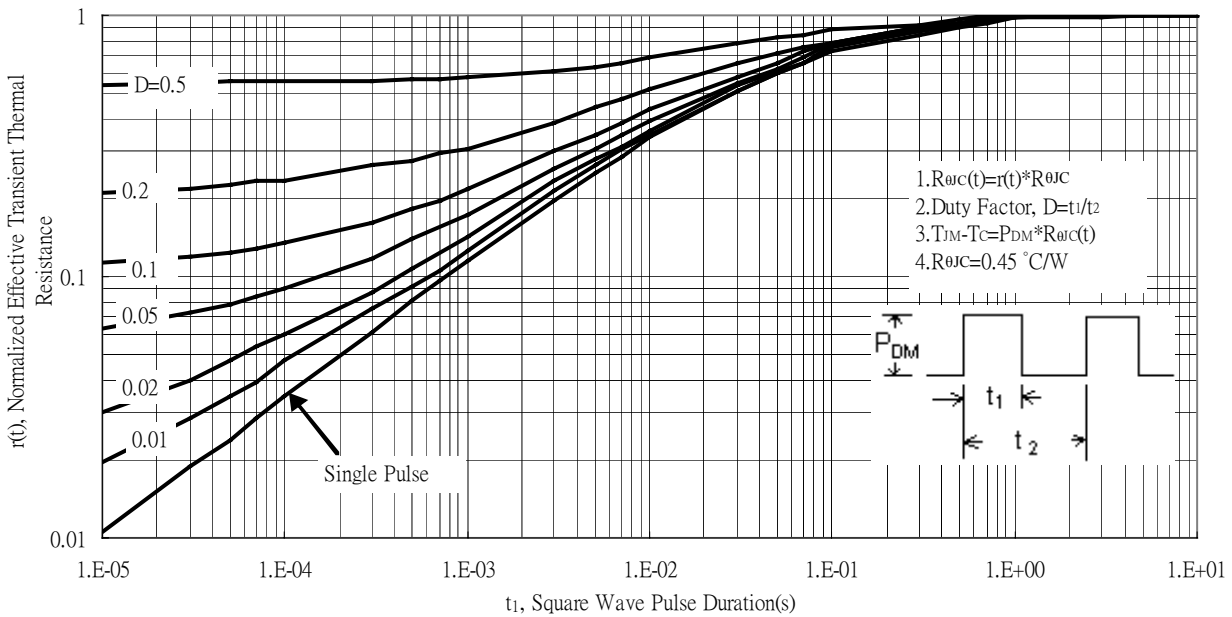
Typical Transfer Characteristics



Single Pulse Maximum Power Dissipation

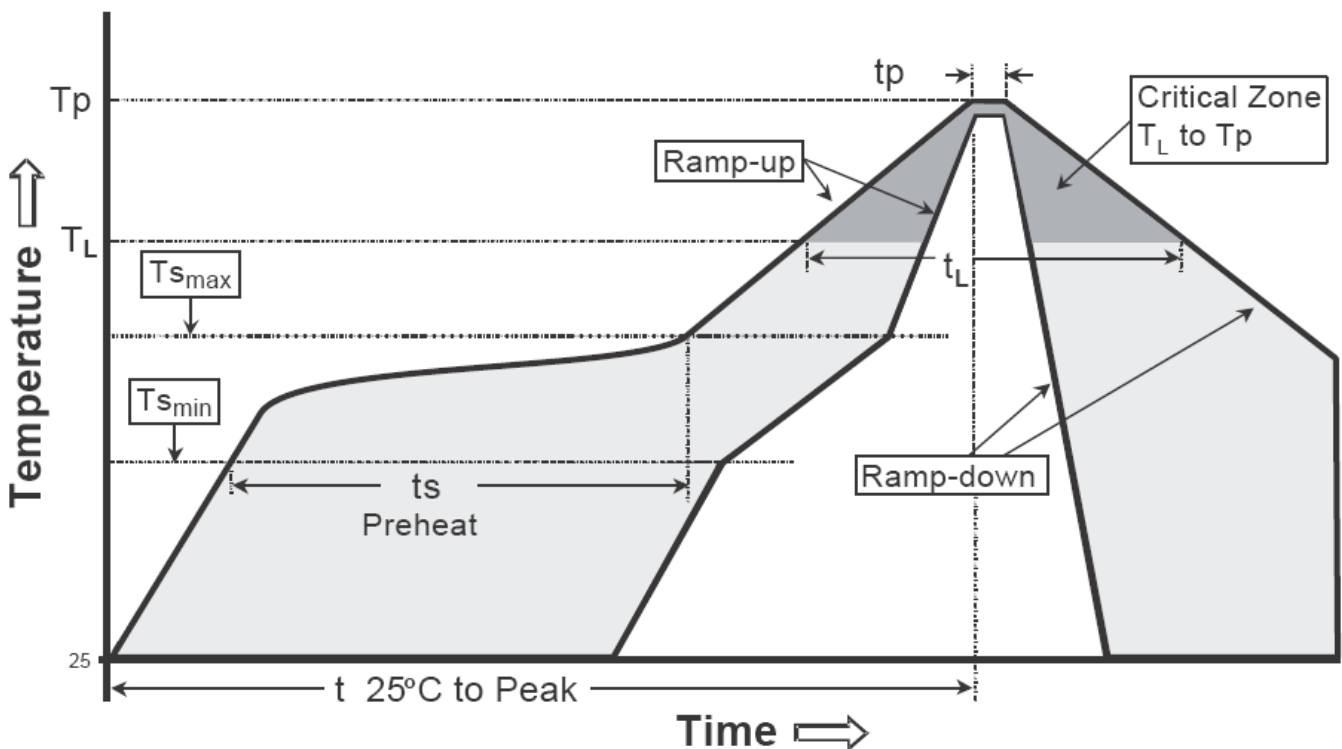


Transient Thermal Response Curves



Recommended wave soldering condition

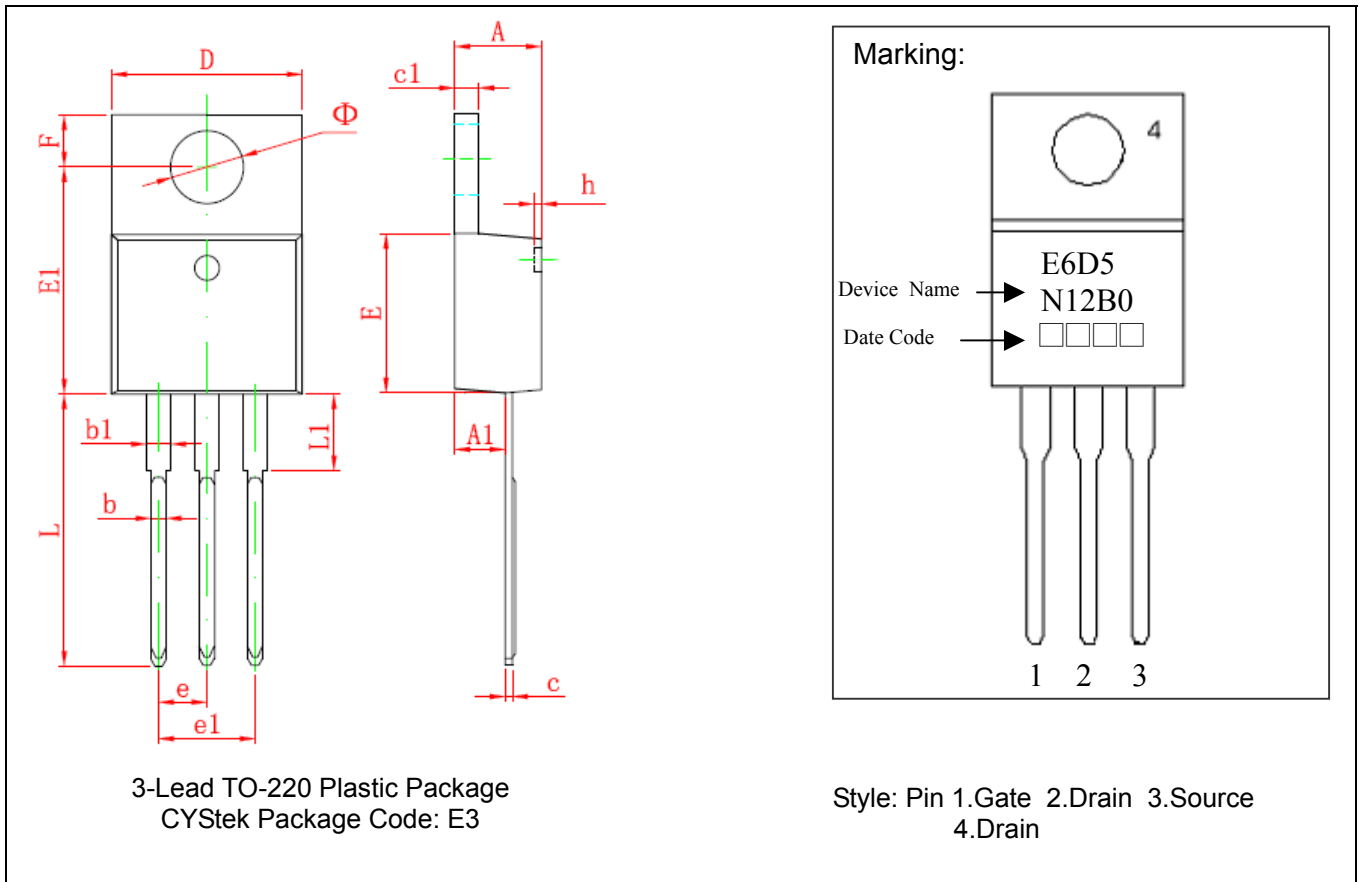
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-220 Dimension



3-Lead TO-220 Plastic Package
 CYStek Package Code: E3

Style: Pin 1.Gate 2.Drain 3.Source
 4.Drain

*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	E1	12.060	12.460	0.475	0.491
A1	2.520	2.820	0.099	0.111	e	2.540*		0.100*	
b	0.710	0.910	0.028	0.036	e1	4.980	5.180	0.196	0.204
b1	1.170	1.370	0.046	0.054	F	2.590	2.890	0.102	0.114
c	0.310	0.530	0.012	0.021	h	0.000	0.300	0.000	0.012
c1	1.170	1.370	0.046	0.054	L	13.400	13.800	0.528	0.543
D	10.010	10.310	0.394	0.406	L1	3.560	3.960	0.140	0.156
E	8.500	8.900	0.335	0.350	Φ	3.735	3.935	0.147	0.155

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.