

# N-Channel Enhancement Mode Power MOSFET

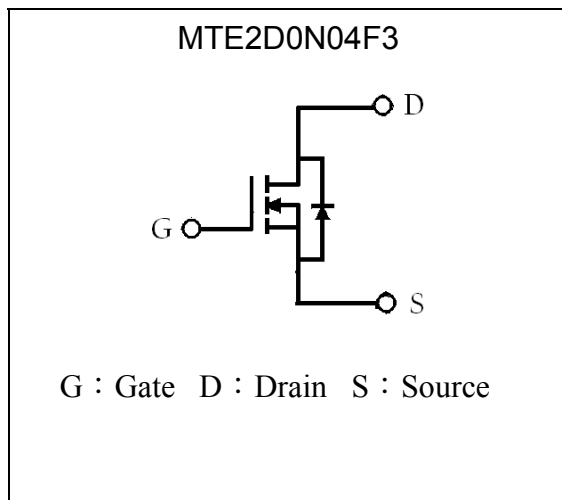
## MTE2D0N04F3

<b>BV<sub>DSS</sub></b>	<b>40V</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>C</sub>=25°C</b>	<b>84A</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>A</sub>=25°C</b>	<b>17.3A</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=10V, I<sub>D</sub>=20A</b>	<b>2.1 mΩ (typ)</b>

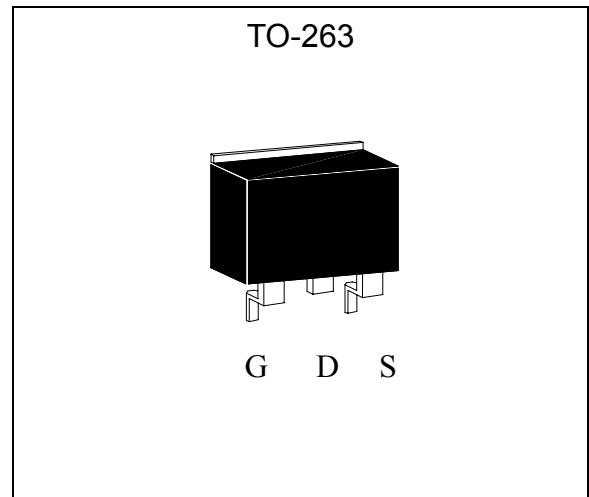
### Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

### Symbol

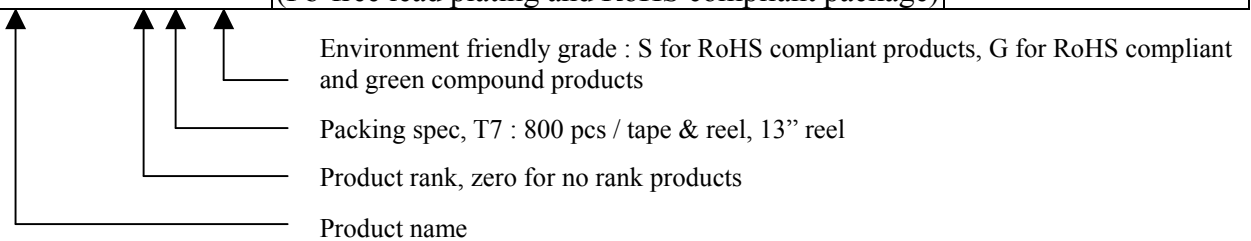


### Outline



### Ordering Information

Device	Package	Shipping
MTE2D0N04F3-0-T7-X	TO-263 (Pb-free lead plating and RoHS compliant package)	800 pcs / Tape & Reel



**Absolute Maximum Ratings** ( $T_C=25^{\circ}\text{C}$ )

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage (Note 1)	$V_{DS}$	40	V	
Gate-Source Voltage	$V_{GS}$	$\pm 30$		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (silicon limit) (Note 5)	$I_D$	160	A	
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (silicon limit) (Note 5)		113		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (package limit) (Note 1)		84		
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)	$I_{DSM}$	17.3		
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)		13.8		
Pulsed Drain Current	$I_{DM}$	584		
Single Pulse Avalanche Current	$I_{AS}$	50		
Single Pulse Avalanche Energy @ $L=1\text{mH}$ , $I_D=50\text{Amps}$ , $V_{DD}=30\text{V}$ (Note 4)	$E_{AS}$	1250	mJ	
Repetitive Avalanche Energy (Note 3)	$E_{AR}$	16		
Power Dissipation	$P_D$	$T_C=25^{\circ}\text{C}$ (Note 1)	166	W
		$T_C=100^{\circ}\text{C}$ (Note 1)	83	
	$P_{DSM}$	$T_A=25^{\circ}\text{C}$ (Note 2)	2.1	
		$T_A=70^{\circ}\text{C}$ (Note 2)	1.3	
Maximum Temperature for Soldering @ Lead at 0.063 in(1.6mm) from case for 10 seconds	$T_L$	300	$^{\circ}\text{C}$	
Maximum Temperature for Soldering @ Package Body for 10 seconds	$T_{PKG}$	260		
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+175		

\*Drain current limited by maximum junction temperature

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	0.9	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note 2)	$R_{\theta JA}$	60	

- Note : 1. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=175^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design, and the maximum temperature of  $175^{\circ}\text{C}$  may be used if the PCB allows it.
3. Pulse width limited by junction temperature  $T_{J(MAX)}=175^{\circ}\text{C}$ .
4. Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ . 100% tested by conditions of  $V_{DD}=30\text{V}$ ,  $I_D=20\text{A}$ ,  $L=1\text{mH}$ ,  $V_{GS}=10\text{V}$ .
5. Calculated continuous drain current based on maximum allowable junction temperature.
6. The static characteristics are obtained using  $<300\mu\text{s}$  pulses, duty cycle 0.5% maximum.
7. The  $R_{\theta JA}$  is the sum of thermal resistance from junction to case  $R_{\theta JC}$  and case to ambient.



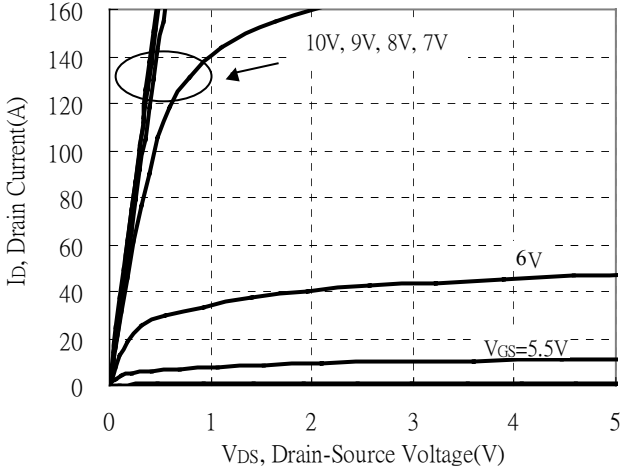
**Characteristics (Tj=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	40	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	32	-	mV/°C	Reference to 25°C, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	-	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
*G <sub>FS</sub>	-	31.5	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±30V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V
	-	-	5		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>j</sub> =55°C
*R <sub>DS(ON)</sub>	-	2.1	2.8	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =20A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	113	-	nC	V <sub>DS</sub> =20V, V <sub>GS</sub> =10V, I <sub>D</sub> =84A
*Q <sub>gs</sub>	-	33	-		
*Q <sub>gd</sub>	-	37	-		
*t <sub>d(ON)</sub>	-	43.8	-	ns	V <sub>DS</sub> =20V, I <sub>D</sub> =20A, V <sub>GS</sub> =10V, R <sub>GS</sub> =2.7Ω
*t <sub>r</sub>	-	34	-		
*t <sub>d(OFF)</sub>	-	74.8	-		
*t <sub>f</sub>	-	23.2	-		
C <sub>iss</sub>	-	5848	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz
C <sub>oss</sub>	-	851	-		
C <sub>rss</sub>	-	390	-		
R <sub>g</sub>	-	0.6	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	84	A	
*I <sub>SM</sub>	-	-	584		
*V <sub>SD</sub>	-	0.69	1	V	I <sub>S</sub> =1A, V <sub>GS</sub> =0V
*t <sub>rr</sub>	-	18	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =20A, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	36	-	nC	

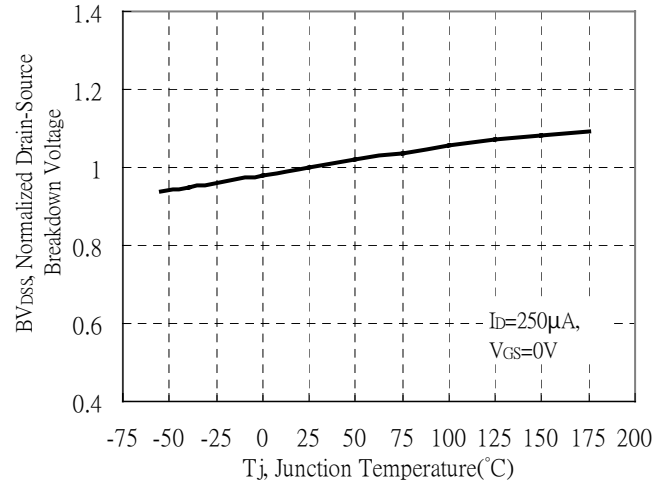
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

**Typical Characteristics**

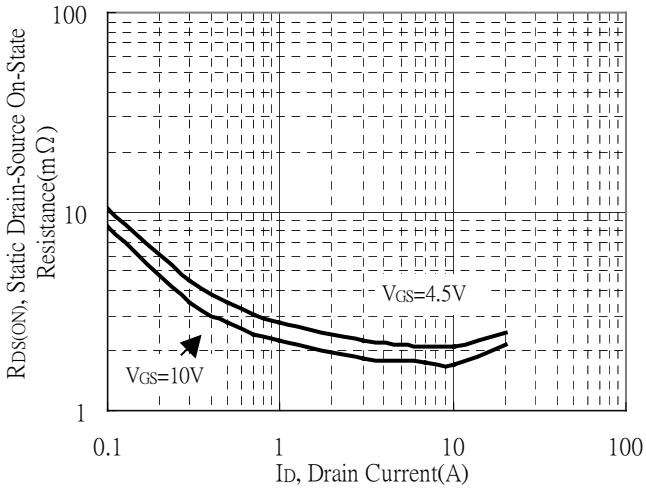
Typical Output Characteristics



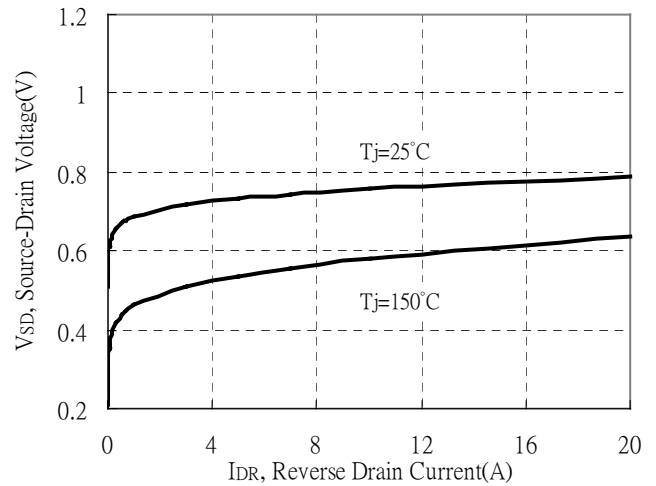
Brekdown Voltage vs Ambient Temperature



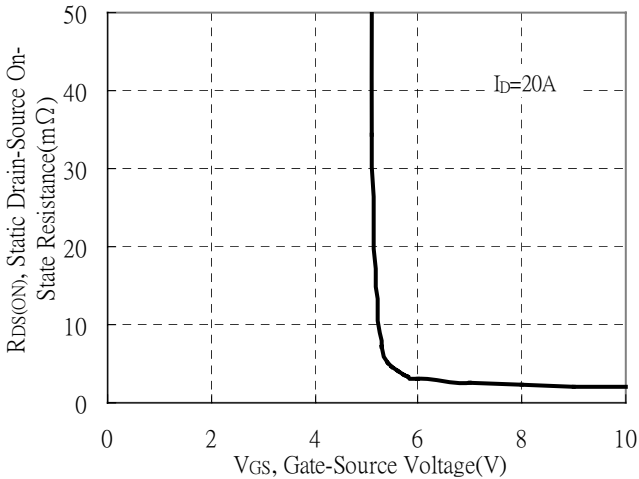
Static Drain-Source On-State resistance vs Drain Current



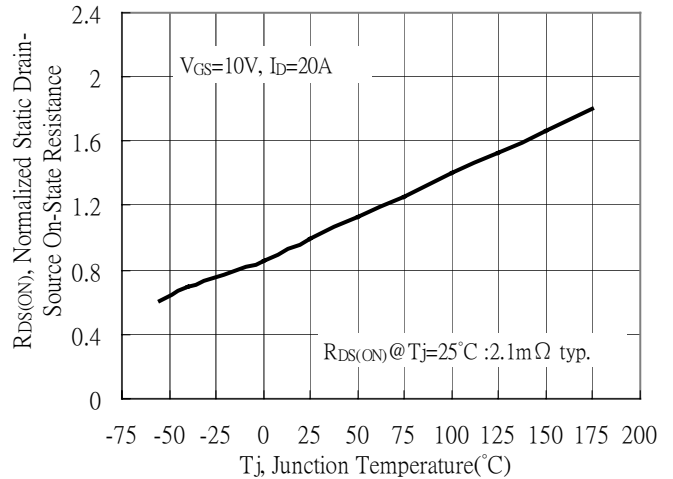
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



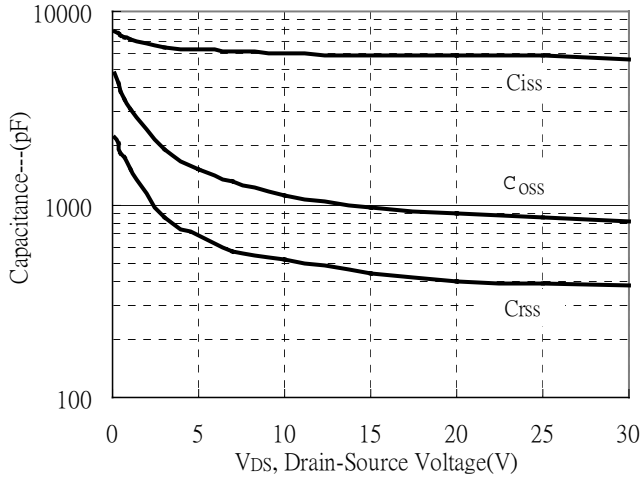
Drain-Source On-State Resistance vs Junction Temperature



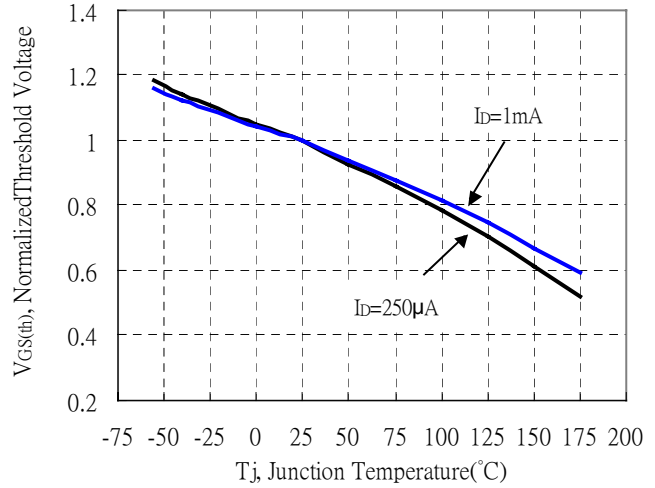


**Typical Characteristics(Cont.)**

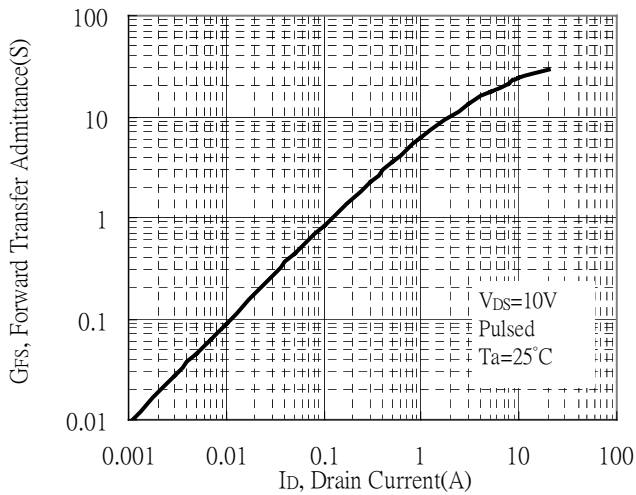
Capacitance vs Drain-to-Source Voltage



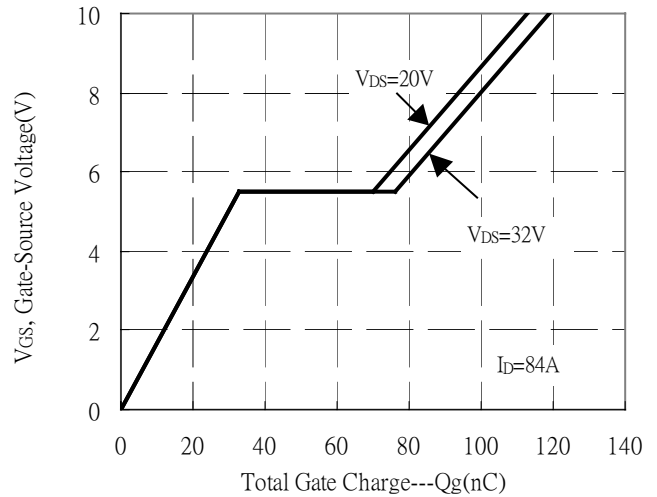
Threshold Voltage vs Junction Temperature



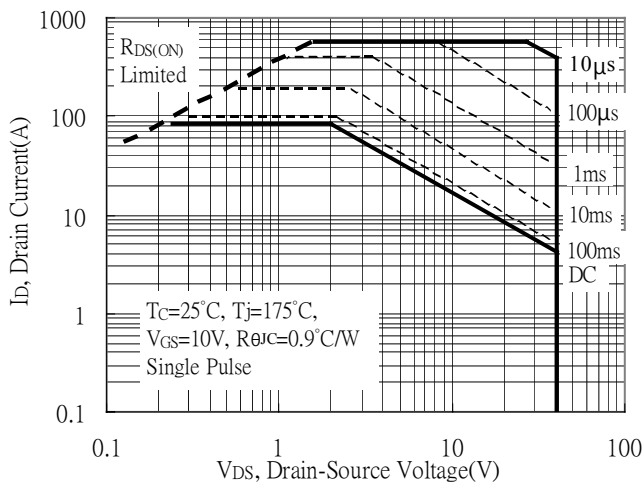
Forward Transfer Admittance vs Drain Current



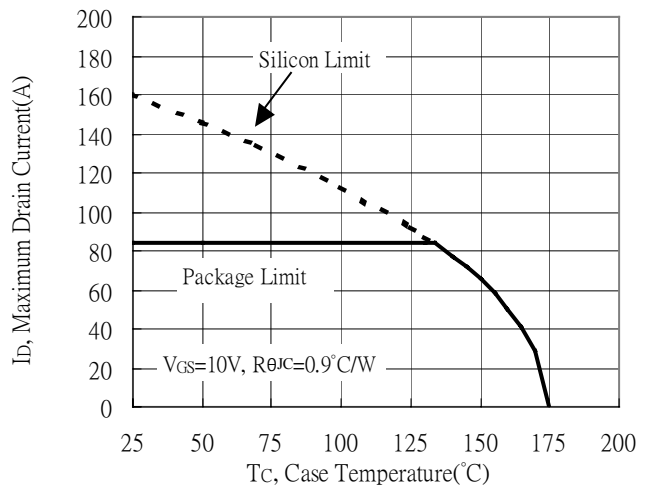
Gate Charge Characteristics



Maximum Safe Operating Area



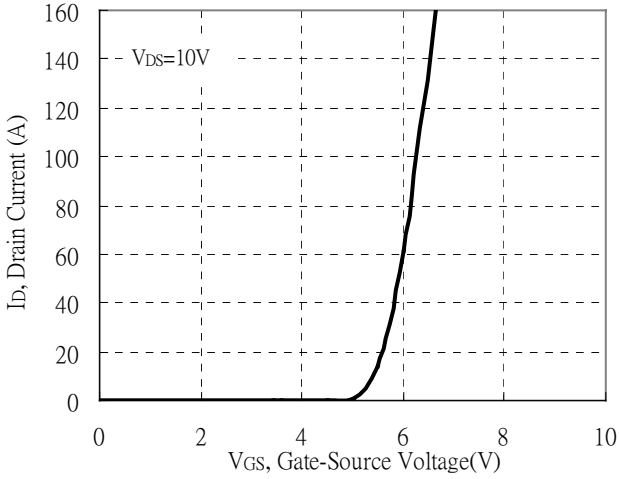
Maximum Drain Current vs Case Temperature



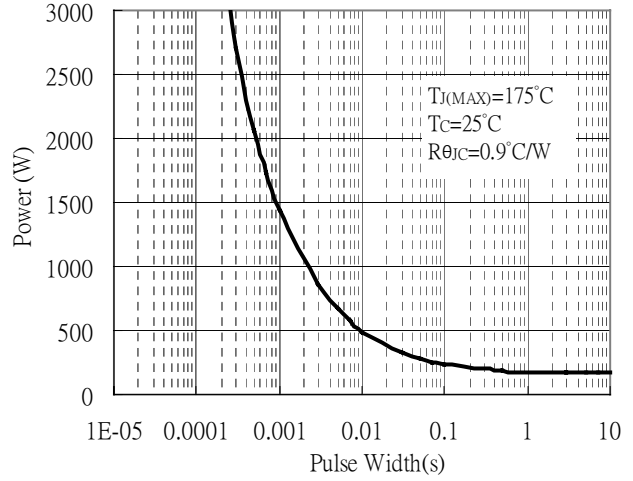


**Typical Characteristics(Cont.)**

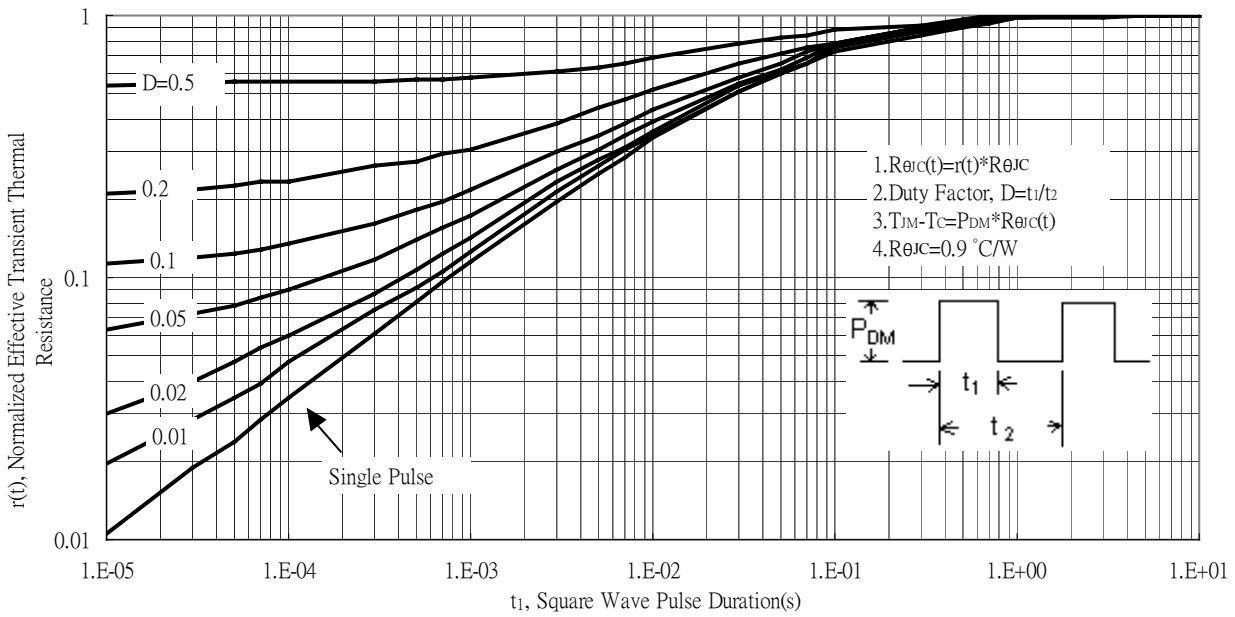
Typical Transfer Characteristics



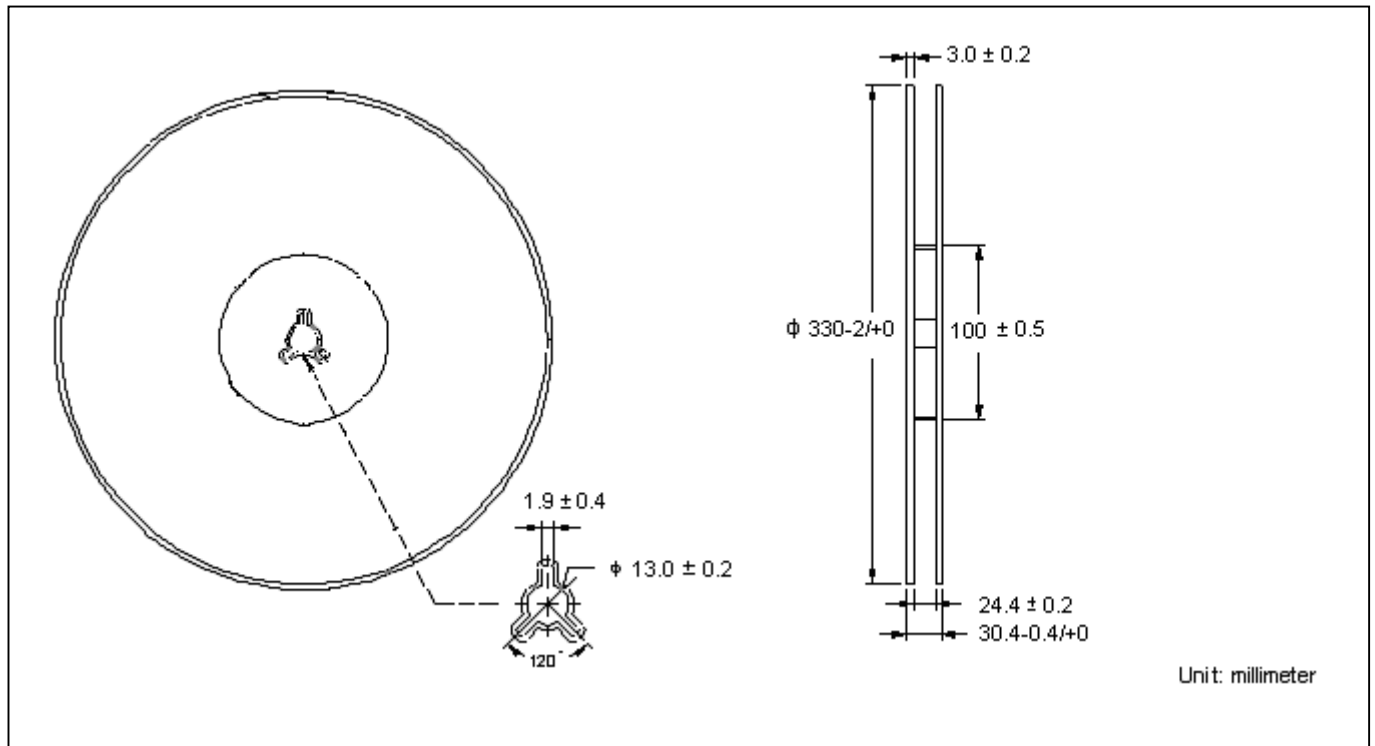
Single Pulse Maximum Power Dissipation



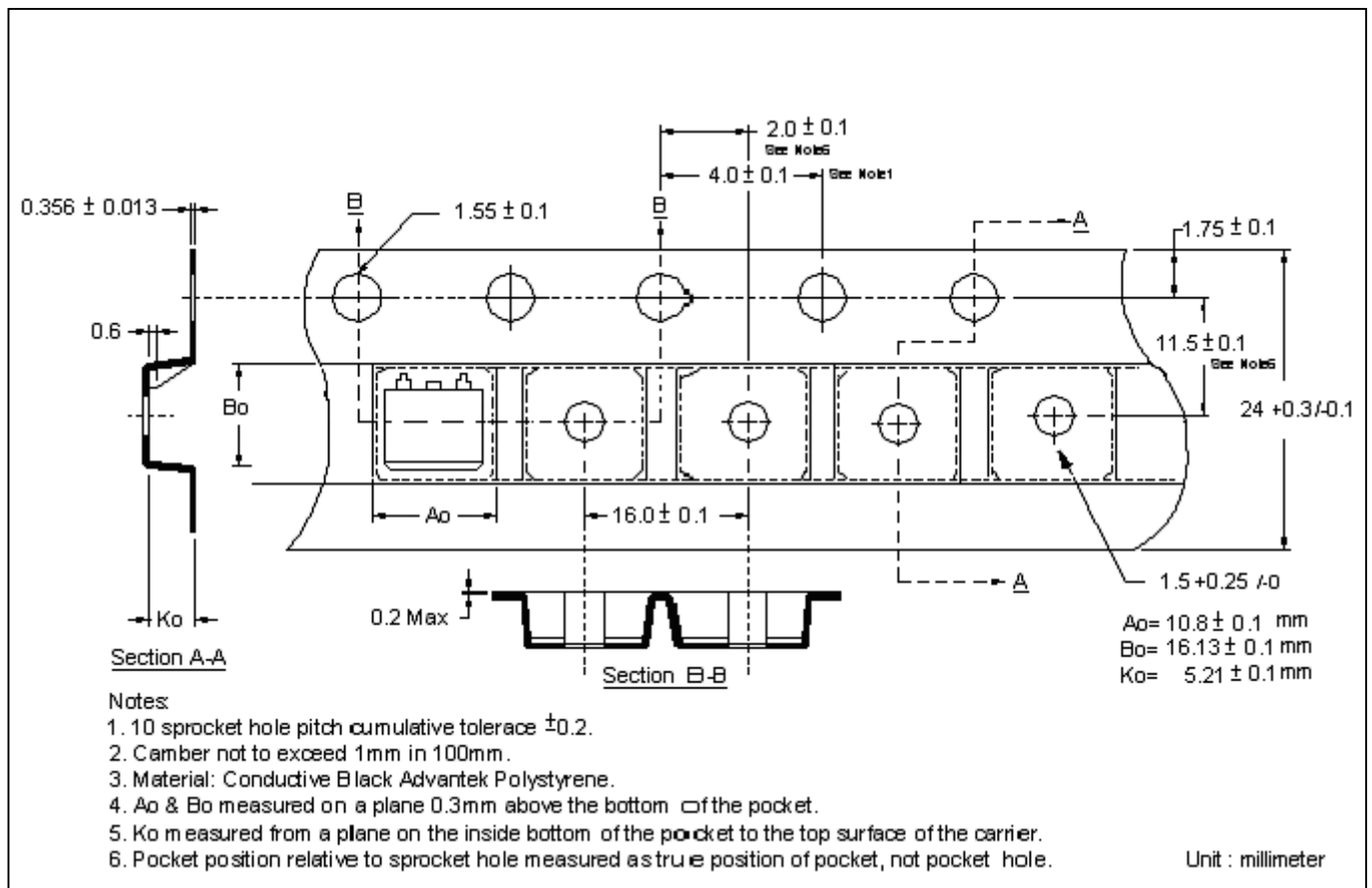
Transient Thermal Response Curves



### Reel Dimension



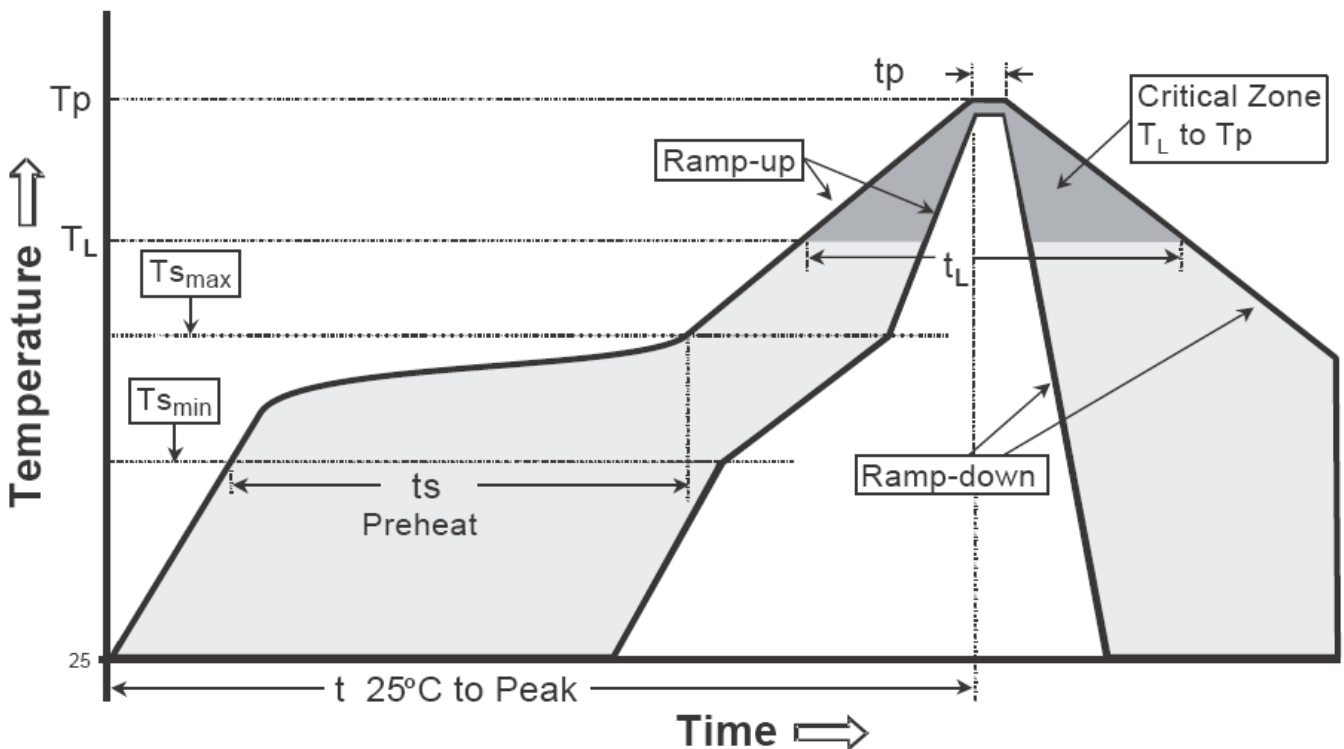
### Carrier Tape Dimension



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

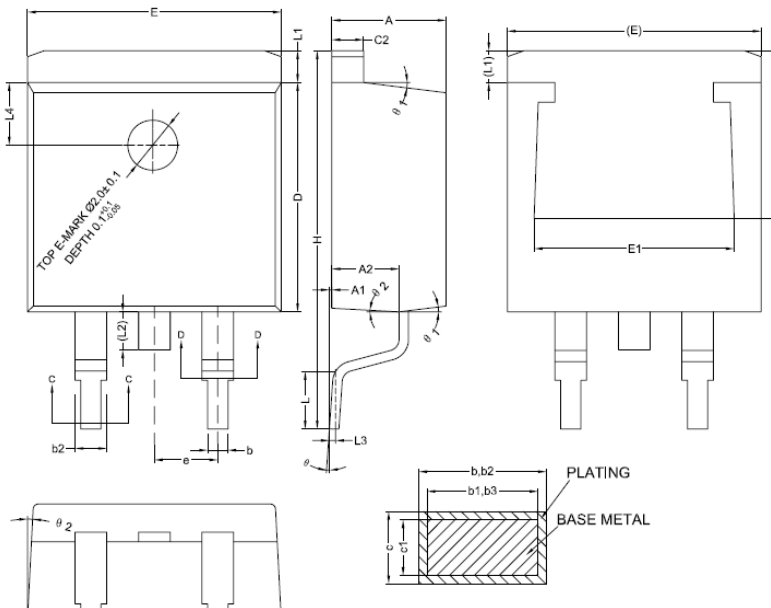


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t <sub>p</sub> )	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

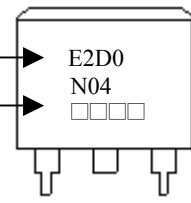
Note : All temperatures refer to topside of the package, measured on the package body surface.



**TO-263 Dimension**



**Marking :**



Device Name → E2D0  
 Date Code → N04 □□□□

Style : Pin 1.Gate 2.Drain  
3.Source

3-Lead Plastic Surface Mounted Package  
 CYStek Package Code : F3

Date Code : (From left to right)  
 First Code : Year code, the last digit of Christnr year. For example, 2014→4, 2015→, 2016→6, ..., etc.  
 Second Code : Month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J,  
 Oct→K, Nov→L, Dec→M  
 Third and fourth codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.40	4.70	0.173	0.185	E	10.06	10.26	0.396	0.404
A1	0.00	0.25	0.000	0.010	E1	7.80	8.20	0.307	0.323
A2	2.59	2.79	0.102	0.110	e	2.54 BSC		0.100 BSC	
b	0.77	0.90	0.030	0.035	H	14.70	15.50	0.579	0.610
b1	0.76	0.86	0.030	0.034	L	2.00	2.60	0.079	0.102
b2	1.23	1.36	0.048	0.054	L1	1.17	1.40	0.046	0.055
b3	1.22	1.32	0.048	0.052	L2	-	1.75	-	0.069
c	0.34	0.47	0.013	0.019	L3	0.25 BSC		0.010 BSC	
c1	0.33	0.43	0.013	0.017	L4	2.00 REF		0.079 BSC	
c2	1.22	1.32	0.048	0.052	θ	0°	8°	0°	8°
D	9.05	9.25	0.356	0.364	Θ1	5°	9°	5°	9°
D1	6.60	-	0.260	-	Θ2	1°	5°	1°	5°

Notes : 1.Controlling dimension : millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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