

**P-Channel Enhancement Mode Power MOSFET**

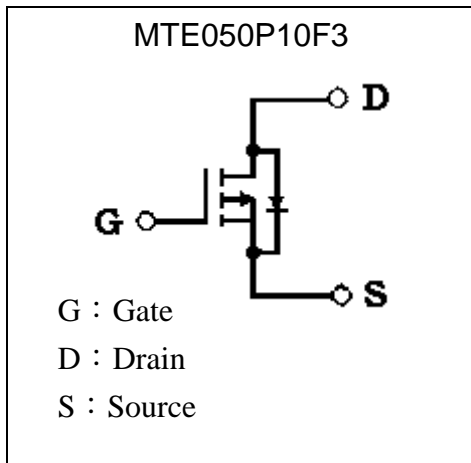
# MTE050P10F3

$BV_{DSS}$	-100V
$I_D @ V_{GS}=-10V, T_C=25^{\circ}C$	-44A
$I_D @ V_{GS}=-10V, T_A=25^{\circ}C$	-4.6A
$R_{DSON(TYP)} @ V_{GS}=-10V, I_D=-20A$	40mΩ

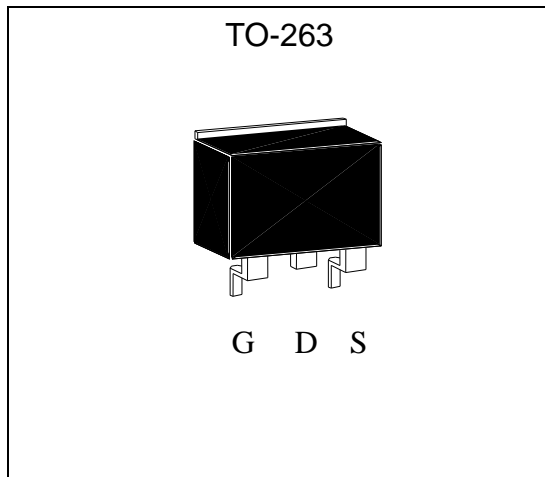
**Features**

- Low Gate Charge
- Simple Drive Requirement
- Repetitive Avalanche Rated
- Fast Switching Characteristic
- RoHS compliant package

**Symbol**

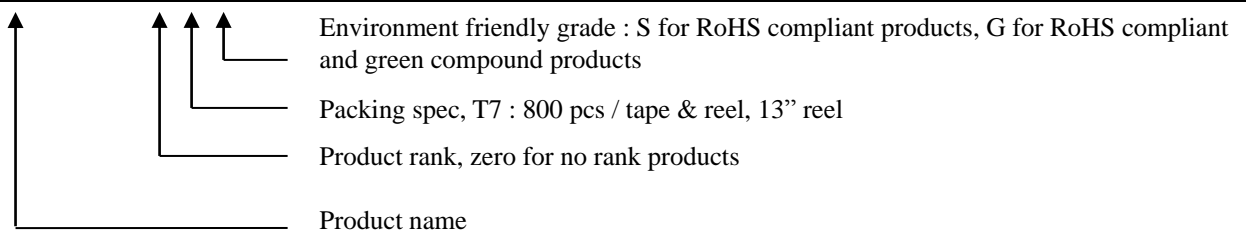


**Outline**



**Ordering Information**

Device	Package	Shipping
MTE050P10F3-0-T7-X	TO-263 (Pb-free lead plating and RoHS compliant package)	800 pcs / Tape & Reel



**Absolute Maximum Ratings** ( $T_C=25^{\circ}\text{C}$ , unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	$V_{DS}$	-100	V	
Gate-Source Voltage	$V_{GS}$	$\pm 25$		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$	$I_D$	-44	A	
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$		-31		
Pulsed Drain Current (Note 3)	$I_{DM}$	-140		
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)	$I_{DSM}$	-4.6		
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$ , $V_{GS}=10\text{V}$ (Note 2)		-3.7		
Avalanche Current (Note 4)	$I_{AS}$	-44		
Avalanche Energy @ $L=1\text{mH}$ , $I_D=-21\text{A}$ , $V_{DD}=-25\text{V}$ (Note 4)	$E_{AS}$	221	mJ	
Repetitive Avalanche Energy @ $L=0.1\text{mH}$ (Note 3)	$E_{AR}$	20		
Power Dissipation	$T_C=25^{\circ}\text{C}$ (Note 1)	$P_D$	200	W
	$T_C=100^{\circ}\text{C}$ (Note 1)		100	
Power Dissipation	$T_A=25^{\circ}\text{C}$ (Note 2)	$P_{DSM}$	2	
	$T_A=70^{\circ}\text{C}$ (Note 2)		1.3	
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+175	$^{\circ}\text{C}$	

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.75	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note 2)	$R_{th,j-a}$	62	

- Note : 1. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=175^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design, and the maximum temperature of  $175^{\circ}\text{C}$  may be used if the PCB allows it.
3. Pulse width limited by junction temperature  $T_{J(MAX)}=175^{\circ}\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
4. 100% tested by conditions of  $L=1\text{mH}$ ,  $I_{AS}=-18\text{A}$ ,  $V_{GS}=-10\text{V}$ ,  $V_{DD}=-25\text{V}$ .
5. The static characteristics are obtained using  $<300\mu\text{s}$  pulses, duty cycle 0.5% maximum.
6. The  $R_{\theta JA}$  is the sum of thermal resistance from junction to case  $R_{\theta JC}$  and case to ambient.



**Characteristics (Tc=25°C, unless otherwise specified)**

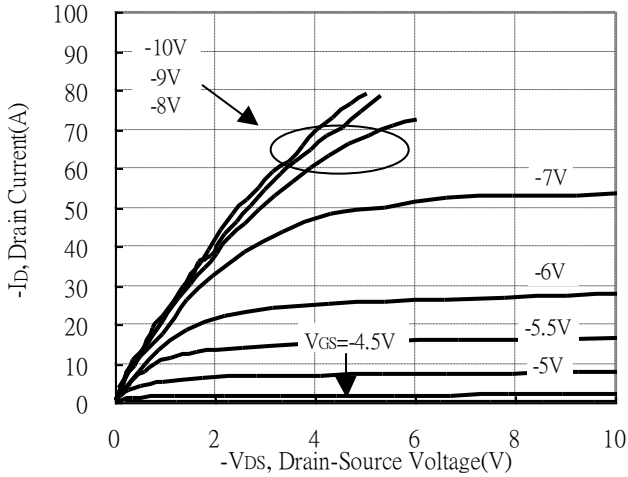
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-2	-	-4		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =-250μA
G <sub>FS</sub>	-	23	-	S	V <sub>DS</sub> = -10V, I <sub>D</sub> =-20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> = -80V, V <sub>GS</sub> = 0V
	-	-	-25		V <sub>DS</sub> = -80V, V <sub>GS</sub> = 0V, T <sub>j</sub> =125°C
*R <sub>DS(ON)</sub>	-	40	50	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> =-20A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	33.5	-	nC	I <sub>D</sub> =-21A, V <sub>DS</sub> =-50V, V <sub>GS</sub> =-10V
*Q <sub>gs</sub>	-	7.6	-		
*Q <sub>gd</sub>	-	11.5	-		
*t <sub>d(ON)</sub>	-	20.6	-	ns	V <sub>DS</sub> =-20V, I <sub>D</sub> =-1A, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω
*t <sub>r</sub>	-	18	-		
*t <sub>d(OFF)</sub>	-	73.2	-		
*t <sub>f</sub>	-	64.2	-		
C <sub>iss</sub>	-	1895	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, f=1MHz
C <sub>oss</sub>	-	207	-		
C <sub>rss</sub>	-	94	-		
R <sub>g</sub>	-	4.7	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	-44	A	
*I <sub>SM</sub>	-	-	-140		
*V <sub>SD</sub>	-	-0.85	-1.2	V	I <sub>S</sub> =-20A, V <sub>GS</sub> =0V
*t <sub>rr</sub>	-	30	-	ns	I <sub>F</sub> =-20A, V <sub>GS</sub> =0V, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	43	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

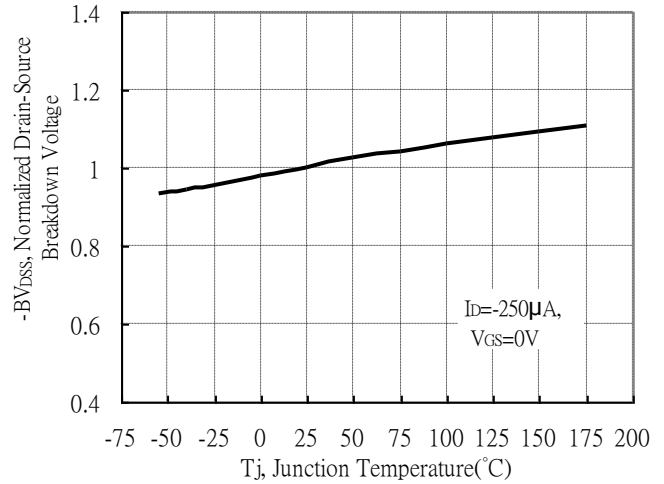


**Typical Characteristics**

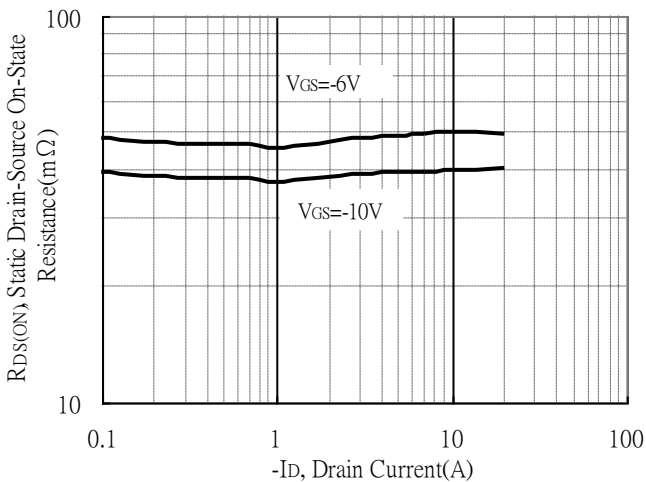
Typical Output Characteristics



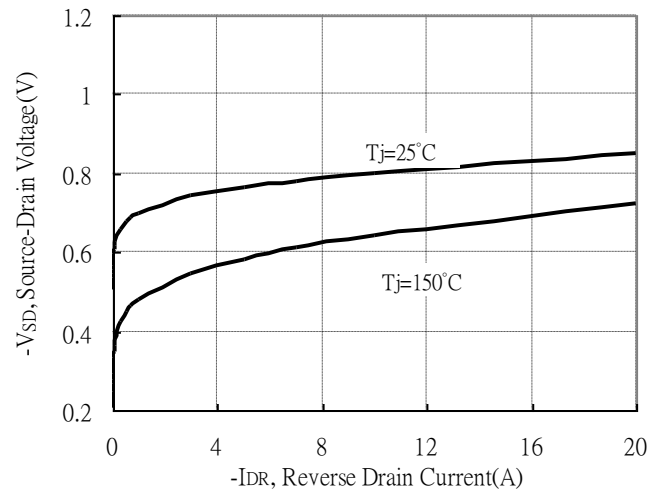
Brekdown Voltage vs Junction Temperature



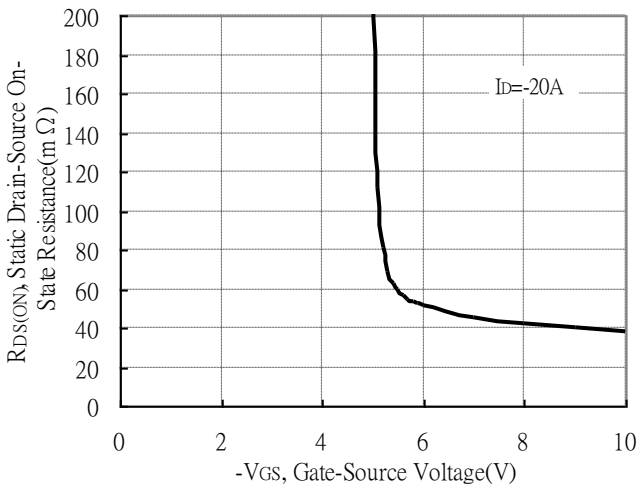
Static Drain-Source On-State resistance vs Drain Current



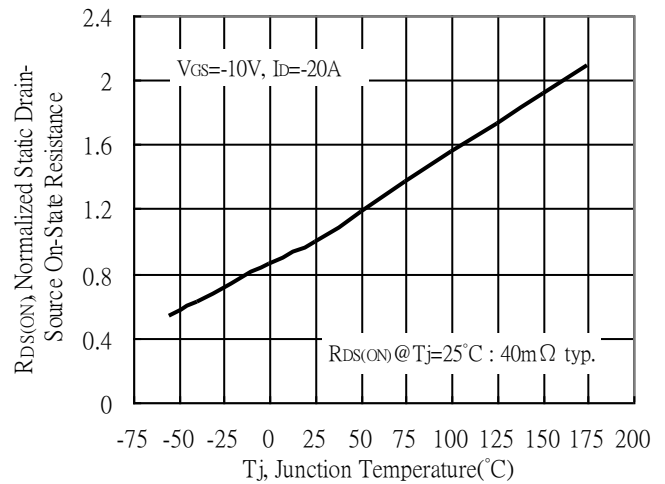
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

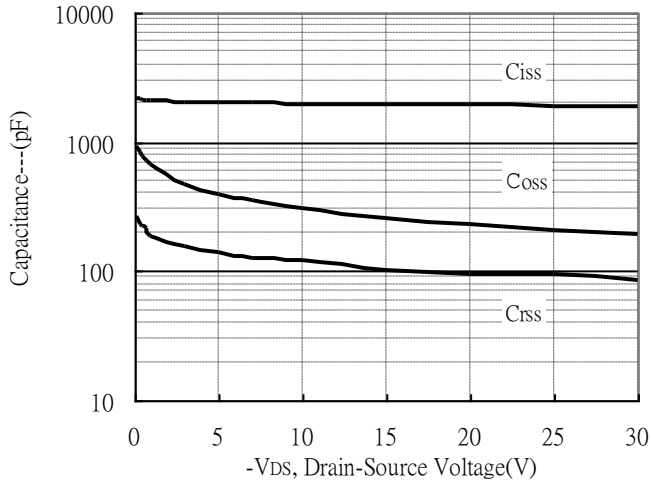


Drain-Source On-State Resistance vs Junction Temperature

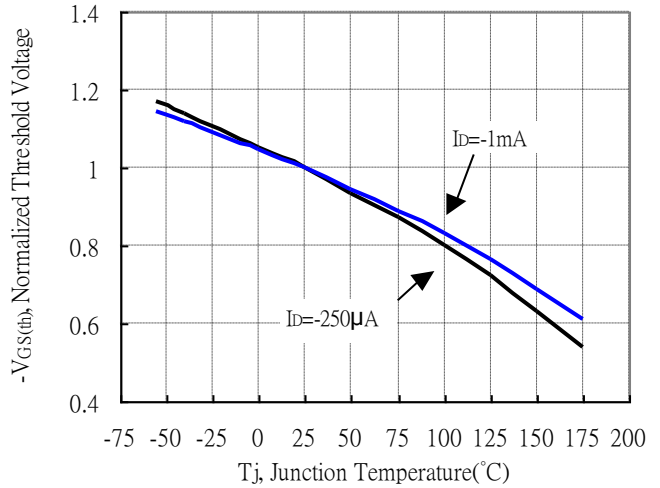


## Typical Characteristics(Cont.)

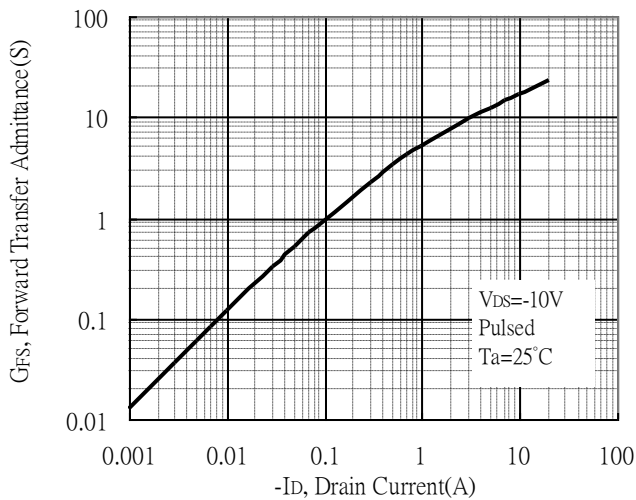
Capacitance vs Drain-to-Source Voltage



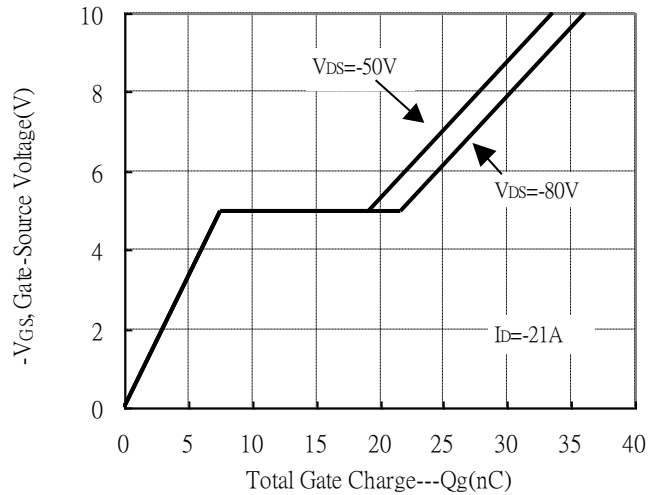
Threshold Voltage vs Junction Temperature



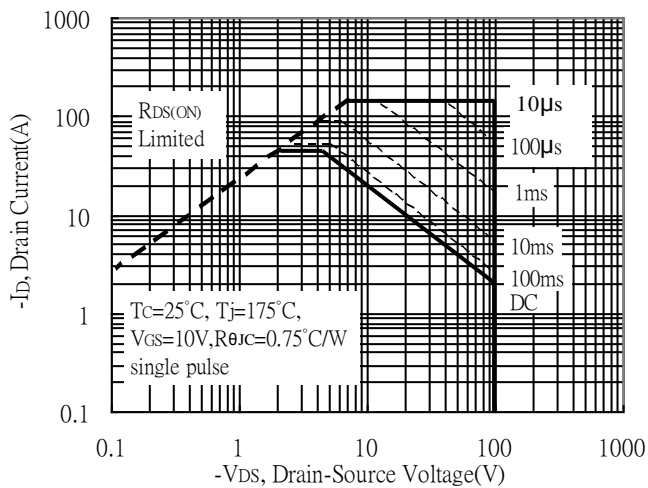
Forward Transfer Admittance vs Drain Current



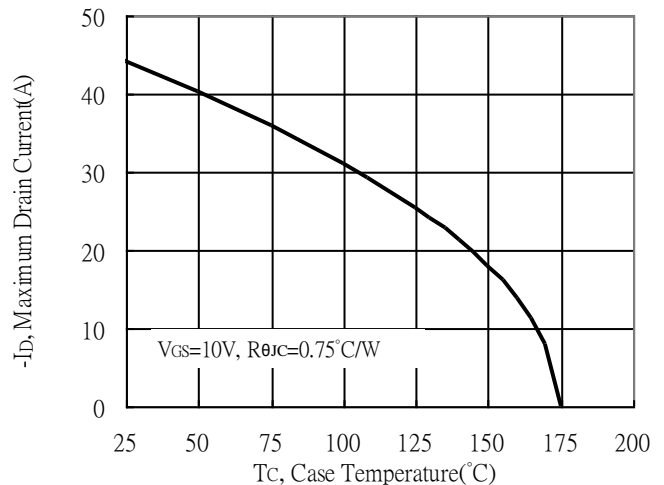
Gate Charge Characteristics



Maximum Safe Operating Area

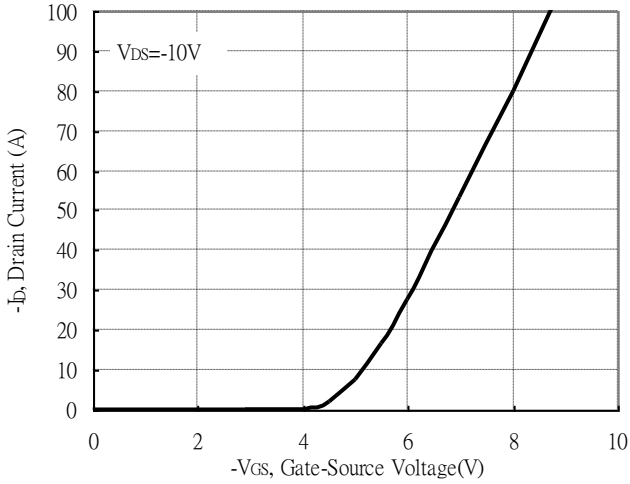


Maximum Drain Current vs Case Temperature

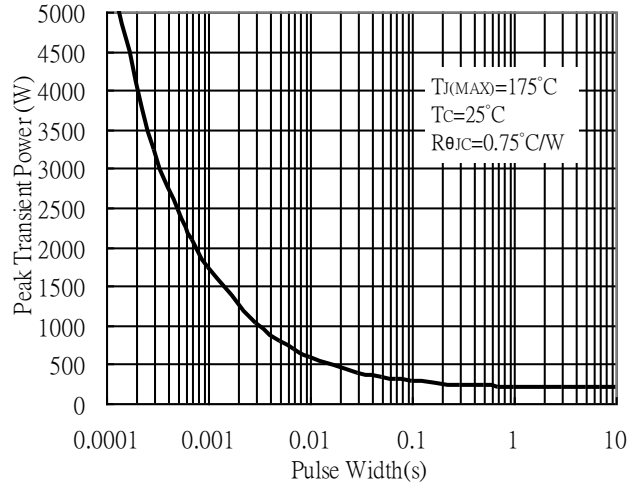


**Typical Characteristics(Cont.)**

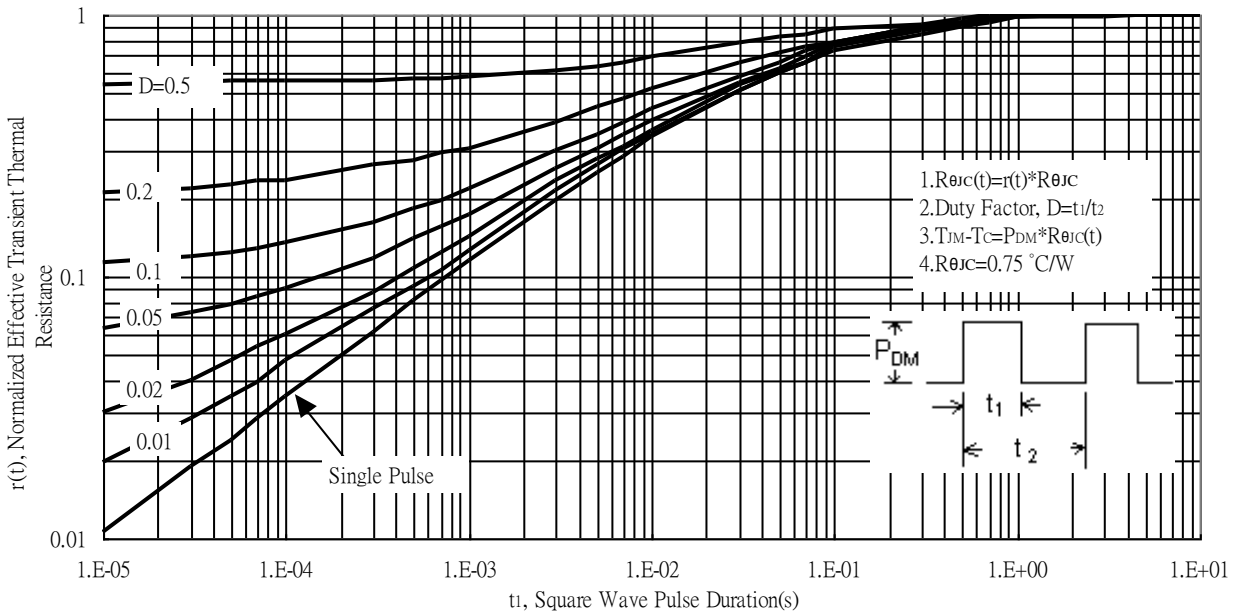
Typical Transfer Characteristics



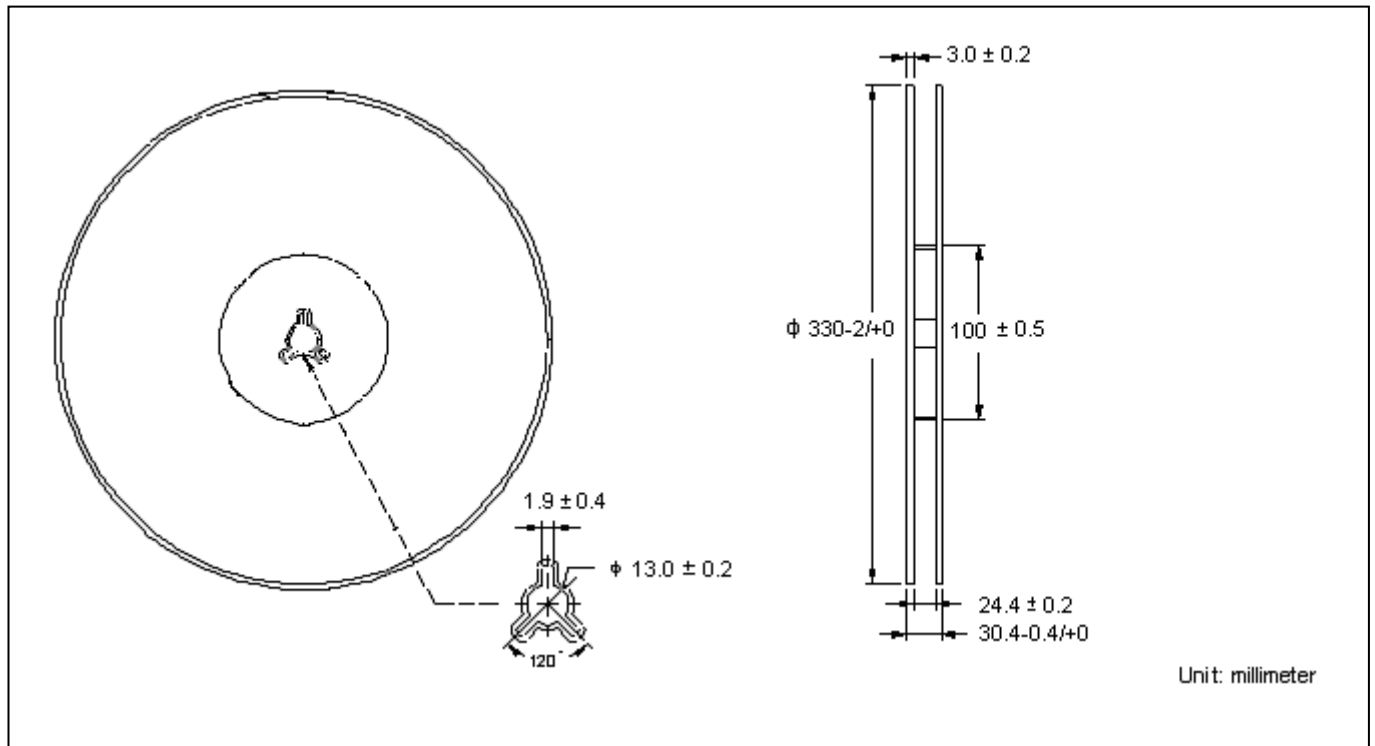
Single Pulse Maximum Power Dissipation



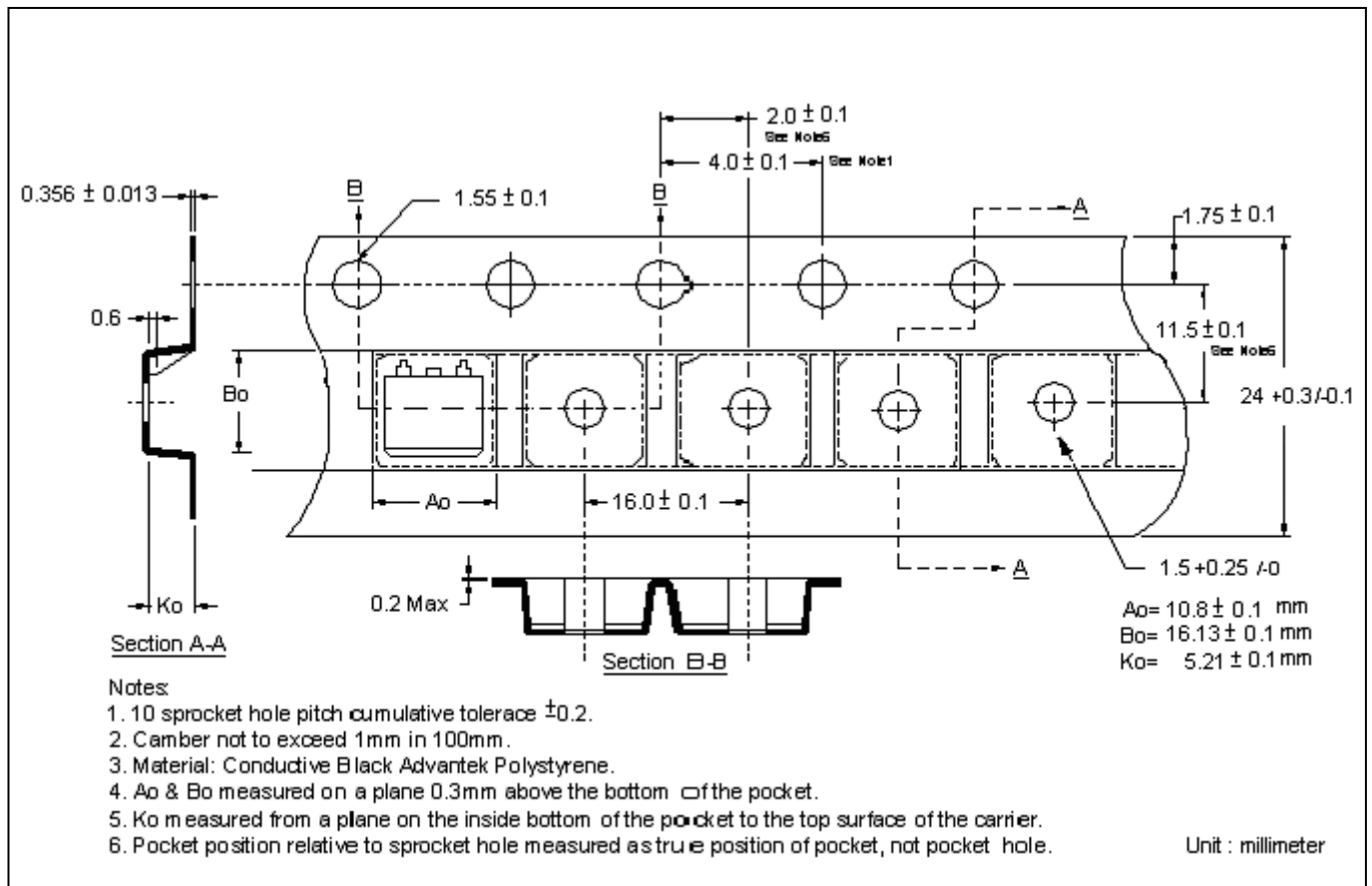
Transient Thermal Response Curves



### Reel Dimension



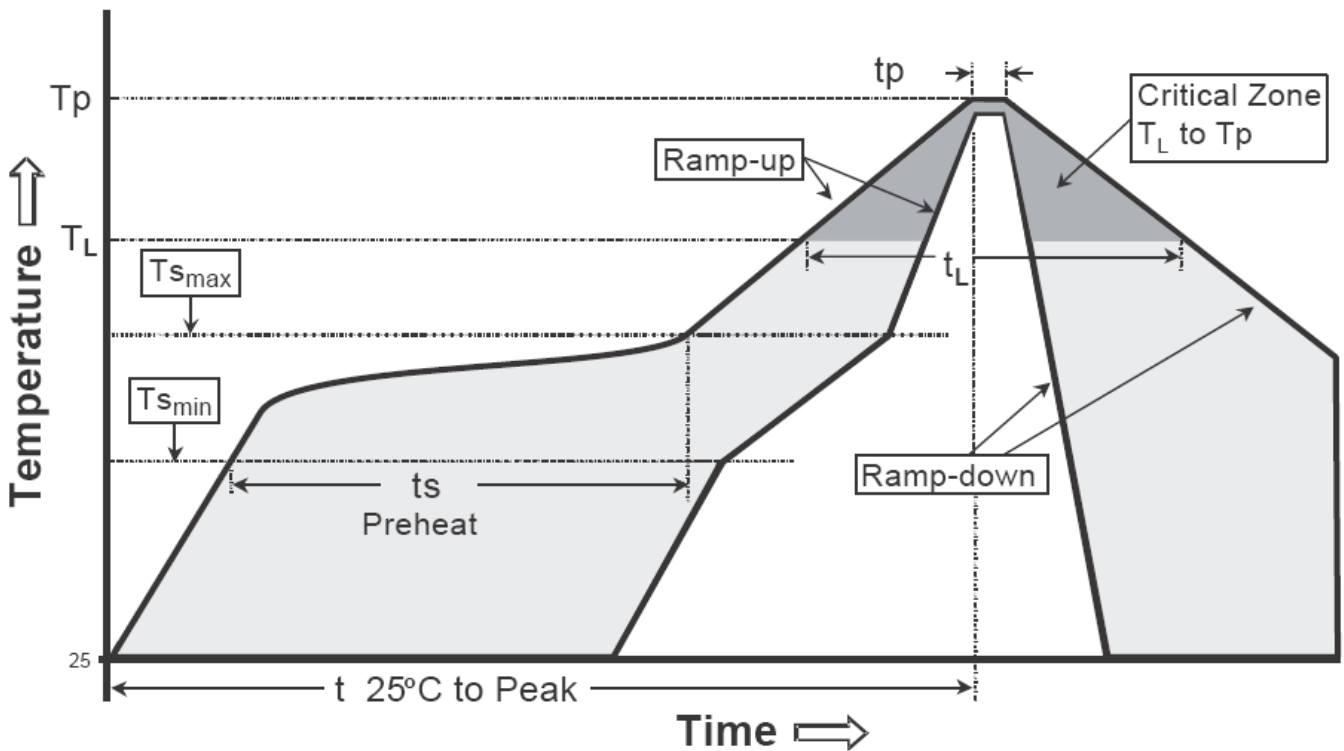
### Carrier Tape Dimension



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

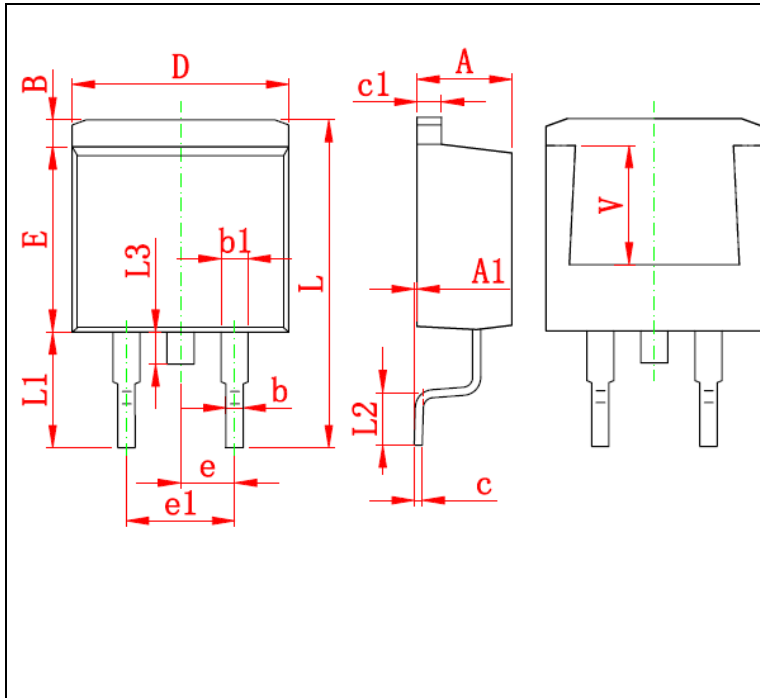


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

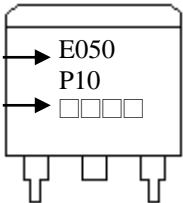


**TO-263 Dimension**



The diagram shows three views of the TO-263 package: a top view, a side view, and a bottom view. Dimensions are labeled with letters and numbers: A, A1, B, C, C1, D, E, E1, L, L1, L2, L3, b, b1, c, e, e1, and V. The top view shows a rectangular package with three leads extending from the bottom. The side view shows the profile of the package and the leads. The bottom view shows the leads from a different perspective.

**Marking :**



Device Name → E050  
 P10  
 Date Code → □□□□

Style : Pin 1.Gate 2.Drain 3.Source

3-Lead Plastic Surface Mounted Package  
 CYStek Package Code : F3

\*:Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184	E	8.500	8.900	0.335	0.350
A1	0.000	0.150	0.000	0.006	e	*2.540		*0.100	
B	1.170	1.370	0.046	0.054	e1	4.980	5.180	0.196	0.204
b	0.710	0.910	0.028	0.036	L	15.050	15.450	0.593	0.608
b1	1.170	1.370	0.046	0.054	L1	5.080	5.480	0.200	0.216
c	0.310	0.530	0.012	0.021	L2	2.340	2.740	0.092	0.108
c1	1.170	1.370	0.046	0.054	L3	1.300	1.700	0.051	0.067
D	10.010	10.310	0.394	0.406	V	5.600	REF	0.220	REF

- Notes :**
- Controlling dimension : millimeters.
  - Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
  - If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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