

N-Channel Enhancement Mode Power MOSFET

MTE050N10KRV8

BV_{DSS}	100V
I_D@V_{GS}=10V, T_C=25°C	10.2A
I_D@V_{GS}=10V, T_A=25°C	4.1A
R_{DS(ON)}@V_{GS}=10V, I_D=8A	54.5mΩ (typ)

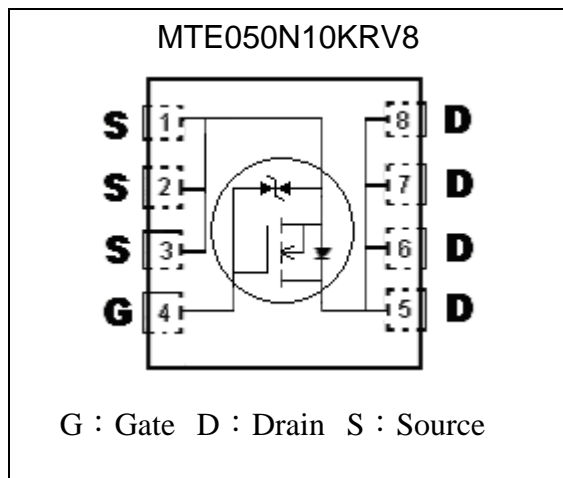
Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- ESD protected gate
- Pb-free lead plating and Halogen-free package

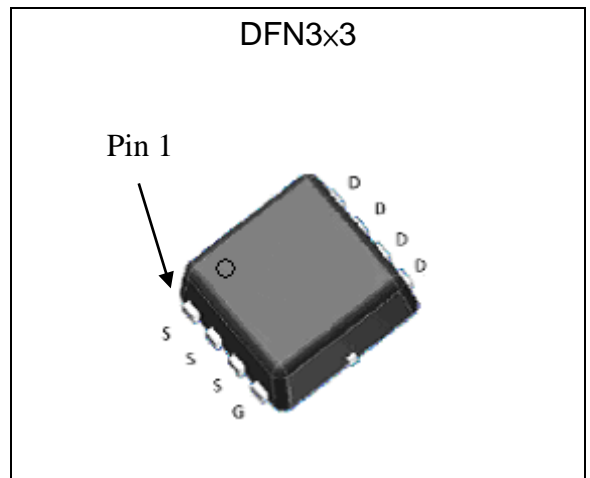
Applications

- POE + Power Source Equipment switch

Symbol

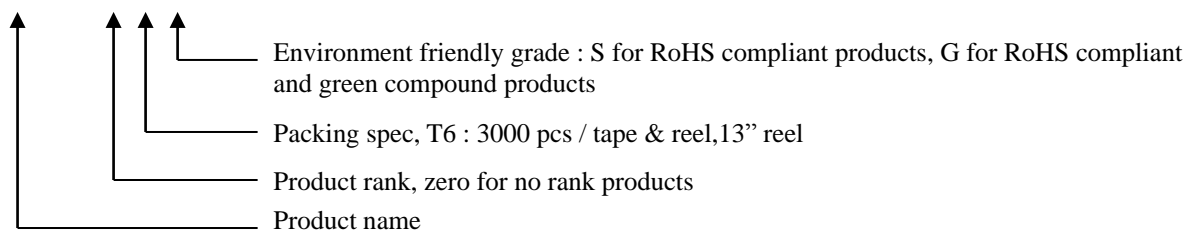


Outline



Ordering Information

Device	Package	Shipping
MTE050N10KRV8-0-T6-G	DFN 3 x3 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel



**Absolute Maximum Ratings** ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V_{DS}	100	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 5)	I_D	10.2	A	
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 5)		6.5		
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 2)	I_{DSM}	4.1		
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$, $V_{GS}=10\text{V}$ (Note 2)		3.3		
Pulsed Drain Current @ $V_{GS}=10\text{V}$ (Note 3)	I_{DM}	30		
Avalanche Current @ $L=0.1\text{mH}$ (Note 3)	I_{AS}	12		
Body Diode Continuous Forward Current	I_S	6.8		
Single Pulse Avalanche Energy @ $L=1\text{mH}$, $I_D=8\text{Amps}$, $V_{DD}=25\text{V}$ (Note 4)	E_{AS}	32	mJ	
Repetitive Avalanche Energy (Note 3)	E_{AR}	1.6		
Power Dissipation	$T_C=25^{\circ}\text{C}$ (Note 1)	P_D	15.6	W
	$T_C=100^{\circ}\text{C}$ (Note 1)		6.2	
	$T_A=25^{\circ}\text{C}$ (Note 2)	P_{DSM}	2.5	
	$T_A=70^{\circ}\text{C}$ (Note 2)		1.6	
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+150	$^{\circ}\text{C}$	

*Drain current limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	8	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note 2)	$R_{\theta JA}$	50	

- Note : 1. The power dissipation P_D is based on $T_{J(MAX)}=150^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in²FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}\text{C}$. The value in any given application depends on the user's specific board design. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C .
3. Pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}\text{C}$.
4. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^{\circ}\text{C}$. 100% tested by conditions of $V_{DD}=25\text{V}$, $I_D=7\text{A}$, $L=100\mu\text{H}$, $V_{GS}=10\text{V}$.
5. Calculated continuous drain current based on maximum allowable junction temperature.
6. The static characteristics are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% maximum.
7. The $R_{\theta JA}$ is the sum of thermal resistance from junction to case $R_{\theta JC}$ and case to ambient.

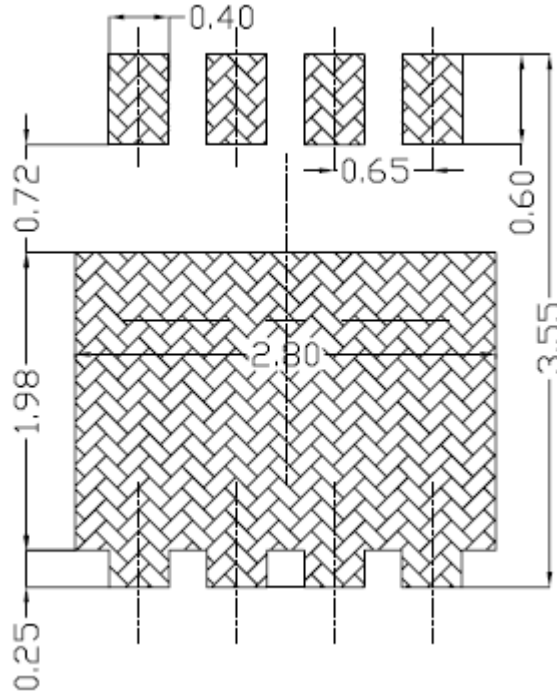


Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	100	-	-	V	V _{GS} =0V, I _D =250μA
ΔBV _{DSS} /ΔT _j	-	0.07	-	V/°C	Reference to 25°C, I _D =250μA
V _{GS(th)}	2	-	4	V	V _{DS} = V _{GS} , I _D =250μA
*G _{FS}	-	5	-	S	V _{DS} =10V, I _D =5A
I _{GSS}	-	-	±10	μA	V _{GS} =±16V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =80V, V _{GS} =0V
	-	-	5		V _{DS} =80V, V _{GS} =0V, T _j =55°C
*R _{DS(ON)}	-	54.5	75	mΩ	V _{GS} =10V, I _D =2A
Dynamic					
*Q _g	-	8.1	15	nC	V _{DS} =80V, I _D =8A, V _{GS} =10V
*Q _{gs}	-	2.9	-		
*Q _{gd}	-	1.6	-		
*t _{d(ON)}	-	9	-	ns	V _{DS} =50V, I _D =8A, V _{GS} =10V, R _G =6Ω
*t _r	-	16.6	-		
*t _{d(OFF)}	-	17.8	-		
*t _f	-	4.4	-		
C _{iss}	-	521	780	pF	V _{GS} =0V, V _{DS} =50V, f=1MHz
C _{oss}	-	48	-		
C _{rss}	-	20	-		
R _g	-	3.4	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	6.8	A	
*I _{SM}	-	-	23		
*V _{SD}	-	0.9	1.2	V	I _S =8A, V _{GS} =0V
*t _{rr}	-	28.1	-	ns	V _{GS} =0V, I _F =8A, dI _F /dt=100A/μs
*Q _{rr}	-	41.6	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

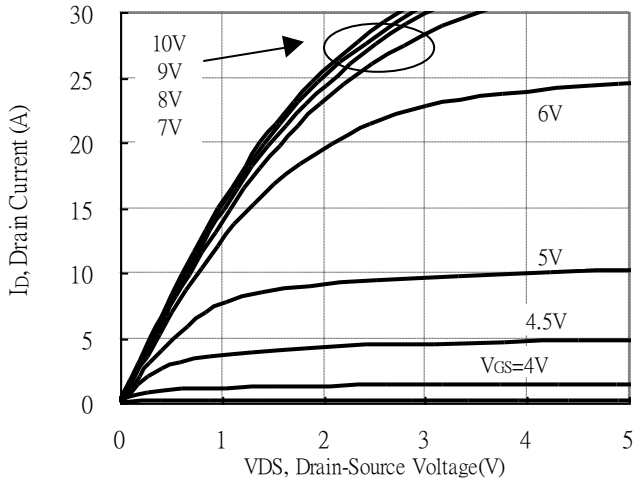
Recommended Soldering Footprint



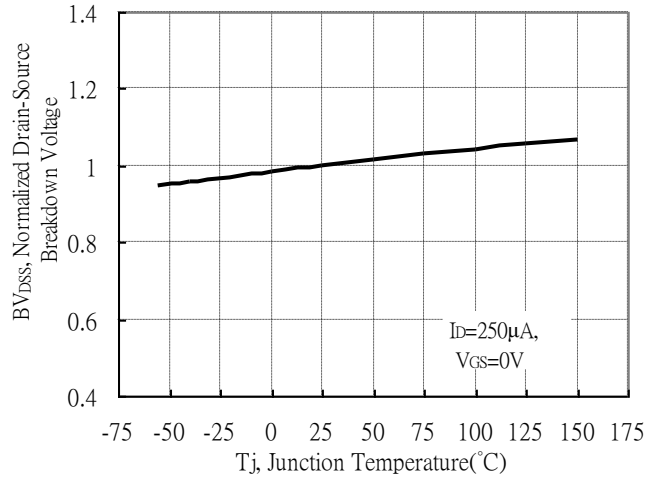


Typical Characteristics

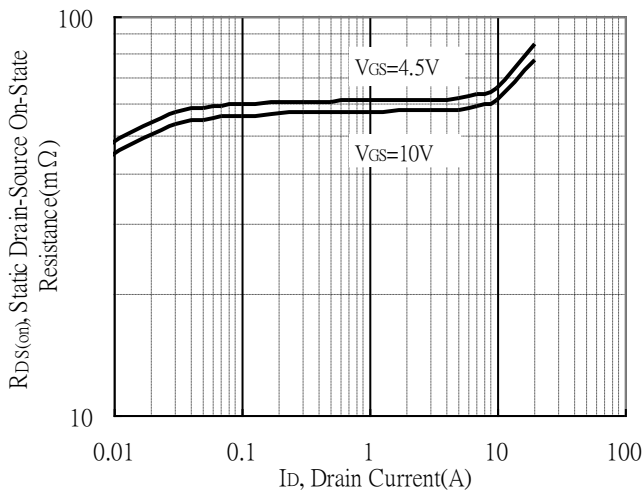
Typical Output Characteristics



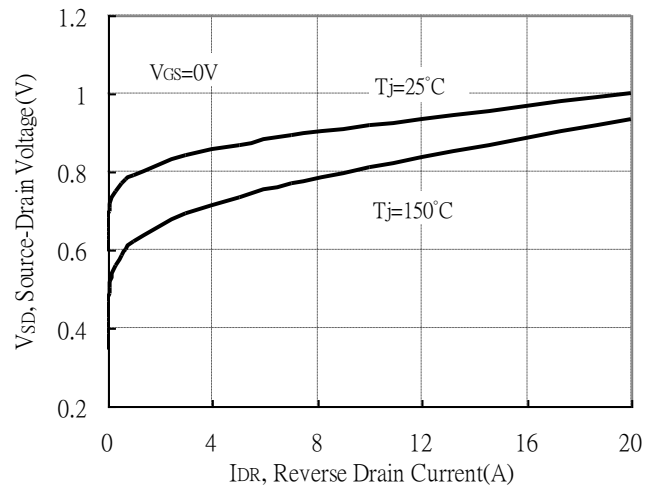
Brekdown Voltage vs Junction Temperature



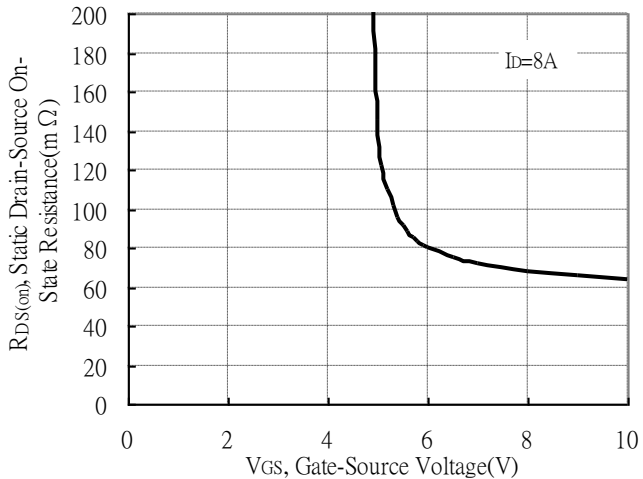
Static Drain-Source On-State resistance vs Drain Current



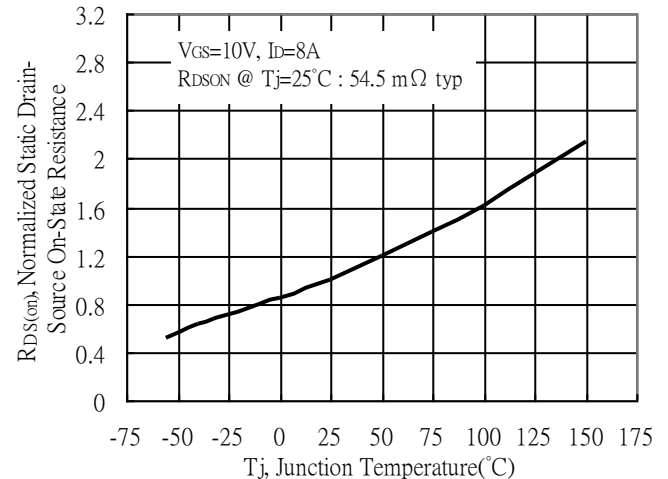
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

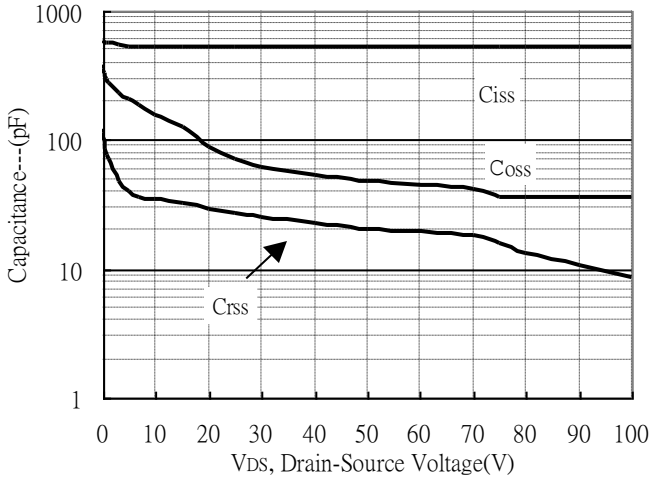


Drain-Source On-State Resistance vs Junction Temperature

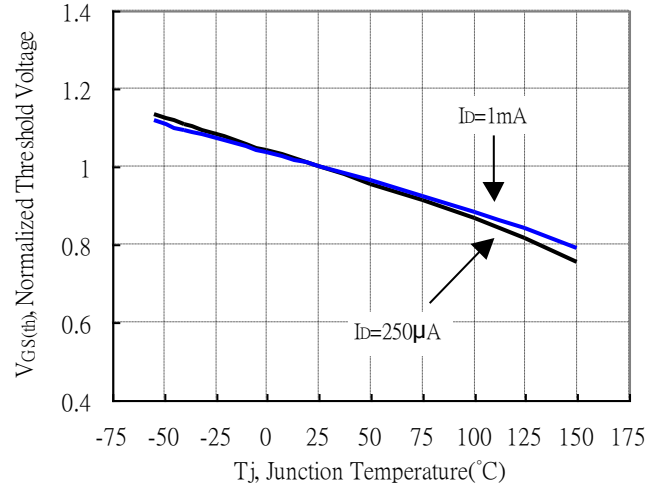


Typical Characteristics (Cont.)

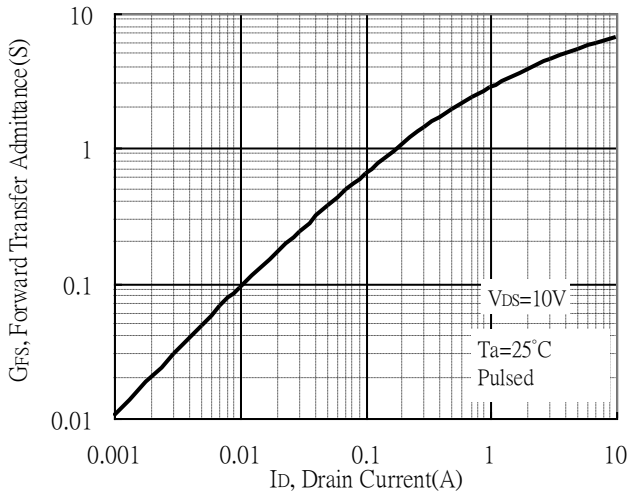
Capacitance vs Drain-to-Source Voltage



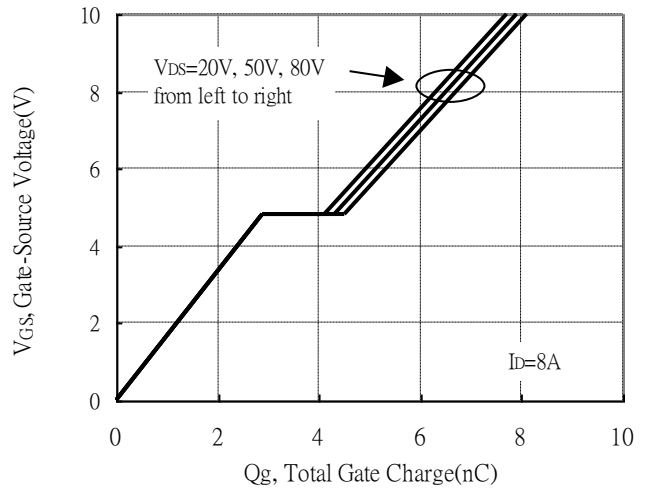
Threshold Voltage vs Junction Temperature



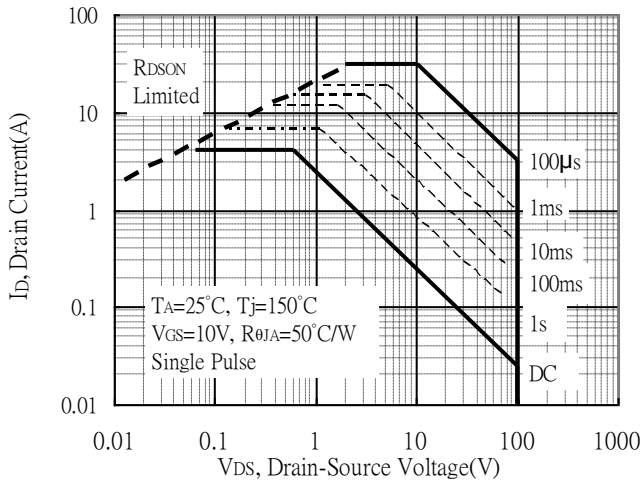
Forward Transfer Admittance vs Drain Current



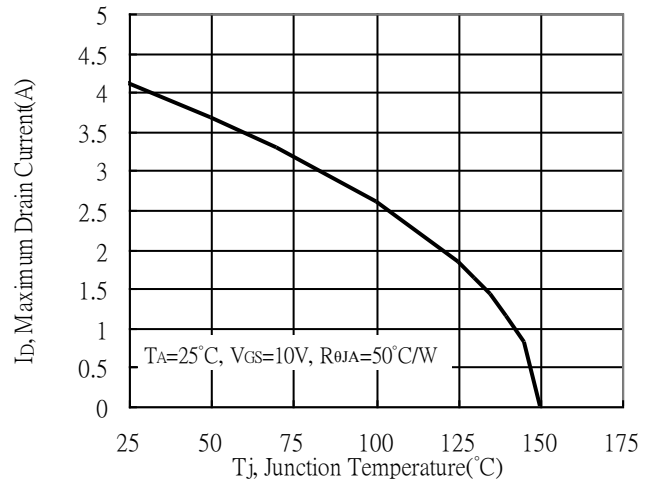
Gate Charge Characteristics



Maximum Safe Operating Area



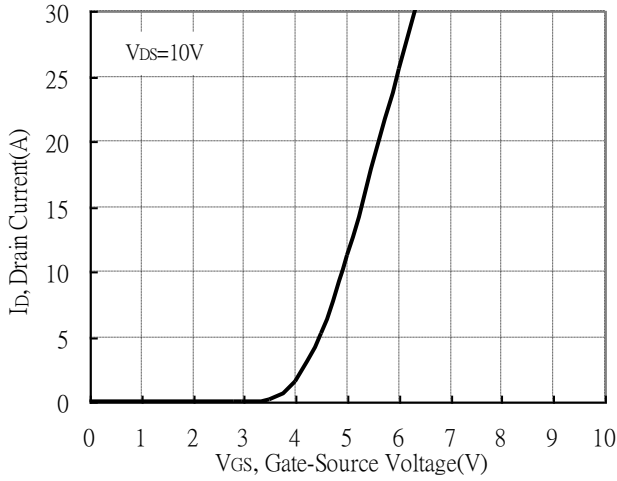
Maximum Drain Current vs Junction Temperature



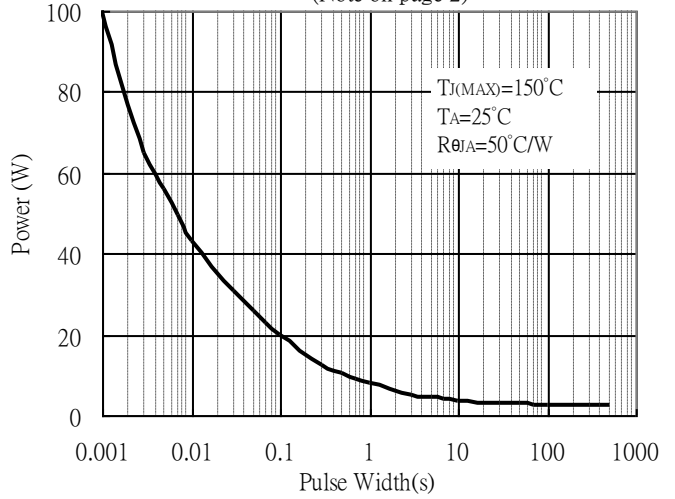


Typical Characteristics (Cont.)

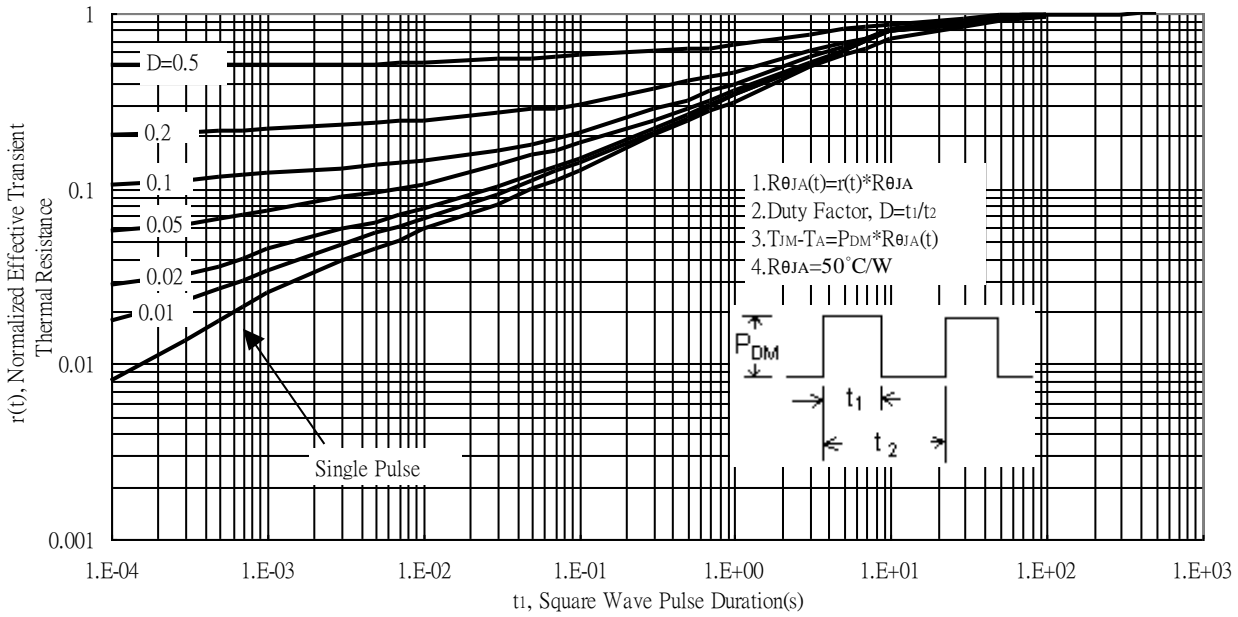
Typical Transfer Characteristics



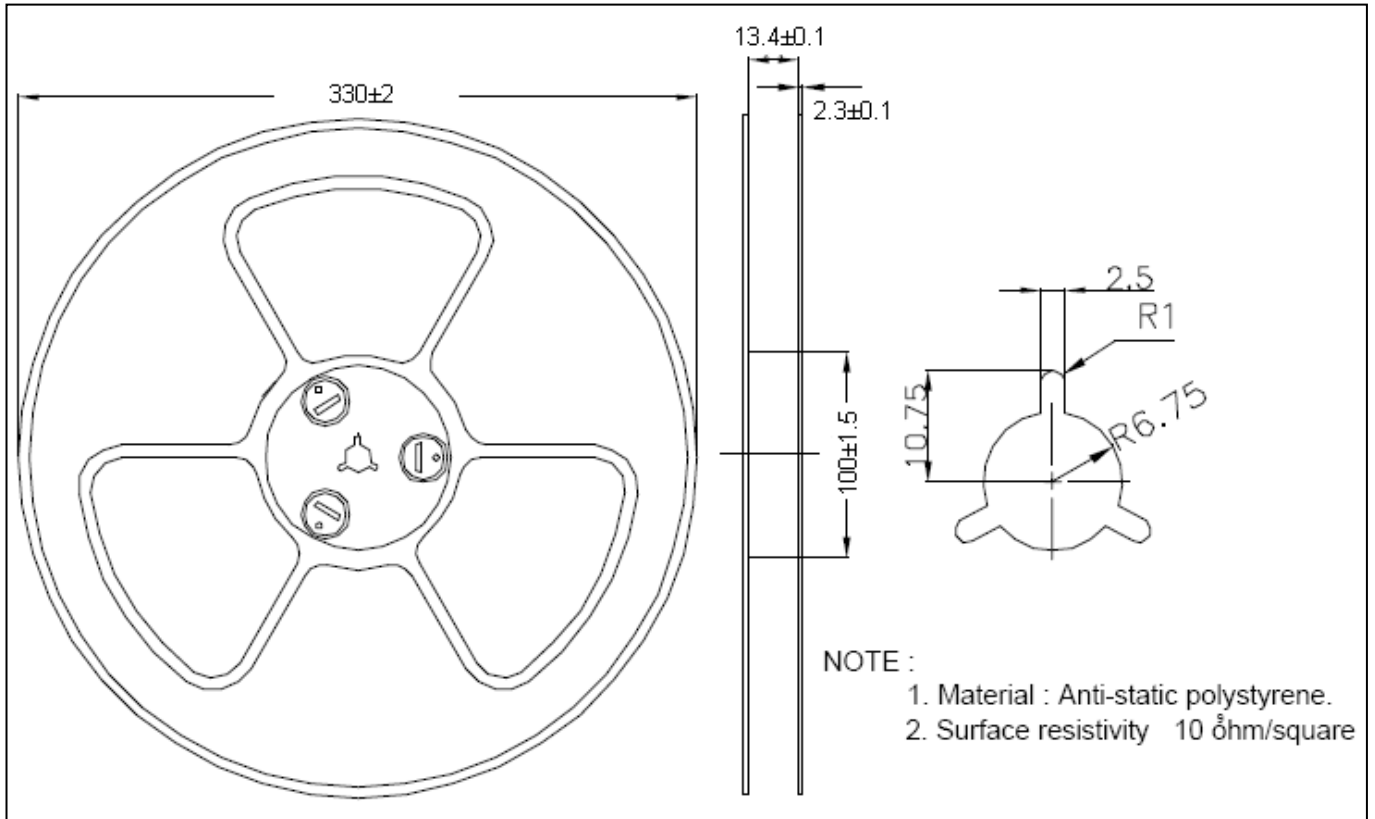
Single Pulse Power Rating, Junction to Ambient
 (Note on page 2)



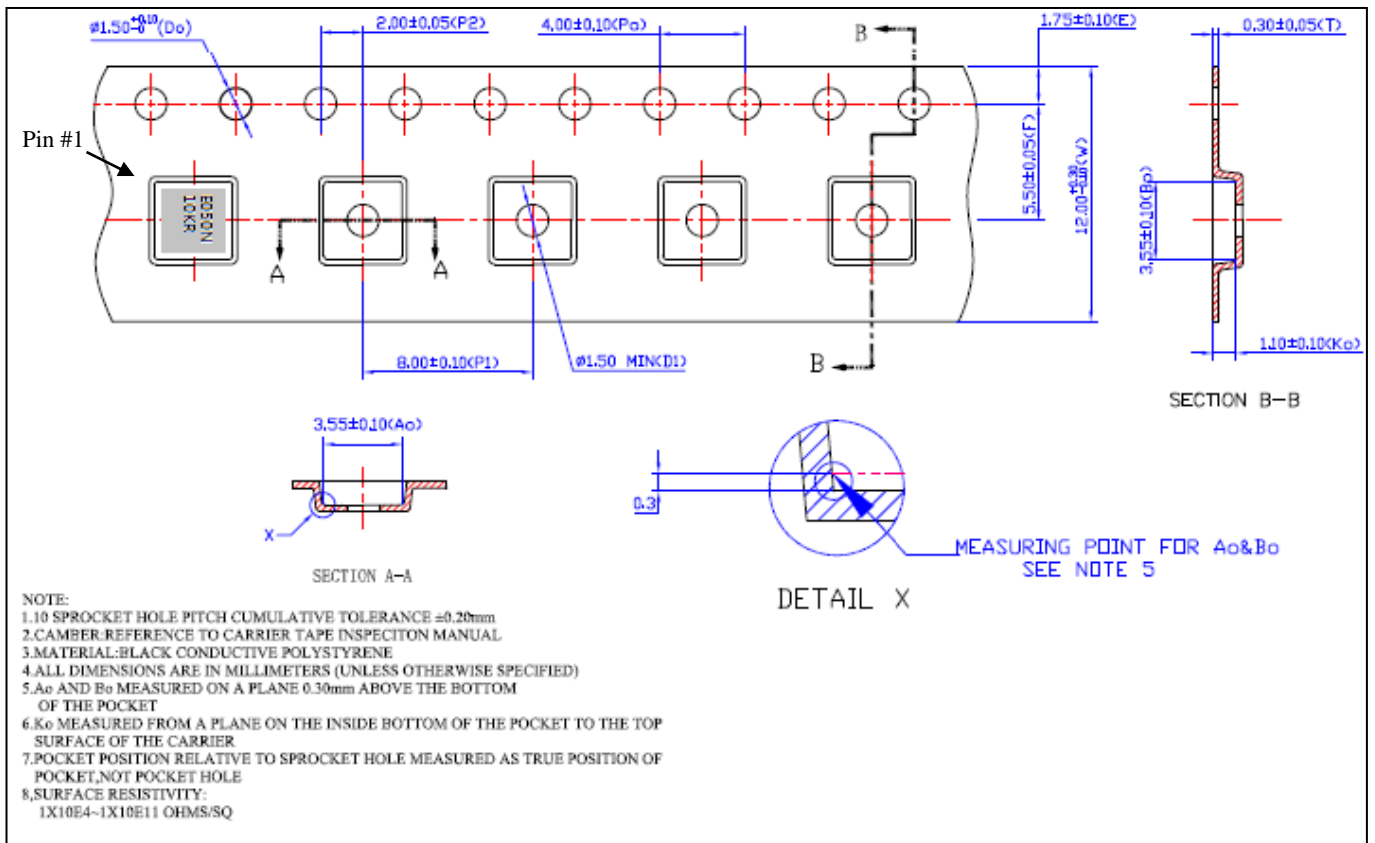
Transient Thermal Response Curves



Reel Dimension



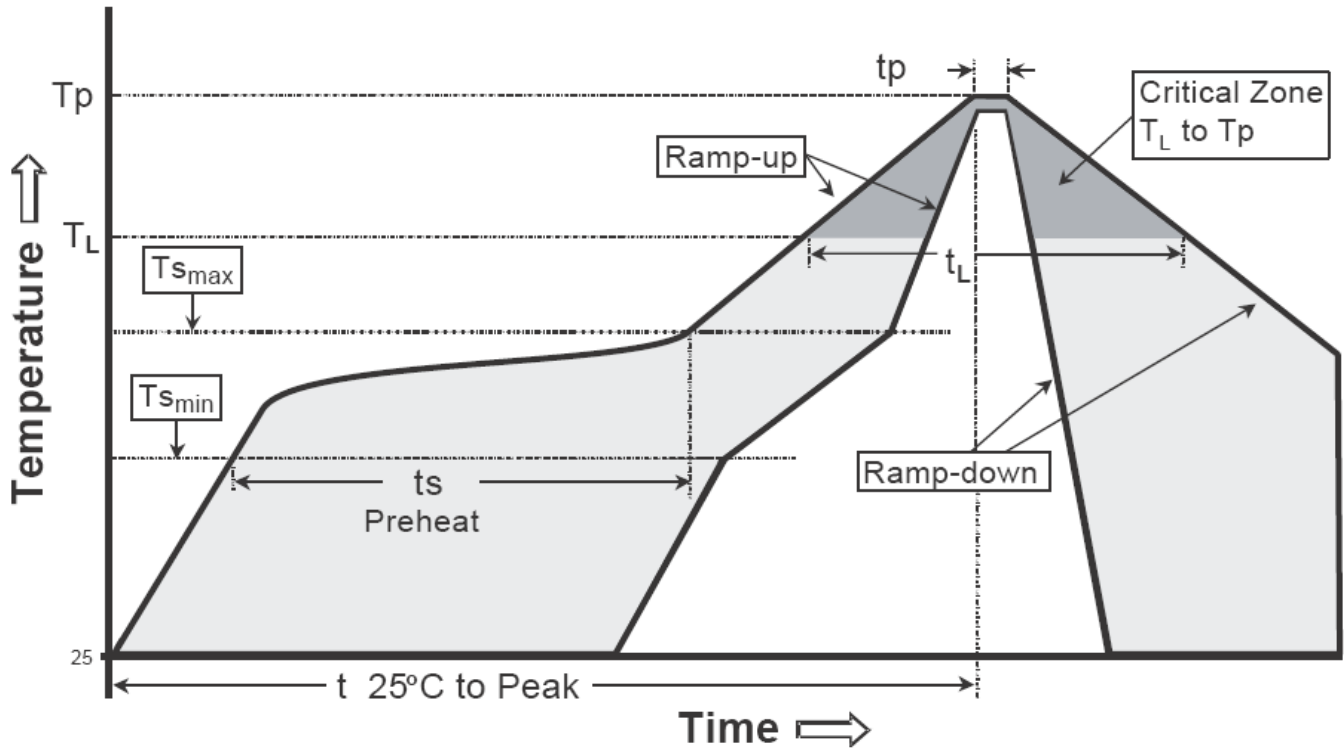
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

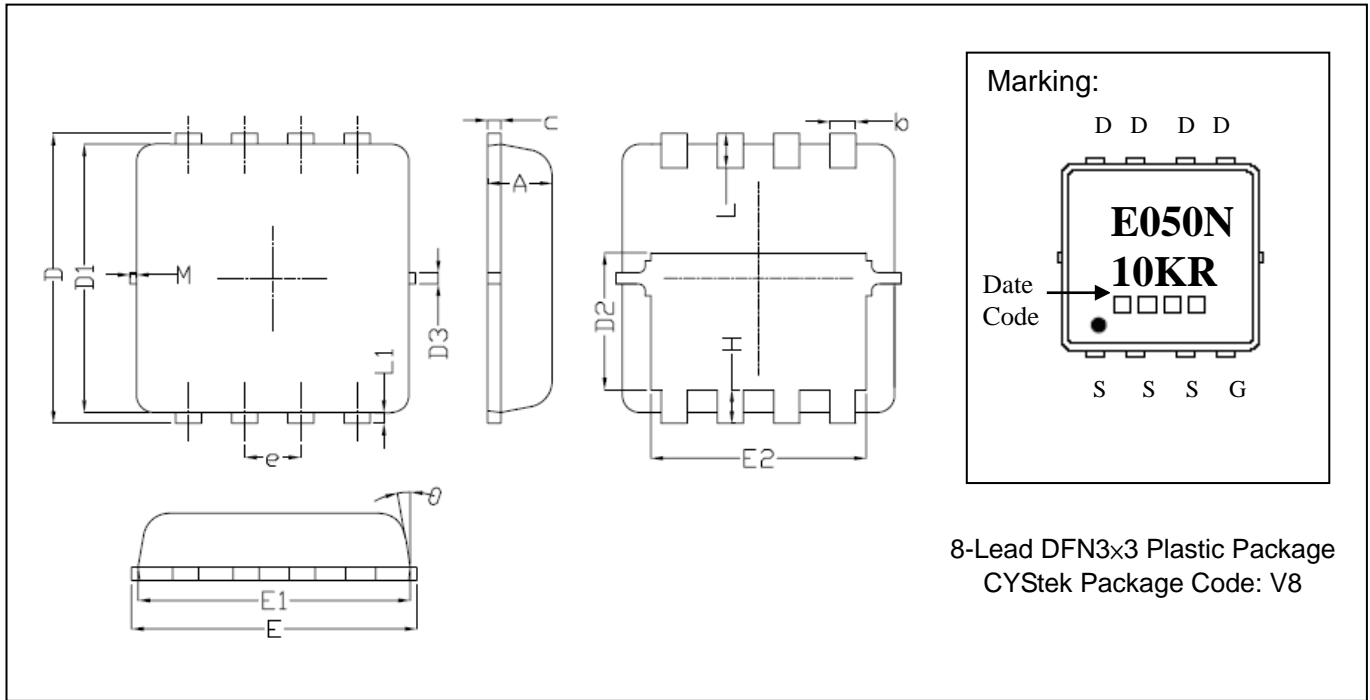
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note :1. All temperatures refer to topside of the package, measured on the package body surface.
 2.For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.

DFN3x3 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.70	0.80	0.028	0.031	E1	3.00	3.20	0.118	0.126
b	0.25	0.35	0.010	0.014	E2	2.39	2.59	0.094	0.102
c	0.10	0.25	0.004	0.010	e	0.65 BSC		0.026 BSC	
D	3.25	3.45	0.128	0.136	H	0.30	0.50	0.012	0.020
D1	3.00	3.20	0.118	0.126	L	0.30	0.50	0.012	0.020
D2	1.48	1.68	0.058	0.066	L1	0.13 TYP		0.005 TYP	
D3	0.13 TYP		0.005 TYP		theta	8°	12°	8°	12°
E	3.20	3.40	0.126	0.134	M	-	0.15	-	0.006

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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