

**N-Channel Enhancement Mode Power MOSFET**

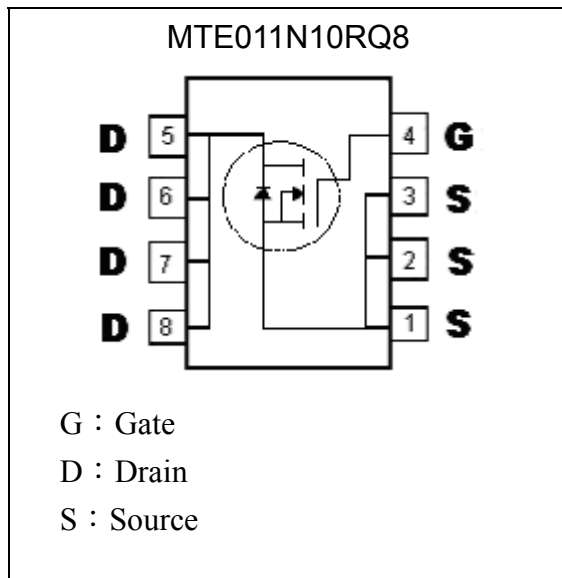
# MTE011N10RQ8

<b>BV<sub>DSS</sub></b>	<b>100V</b>
<b>I<sub>D</sub> @ T<sub>A</sub>=25°C, V<sub>GS</sub>=10V</b>	<b>10.6A</b>
<b>R<sub>DS(ON)</sub>@ V<sub>GS</sub>=10V, I<sub>D</sub>=4A</b>	<b>9.6 mΩ (typ)</b>

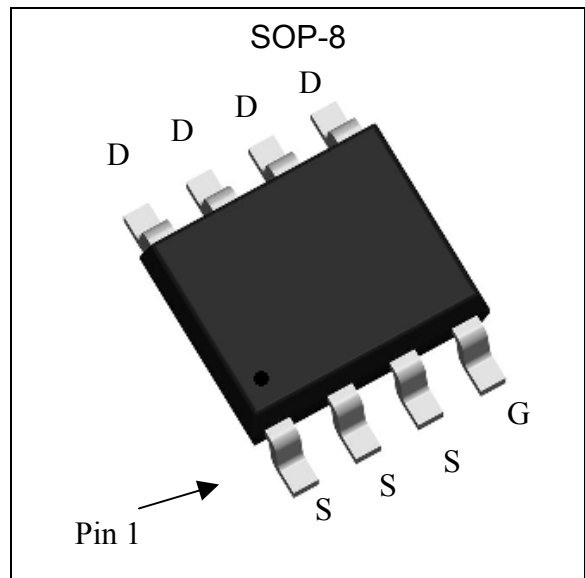
**Features**

- Single Drive Requirement
- Low On-resistance
- Fast Switching Characteristic
- Repetitive Avalanche Rated
- Pb-free & Halogen-free package

**Symbol**

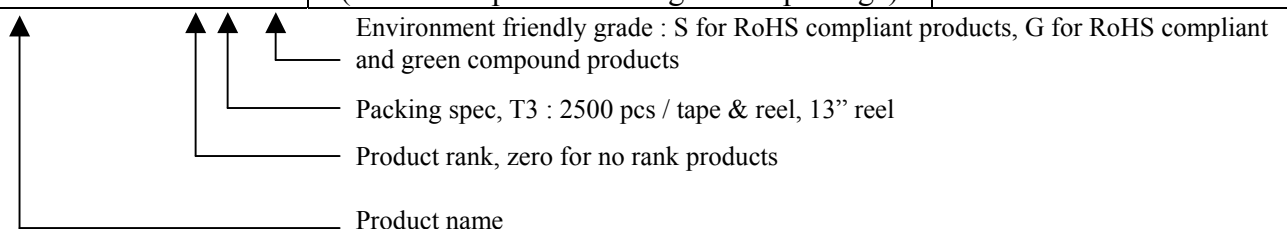


**Outline**



**Ordering Information**

Device	Package	Shipping
MTE011N10RQ8-0-T3-G	SOP-8 (RoHS compliant & Halogen-free package)	2500 pcs / Tape & Reel





**Absolute Maximum Ratings** (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	V <sub>DS</sub>	100	V	
Gate-Source Voltage	V <sub>GS</sub>	±30		
Continuous Drain Current @ T <sub>A</sub> =25°C, V <sub>GS</sub> =10V	I <sub>D</sub>	10.6	A	
Continuous Drain Current @ T <sub>A</sub> =70°C, V <sub>GS</sub> =10V		8.5		
Pulsed Drain Current	I <sub>DM</sub>	42 *1		
Avalanche Current	I <sub>AS</sub>	10.6		
Avalanche Energy @ L=5mH, I <sub>D</sub> =10.6A, V <sub>DD</sub> =50V	E <sub>AS</sub>	281	mJ	
Repetitive Avalanche Energy @ L=0.05mH	E <sub>AR</sub>	1.6 *2		
Total Power Dissipation	P <sub>D</sub>	T <sub>A</sub> =25 °C	3.1	W
		T <sub>A</sub> =70 °C	2	
Operating Junction and Storage Temperature	T <sub>j</sub> , T <sub>stg</sub>	-55~+150	°C	

Note : \*1. Pulse width limited by maximum junction temperature  
 \*2. Duty cycle ≤ 1%

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case	R <sub>th,j-c</sub>	20	°C/W
Thermal Resistance, Junction-to-ambient (Note)	R <sub>th,j-a</sub>	40	

Note : 40°C / W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper, t≤10s; 125°C/W when mounted on minimum pad.

**Characteristics (Tc=25°C, unless otherwise specified)**

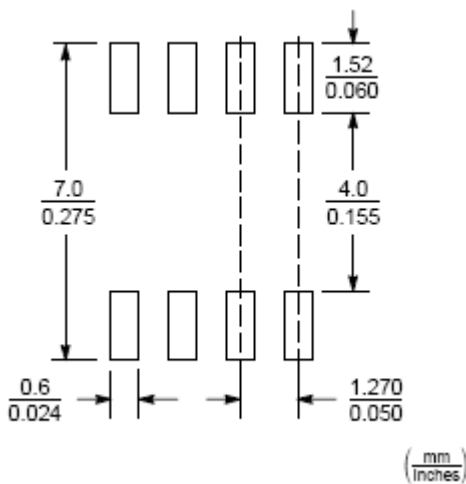
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	2	-	4		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> =250μA
G <sub>FS</sub>	-	11	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =5A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±30V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
	-	-	25		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
*R <sub>DS(ON)</sub>	-	9.6	13	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =4A
<b>Dynamic</b>					
Q <sub>g</sub> *1,2	-	46.2	-	nC	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =4A
Q <sub>gs</sub> *1,2	-	13	-		
Q <sub>gd</sub> *1,2	-	11.1	-		
C <sub>iss</sub>	-	2736	-	pF	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	371	-		
C <sub>rss</sub>	-	61	-		

**Characteristics (Cont. Tc=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Dynamic</b>					
$t_{d(ON)}$ *1, 2	-	27.4	-	ns	$V_{DS}=50V, I_D=4A, V_{GS}=10V, R_{GS}=1\Omega$
$t_r$ *1, 2	-	19.4	-		
$t_{d(OFF)}$ *1, 2	-	49	-		
$t_f$ *1, 2	-	10.6	-		
<b>Source-Drain Diode Ratings and Characteristics</b>					
$I_S$ *1	-	-	4	A	
$I_{SM}$ *3	-	-	16		
$V_{SD}$ *1	-	0.76	1.2	V	$I_S=4A, V_{GS}=0V$
$t_{rr}$	-	36	-	ns	$I_F=4A, dI_F/dt=100A/\mu s$
$Q_{rr}$	-	52	-	nC	

Note : \*1.Pulse Test : Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$   
 \*2.Independent of operating temperature  
 \*3.Pulse width limited by maximum junction temperature.

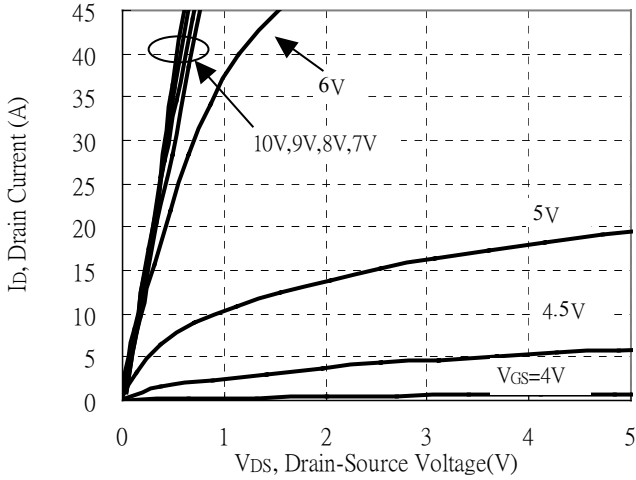
**Recommended Soldering Footprint**



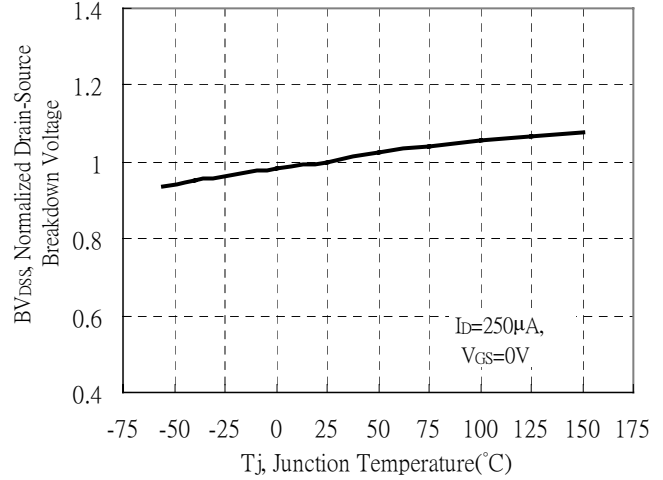


**Typical Characteristics**

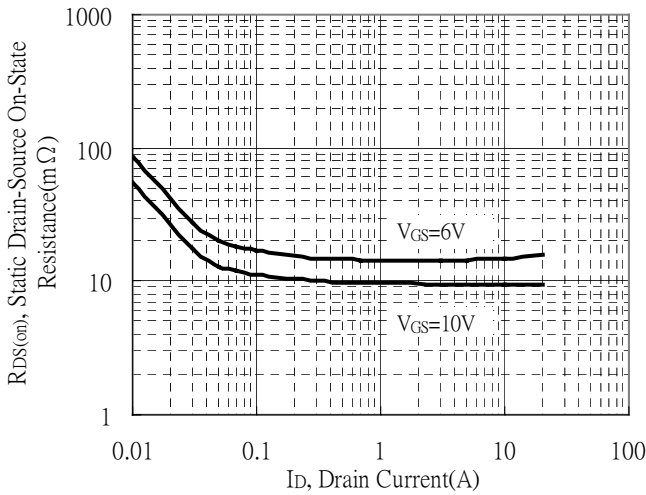
Typical Output Characteristics



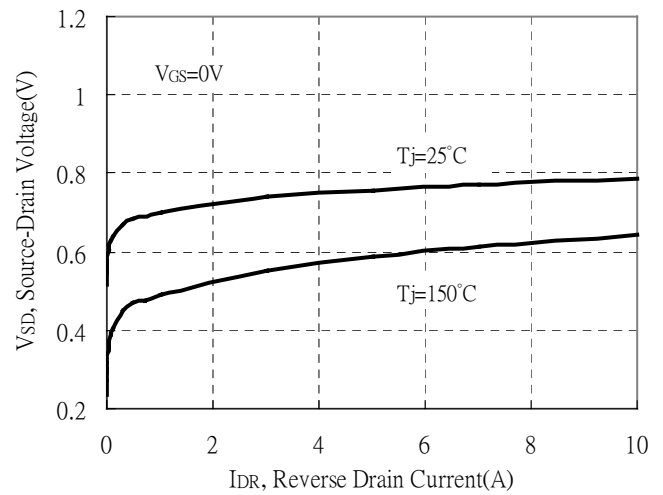
Brekdown Voltage vs Ambient Temperature



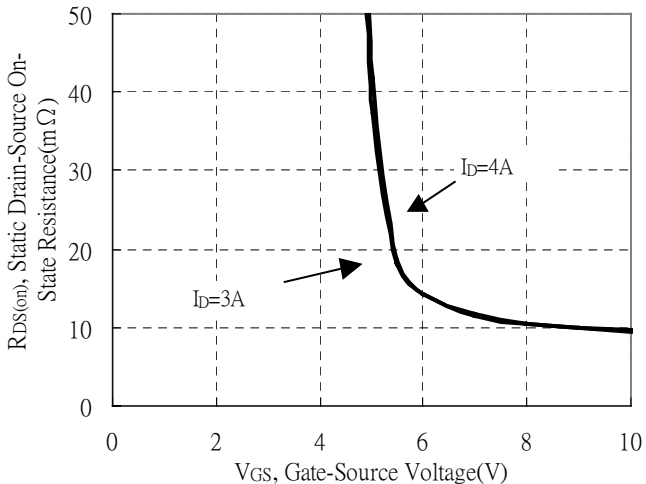
Static Drain-Source On-State resistance vs Drain Current



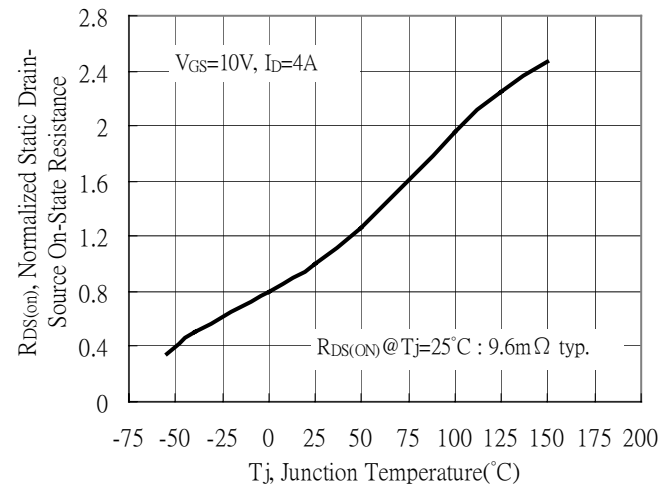
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

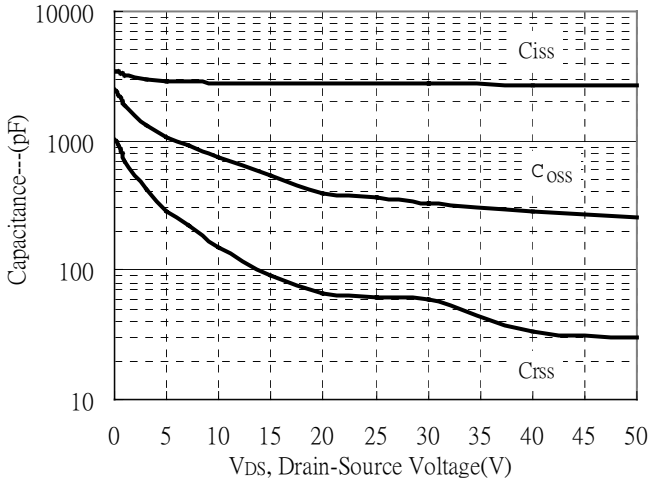


Drain-Source On-State Resistance vs Junction Temperature

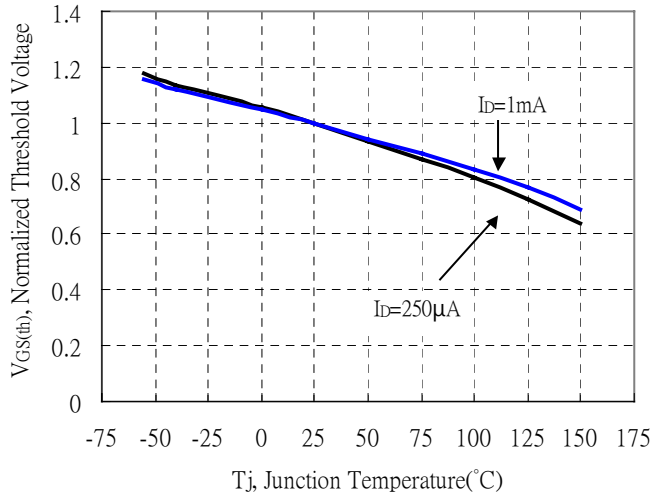


**Typical Characteristics(Cont.)**

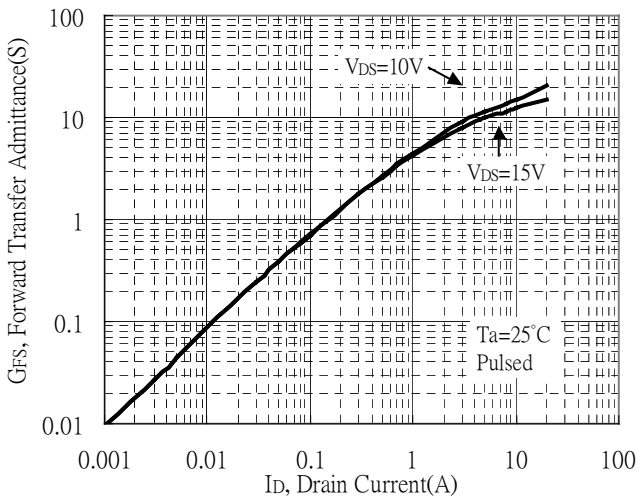
Capacitance vs Drain-to-Source Voltage



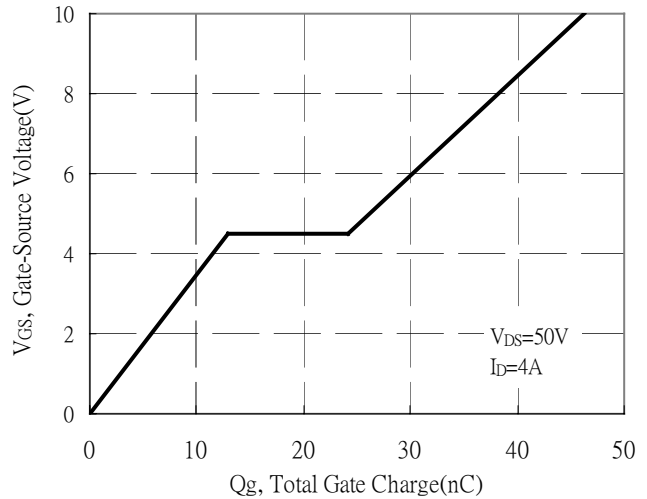
Threshold Voltage vs Junction Temperature



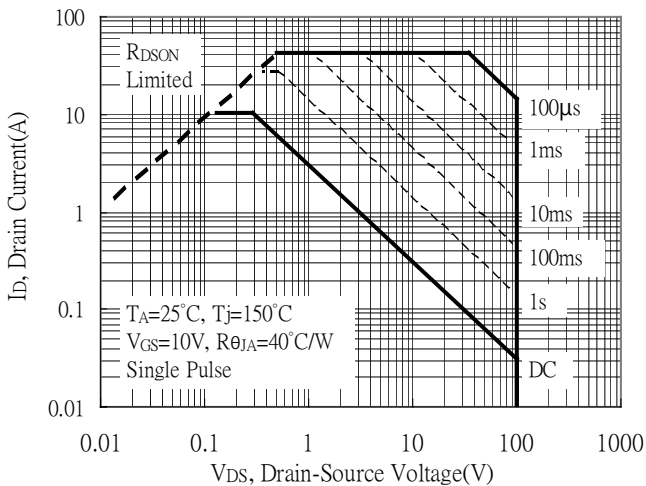
Forward Transfer Admittance vs Drain Current



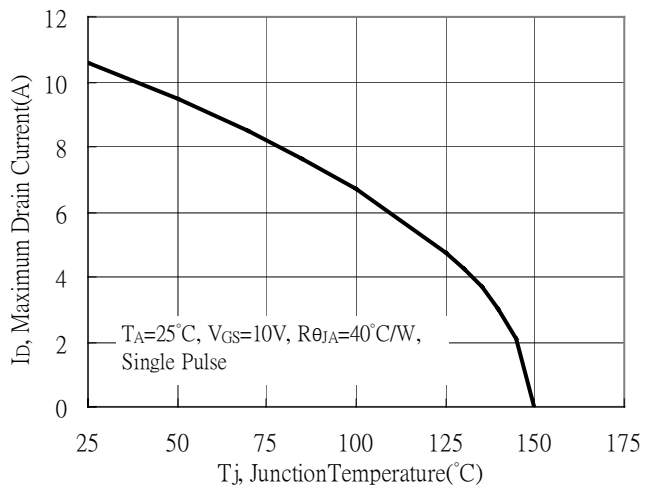
Gate Charge Characteristics



Maximum Safe Operating Area

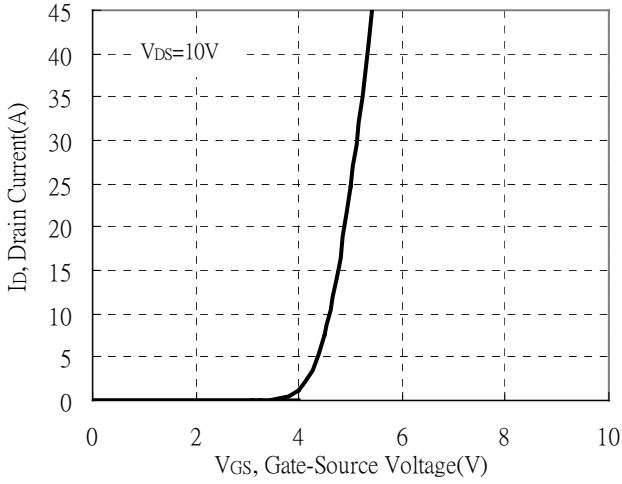


Maximum Drain Current vs Junction Temperature

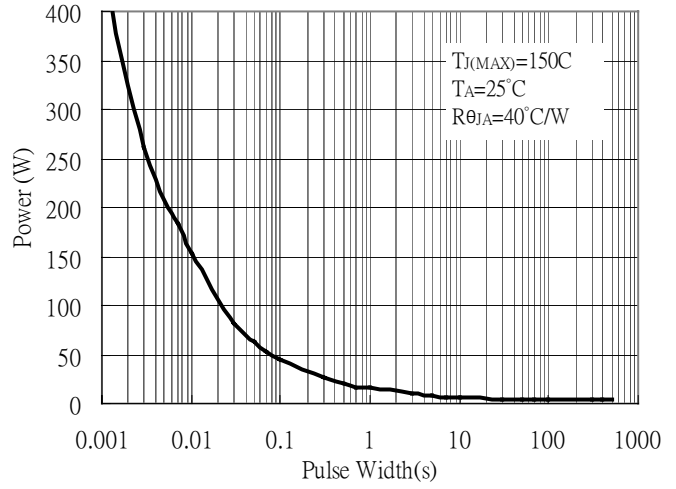


**Typical Characteristics(Cont.)**

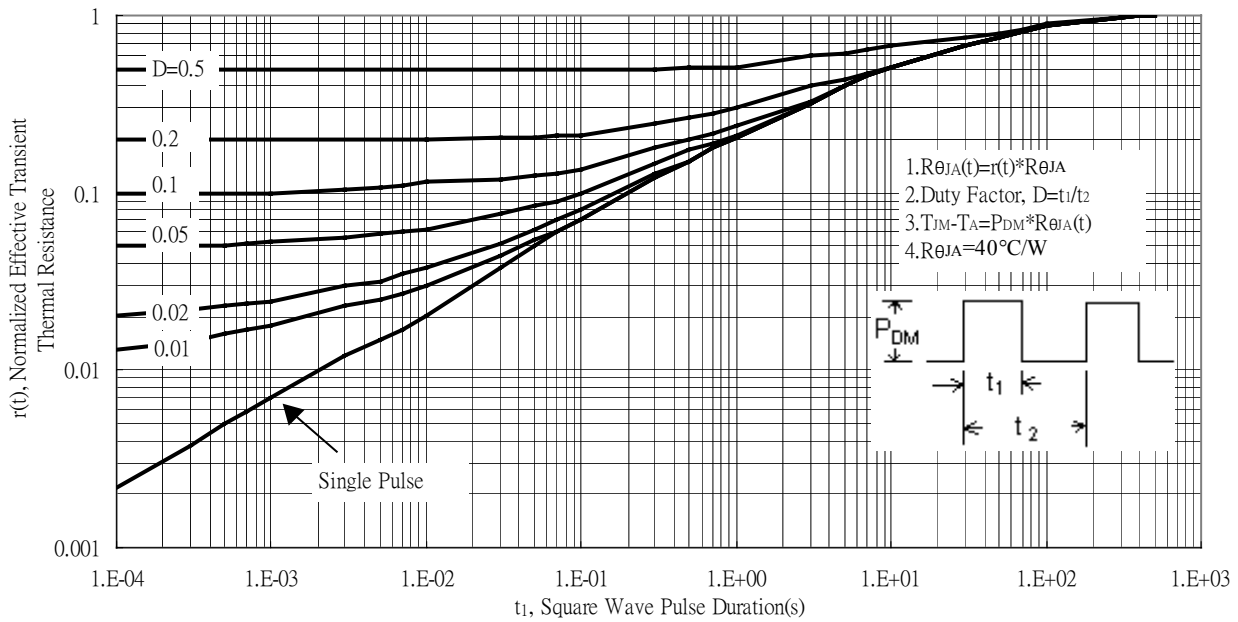
Typical Transfer Characteristics



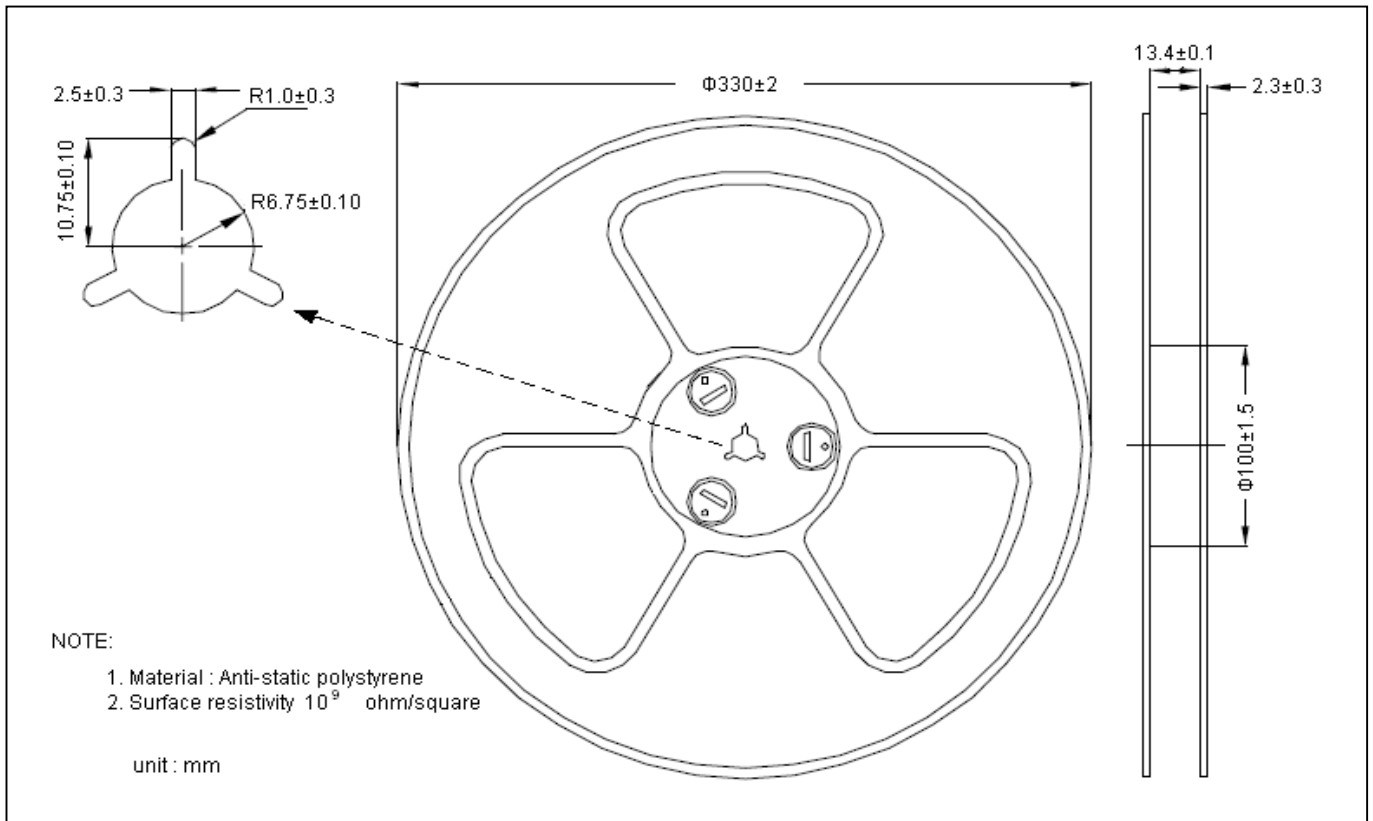
Single Pulse Maximum Power Dissipation



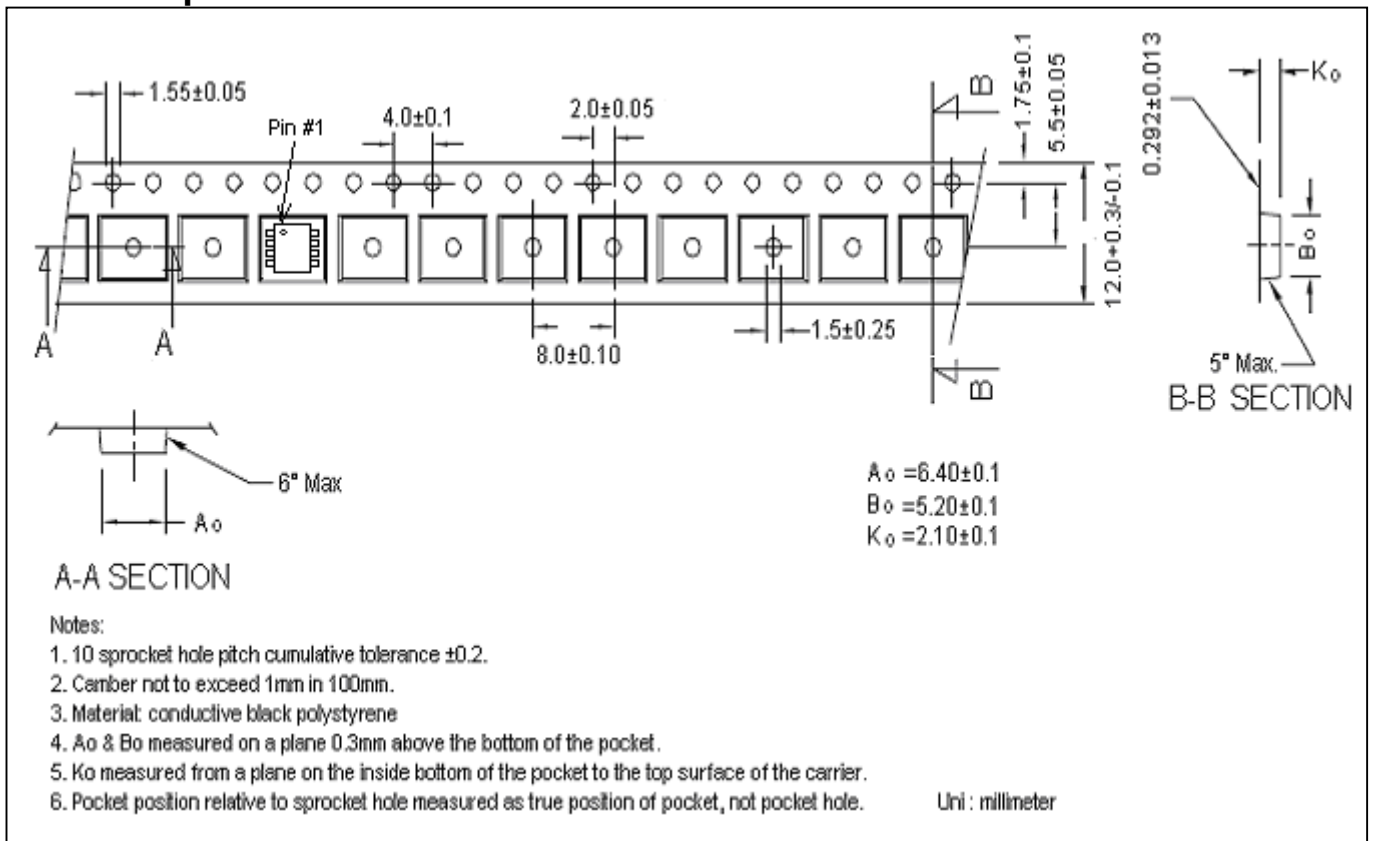
Transient Thermal Response Curves



**Reel Dimension**



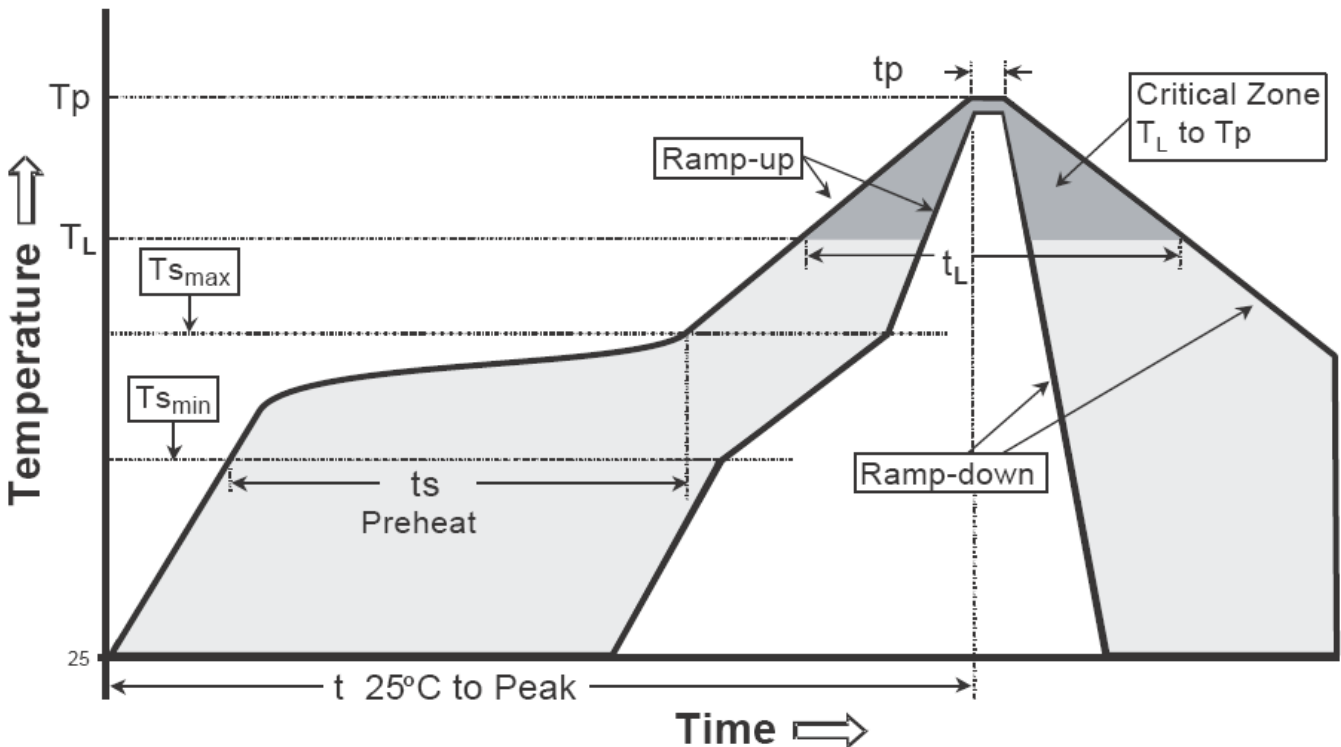
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**

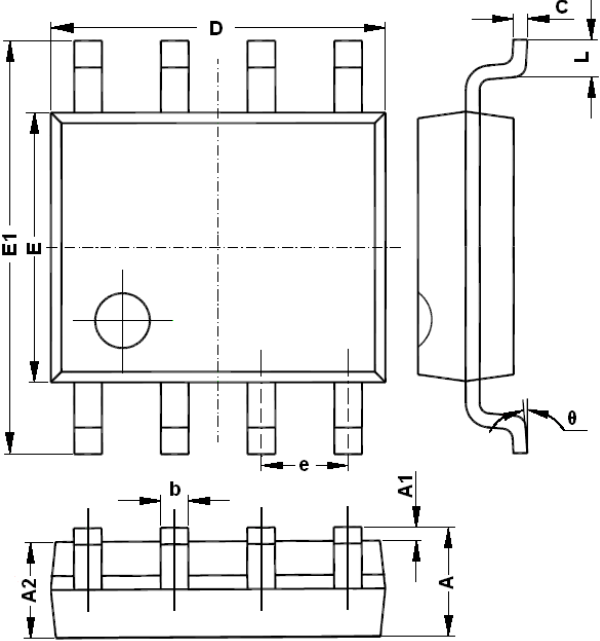


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tl)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

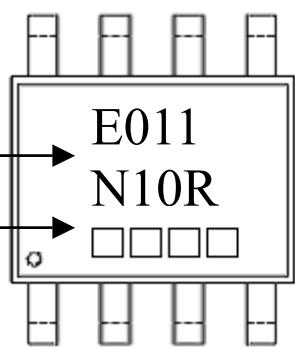


**SOP-8 Dimension**



The diagram shows three views of an 8-lead SOP-8 package: a top view with dimensions D, E1, and E; a side view with dimensions c, L, and  $\theta$ ; and a bottom view with dimensions A1, A2, A, b, and e.

**Marking:**



The marking diagram shows a rectangular package with eight leads. The top surface is marked with 'E011' and 'N10R'. Below these markings are four small squares representing the date code. Arrows point from the text 'Device Name' and 'Date Code' to the corresponding markings on the package.

Device Name → E011  
 N10R

Date Code → □ □ □ □

Date Code(counting from left to right) :  
 1<sup>st</sup> code: year code, the last digit of Christian year  
 2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C, Apr→D  
                   May→E, Jun→F, Jul→G, Aug→H, Sep→J,  
                   Oct→K, Nov→L, Dec→M  
 3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

**8-Lead SOP-8 Plastic Package**  
 CYStek Package Code: Q8

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	1.270 (BSC)		0.050 (BSC)	
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	$\theta$	0	8°	0	8°
D	4.700	5.100	0.185	0.200					

**Notes:** 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

**Important Notice:**

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.