

**P-Channel Enhancement Mode Power MOSFET**

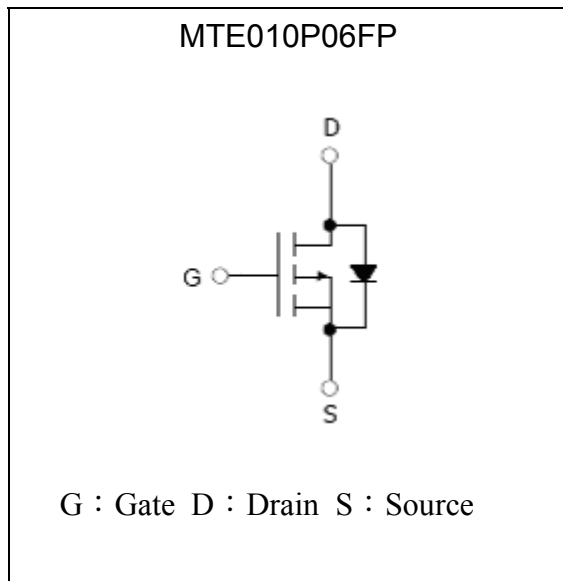
# MTE010P06FP

<b>BV<sub>DSS</sub></b>	<b>-60V</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>C</sub>=25°C</b>	<b>-50A</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>A</sub>=25°C</b>	<b>-9.5A</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=-10V, I<sub>D</sub>=-20A</b>	<b>11.3 mΩ (typ)</b>

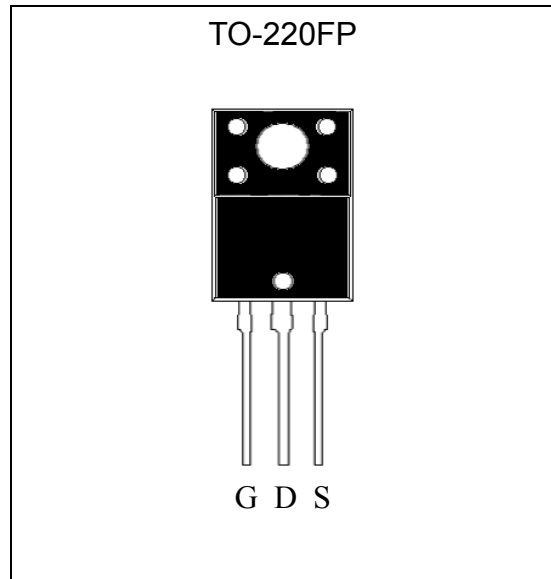
**Features**

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Insulating package, front/back side insulating voltage=2500V(AC)
- RoHS compliant package

**Symbol**

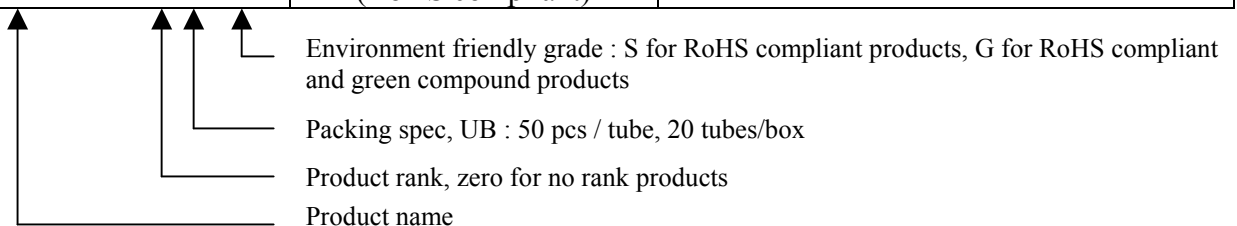


**Outline**



**Ordering Information**

Device	Package	Shipping
MTE010P06FP-0-UB-S	TO-220FP (RoHS compliant)	50 pcs/tube, 20 tubes/box, 4 boxes / carton



**Absolute Maximum Ratings** ( $T_C=25^{\circ}\text{C}$ )

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage (Note 1)	$V_{DS}$	-60	V	
Gate-Source Voltage	$V_{GS}$	$\pm 30$		
Continuous Drain Current @ $T_C=25^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$ (Note 1)	$I_D$	-50*	A	
Continuous Drain Current @ $T_C=100^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$ (Note 1)		-35.4*		
Continuous Drain Current @ $T_A=25^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$ (Note 2)	$I_{DSM}$	-9.5		
Continuous Drain Current @ $T_A=70^{\circ}\text{C}$ , $V_{GS}=-10\text{V}$ (Note 2)		-7.6		
Pulsed Drain Current	$I_{DM}$	-200*		
Avalanche Current @ $L=0.1\text{mH}$	$I_{AS}$	-60		
Single Pulse Avalanche Energy @ $L=1\text{mH}$ , $I_D=-35\text{Amps}$ , $V_{DD}=-50\text{V}$ (Note 4)	$E_{AS}$	612	mJ	
Repetitive Avalanche Energy (Note 3)	$E_{AR}$	6	W	
Power Dissipation	$P_D$	$T_C=25^{\circ}\text{C}$ (Note 1)		60
		$T_C=100^{\circ}\text{C}$ (Note 1)		30
	$P_{DSM}$	$T_A=25^{\circ}\text{C}$ (Note 2)		2
		$T_A=70^{\circ}\text{C}$ (Note 2)	1.3	
Maximum Temperature for Soldering @ Lead at 0.063 in(1.6mm) from case for 10 seconds	$T_L$	300	$^{\circ}\text{C}$	
Maximum Temperature for Soldering @ Package Body for 10 seconds	$T_{PKG}$	260		
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+175		

\*Drain current limited by maximum junction temperature

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	2.5	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max (Note 2)	$R_{\theta JA}$	62	

- Note : 1. The power dissipation  $P_D$  is based on  $T_{J(MAX)}=175^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with  $T_A=25^{\circ}\text{C}$ . The power dissipation  $P_{DSM}$  is based on  $R_{\theta JA}$  and the maximum allowed junction temperature of  $150^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design.
3. Pulse width limited by junction temperature  $T_{J(MAX)}=175^{\circ}\text{C}$ . Ratings are based on low frequency and low duty cycles to keep initial  $T_J=25^{\circ}\text{C}$ .
4. 100% tested by condition of  $V_{DD}=-25\text{V}$ ,  $I_D=-30\text{A}$ ,  $L=0.1\text{mH}$ ,  $V_{GS}=-10\text{V}$ .



**Characteristics (Tj=25°C, unless otherwise specified)**

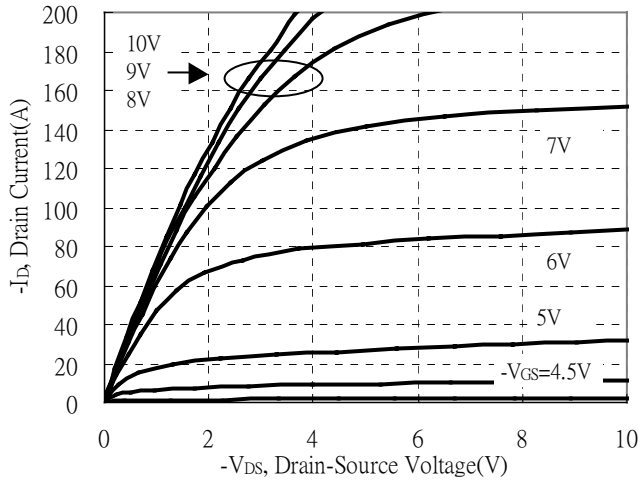
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-60	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	-56	-	mV/°C	Reference to 25°C, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-2	-	-4	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
*G <sub>FS</sub>	-	28.5	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V
	-	-	10		V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V, T <sub>j</sub> =85°C
*R <sub>DS(ON)</sub>	-	11.3	15	mΩ	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	67.2	-	nC	V <sub>DD</sub> =-48V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V
*Q <sub>gs</sub>	-	12.1	-		
*Q <sub>gd</sub>	-	25.4	-		
*t <sub>d(ON)</sub>	-	27.8	-	ns	V <sub>DD</sub> =-30V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V, R <sub>G</sub> =1 Ω
*t <sub>r</sub>	-	23.6	-		
*t <sub>d(OFF)</sub>	-	60.4	-		
*t <sub>f</sub>	-	15.8	-		
C <sub>iss</sub>	-	3465	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =-30V, f=1MHz
C <sub>oss</sub>	-	375	-		
C <sub>rss</sub>	-	229	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	-50	A	
*I <sub>SM</sub>	-	-	-200		
*V <sub>SD</sub>	-	-0.84	-1.2	V	I <sub>S</sub> =-20A, V <sub>GS</sub> =0V
*t <sub>rr</sub>	-	21.1	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =-20A, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	18.6	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

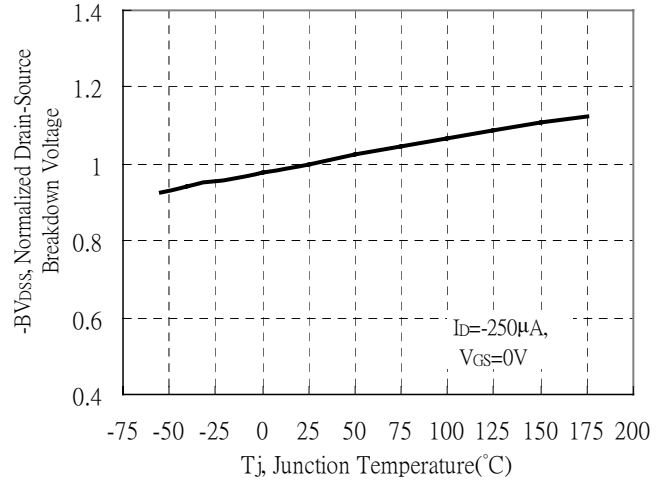


**Typical Characteristics**

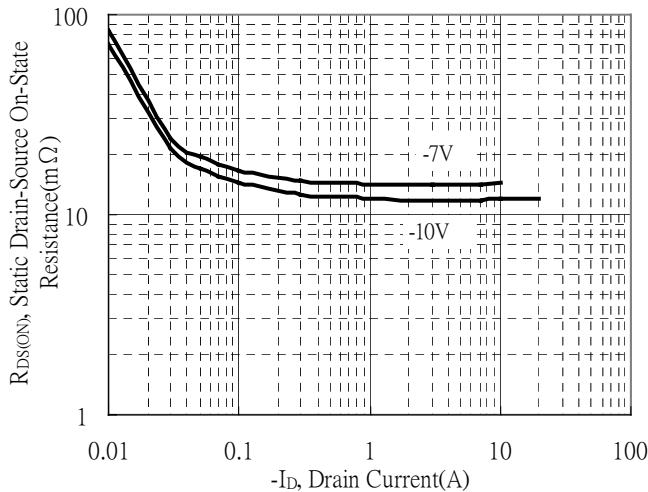
Typical Output Characteristics



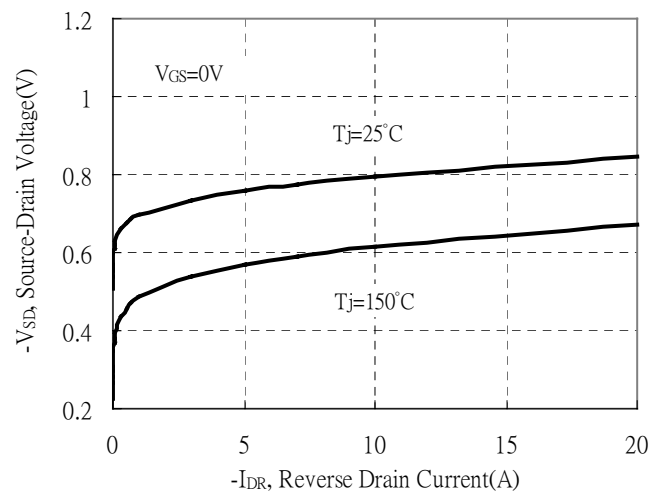
Brekdown Voltage vs Ambient Temperature



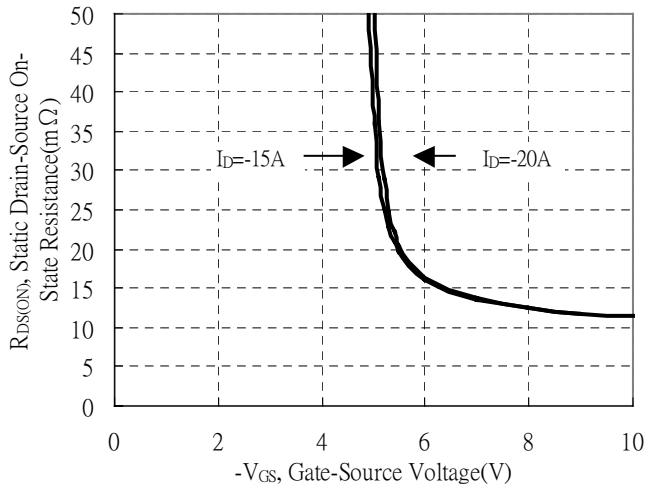
Static Drain-Source On-State resistance vs Drain Current



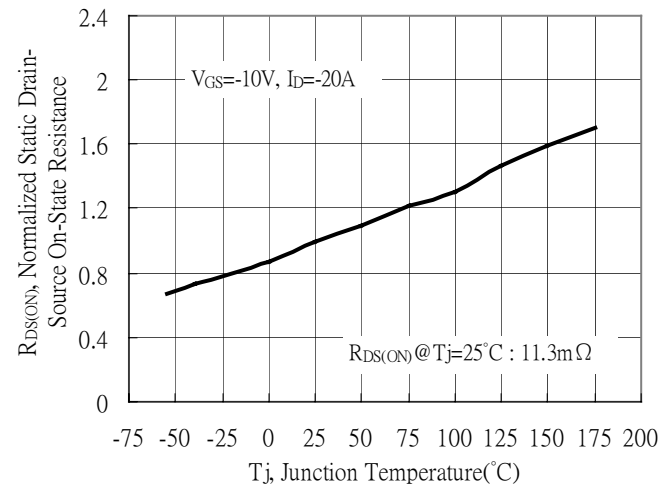
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

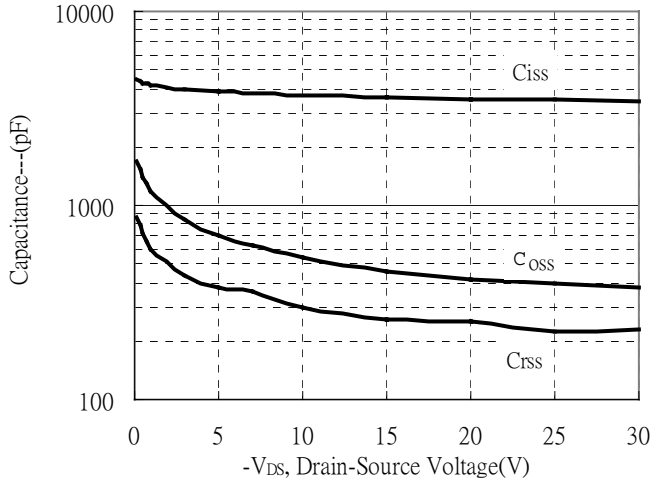


Drain-Source On-State Resistance vs Junction Temperature

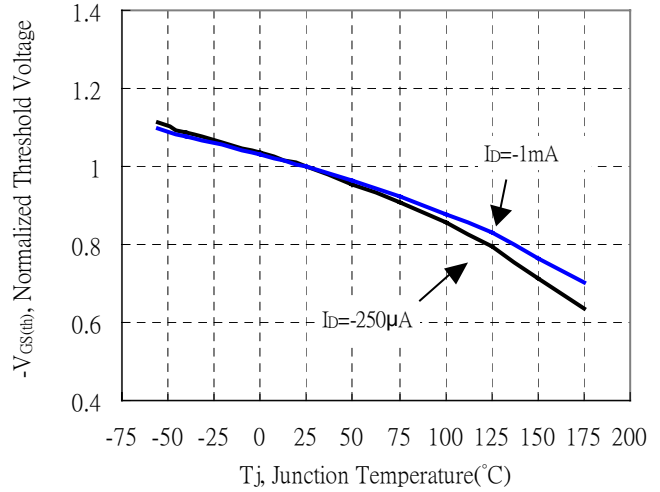


## Typical Characteristics(Cont.)

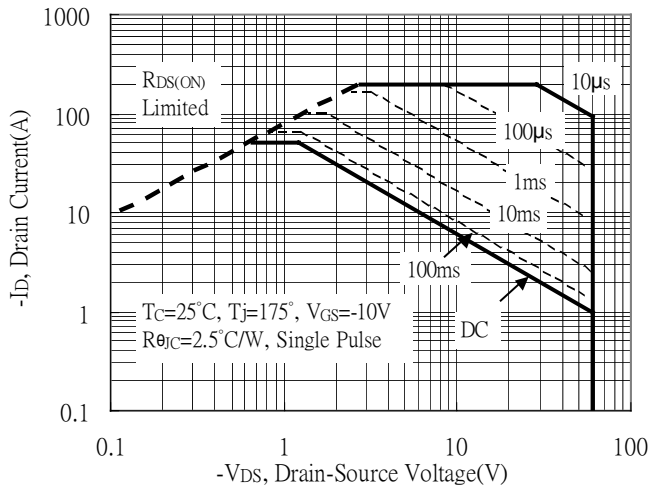
Capacitance vs Drain-to-Source Voltage



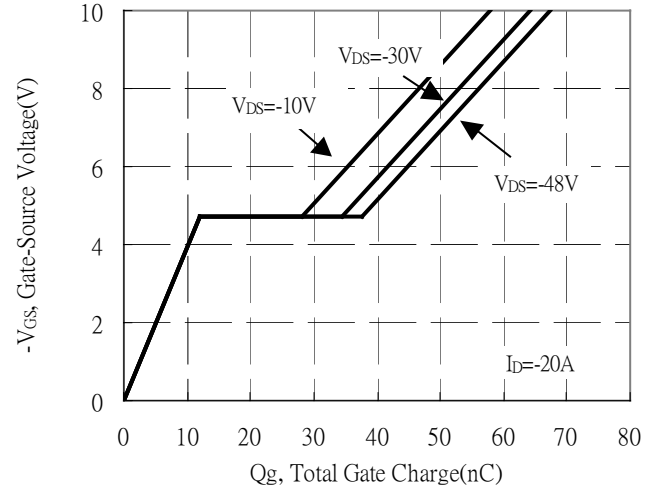
Threshold Voltage vs Junction Temperature



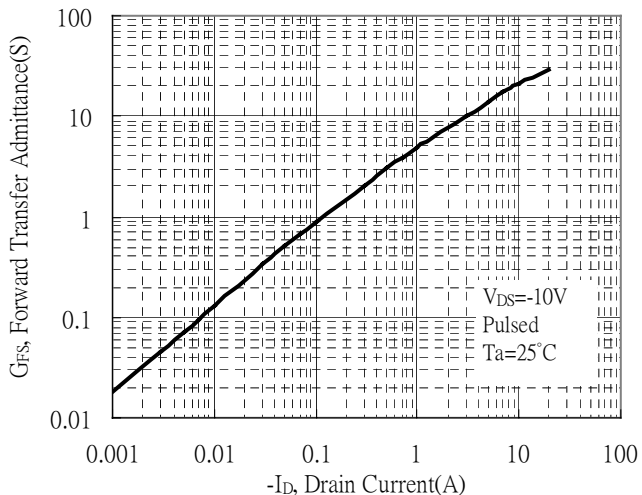
Maximum Safe Operating Area



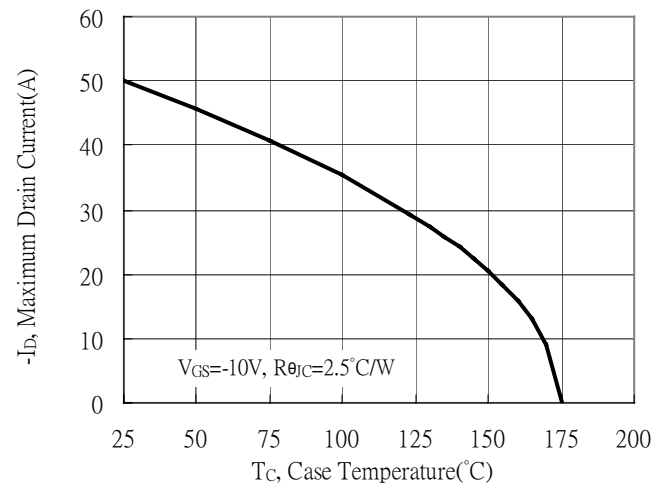
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current



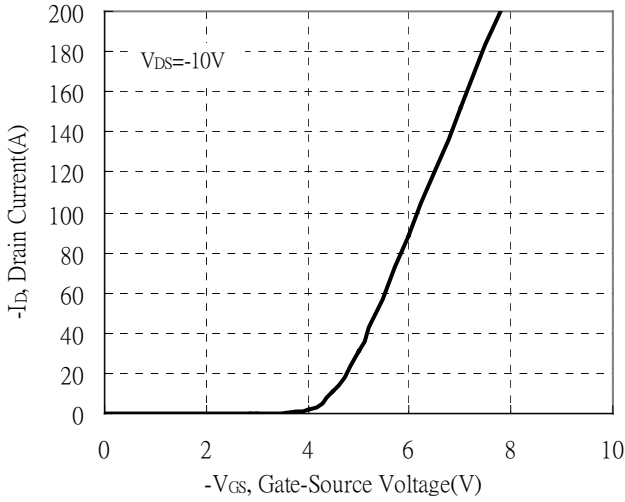
Maximum Drain Current vs Case Temperature



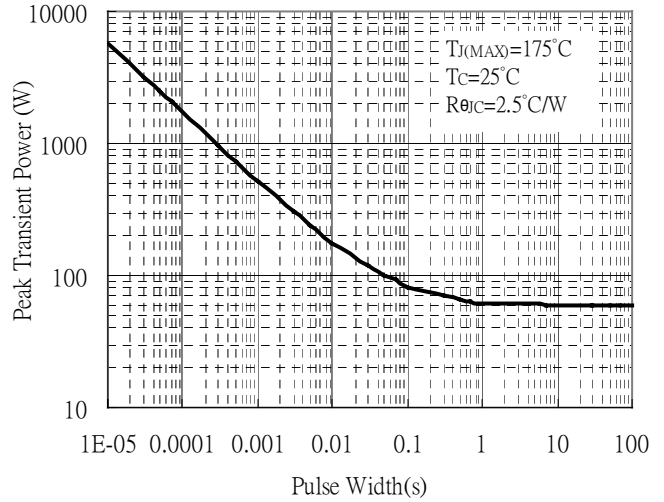


**Typical Characteristics(Cont.)**

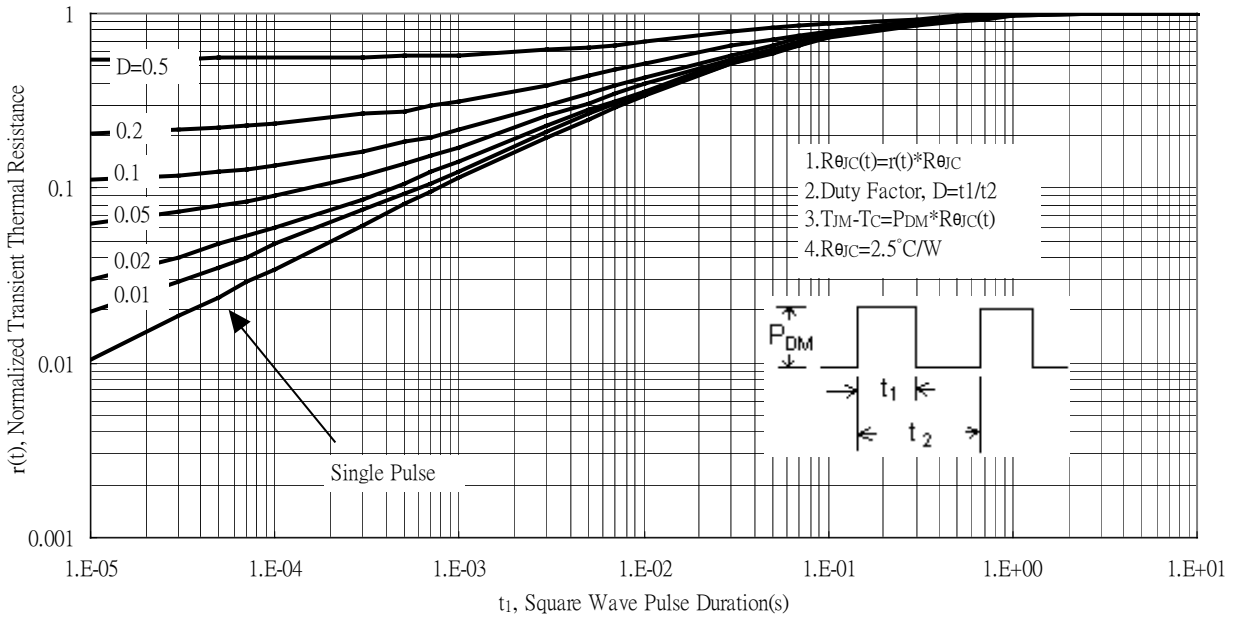
Typical Transfer Characteristics



Single Pulse Maximum Power Dissipation

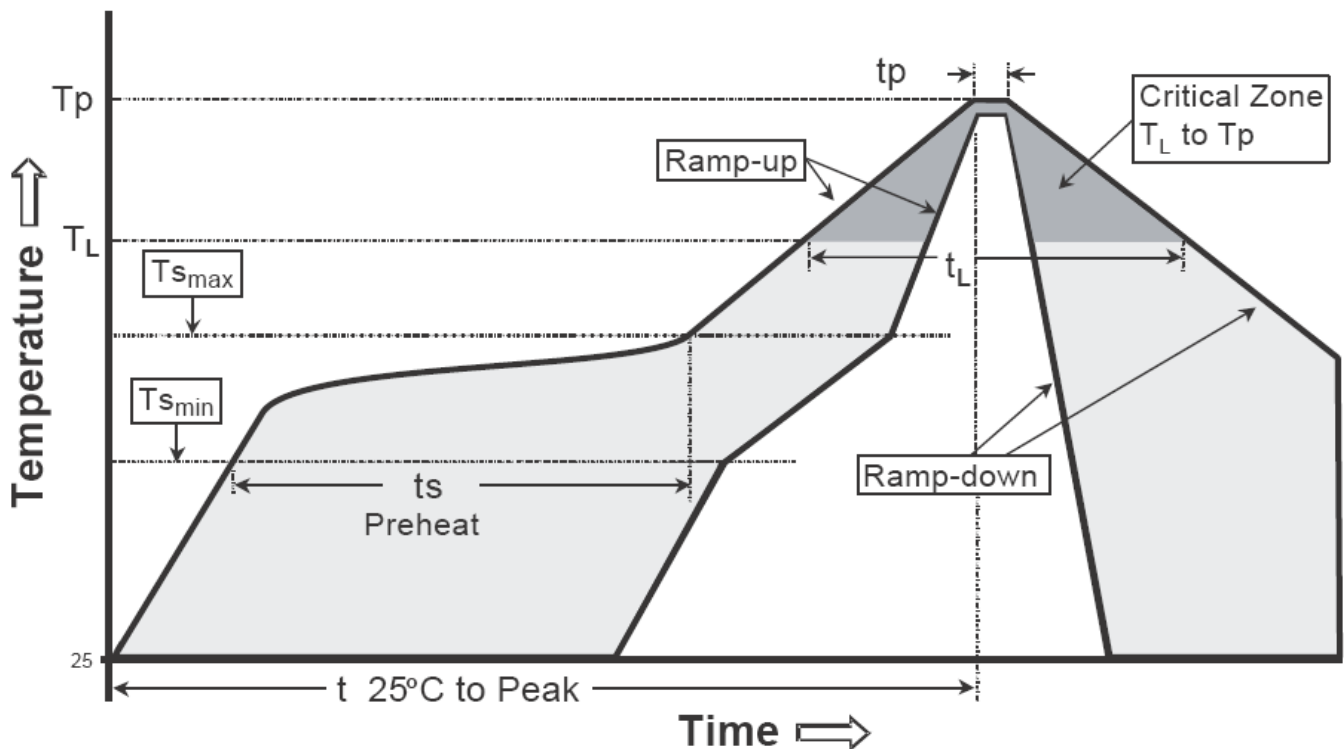


Transient Thermal Response Curves



**Recommended wave soldering condition**

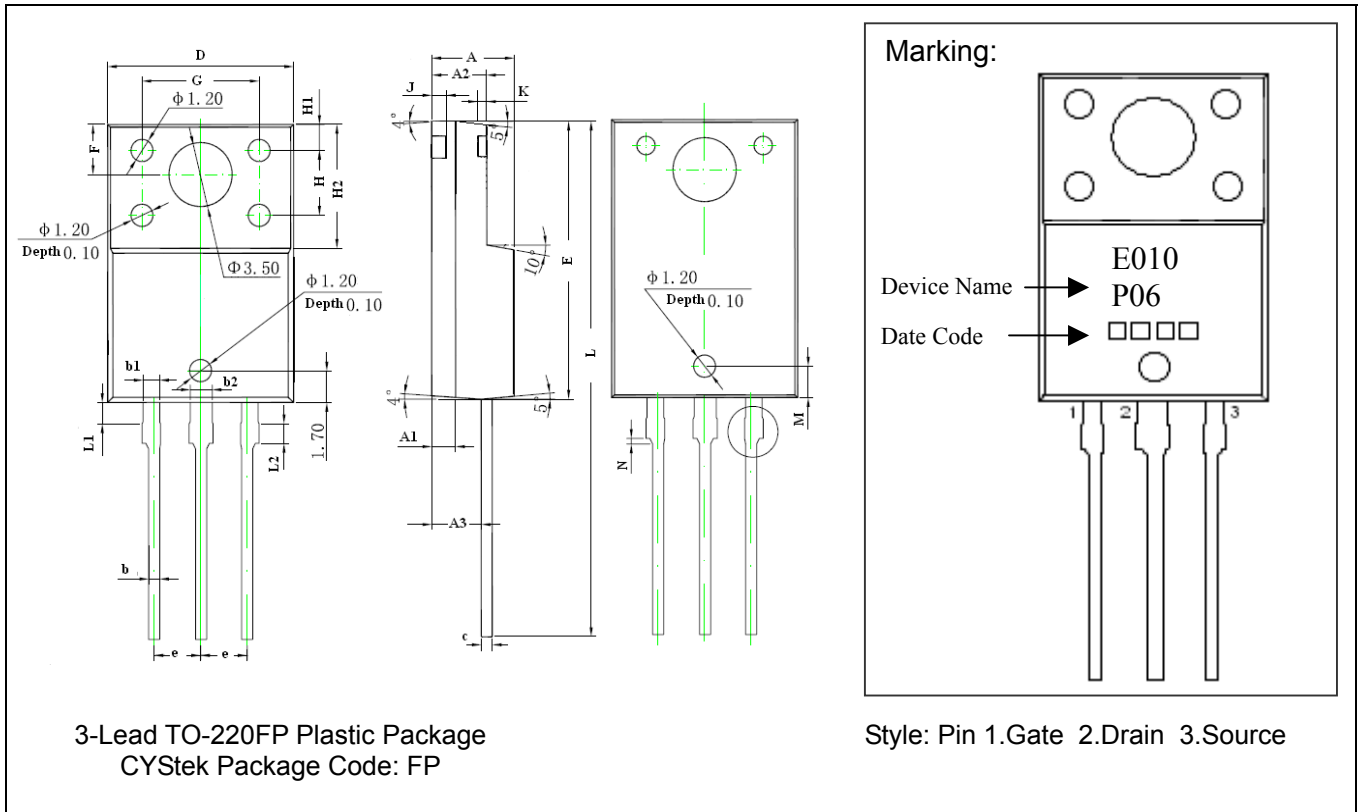
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

**Recommended temperature profile for IR reflow**


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>p</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**TO-220FP Dimension**



3-Lead TO-220FP Plastic Package  
 CYStek Package Code: FP

Style: Pin 1.Gate 2.Drain 3.Source

\*Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.171	0.183	4.35	4.65	G	0.246	0.258	6.25	6.55
A1	0.051 REF		1.300 REF		H	0.138 REF		3.50 REF	
A2	0.112	0.124	2.85	3.15	H1	0.055 REF		1.40 REF	
A3	0.102	0.110	2.60	2.80	H2	0.256	0.272	6.50	6.90
b	0.020	0.030	0.50	0.75	J	0.031 REF		0.80 REF	
b1	0.031	0.041	0.80	1.05	K	0.020		0.50 REF	
b2	0.047 REF		1.20 REF		L	1.102	1.118	28.00	28.40
c	0.020	0.030	0.500	0.750	L1	0.043	0.051	1.10	1.30
D	0.396	0.404	10.06	10.26	L2	0.036	0.043	0.92	1.08
E	0.583	0.598	14.80	15.20	M	0.067 REF		1.70 REF	
e	0.100 *		2.54*		N	0.012 REF		0.30 REF	
F	0.106 REF		2.70 REF						

- Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

**Important Notice:**

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.