

N-Channel Enhancement Mode Power MOSFET

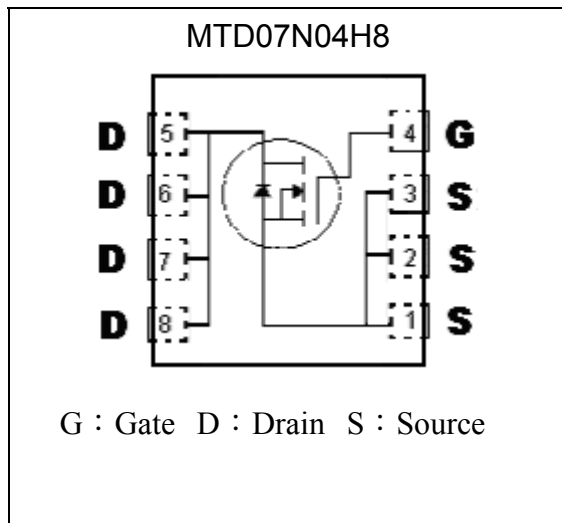
MTD07N04H8

BV_{DSS}	40V
I_D@V_{GS}=10V, T_C=25°C	67A
I_D@V_{GS}=10V, T_C=100°C	42.4A
I_D@V_{GS}=10V, T_A=25°C	14A
I_D@V_{GS}=10V, T_A=70°C	11.2A
R_{DS(ON)}@V_{GS}=10V, I_D=11A	4.7mΩ (typ)
R_{DS(ON)}@V_{GS}=4.5V, I_D=9A	8.0mΩ (typ)

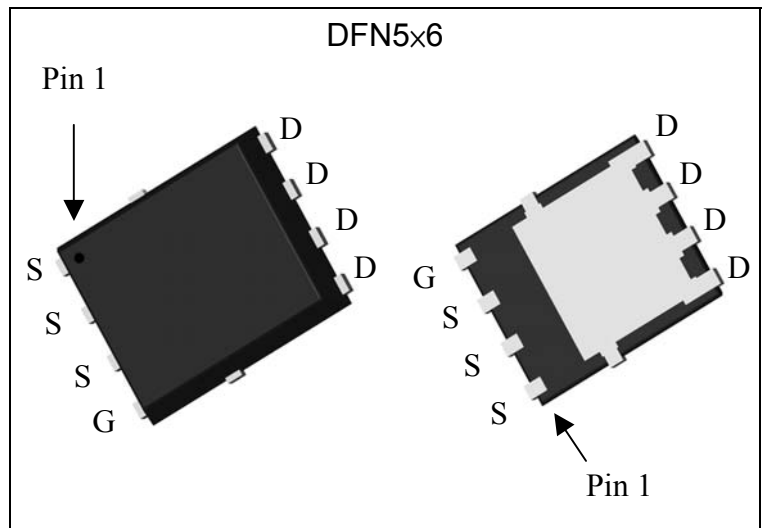
Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and Halogen-free package

Symbol

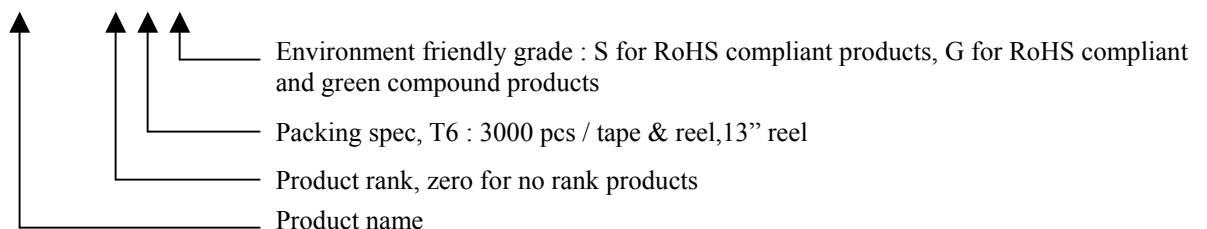


Outline



Ordering Information

Device	Package	Shipping
MTD07N04H8-0-T6-G	DFN 5 × 6 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings ($T_C=25^{\circ}C$, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage	(Note 1)	V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 25	
Continuous Drain Current @ $T_C=25^{\circ}C$, $V_{GS}=10V$	(Note 1)	I_D	67	A
Continuous Drain Current @ $T_C=100^{\circ}C$, $V_{GS}=10V$	(Note 1)		42.4	
Continuous Drain Current @ $T_A=25^{\circ}C$, $V_{GS}=10V$	(Note 2)	I_{DSM}	14	
Continuous Drain Current @ $T_A=70^{\circ}C$, $V_{GS}=10V$	(Note 2)		11.2	
Pulsed Drain Current @ $V_{GS}=10V$	(Note 3)	I_{DM}	200	
Avalanche Current	(Note 3)	I_{AS}	20	
Single Pulse Avalanche Energy @ $L=0.5mH$, $I_D=20Amps$, $V_{DD}=50V$	(Note 2)	E_{AS}	100	
Repetitive Avalanche Energy	(Note 3)	E_{AR}	5.7	W
Power Dissipation	$T_C=25^{\circ}C$	P_D	57	
	$T_C=100^{\circ}C$		36.5	
	$T_A=25^{\circ}C$	P_{DSM}	2.5	
	$T_A=70^{\circ}C$		1.6	
Operating Junction and Storage Temperature		T_j, T_{stg}	-55~+150	$^{\circ}C$

*Drain current limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	2.2	$^{\circ}C/W$
Thermal Resistance, Junction-to-ambient, max (Note 4)	$R_{\theta JA}$	50	

- Note :
- The power dissipation P_D is based on $T_{j(MAX)}=150^{\circ}C$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
 - The value of $R_{\theta JA}$ is measured with the device mounted on 1 in²FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}C$. The value in any given application depends on the user's specific board design. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of $150^{\circ}C$.
 - Ratings are based on low frequency and low duty cycles to keep initial $T_j=25^{\circ}C$.
 - When mounted on 1 in² copper pad of FR-4 board, $t \leq 10s$; $125^{\circ}C/W$ when mounted on minimum copper pad.

Characteristics ($T_j=25^{\circ}C$, unless otherwise specified)

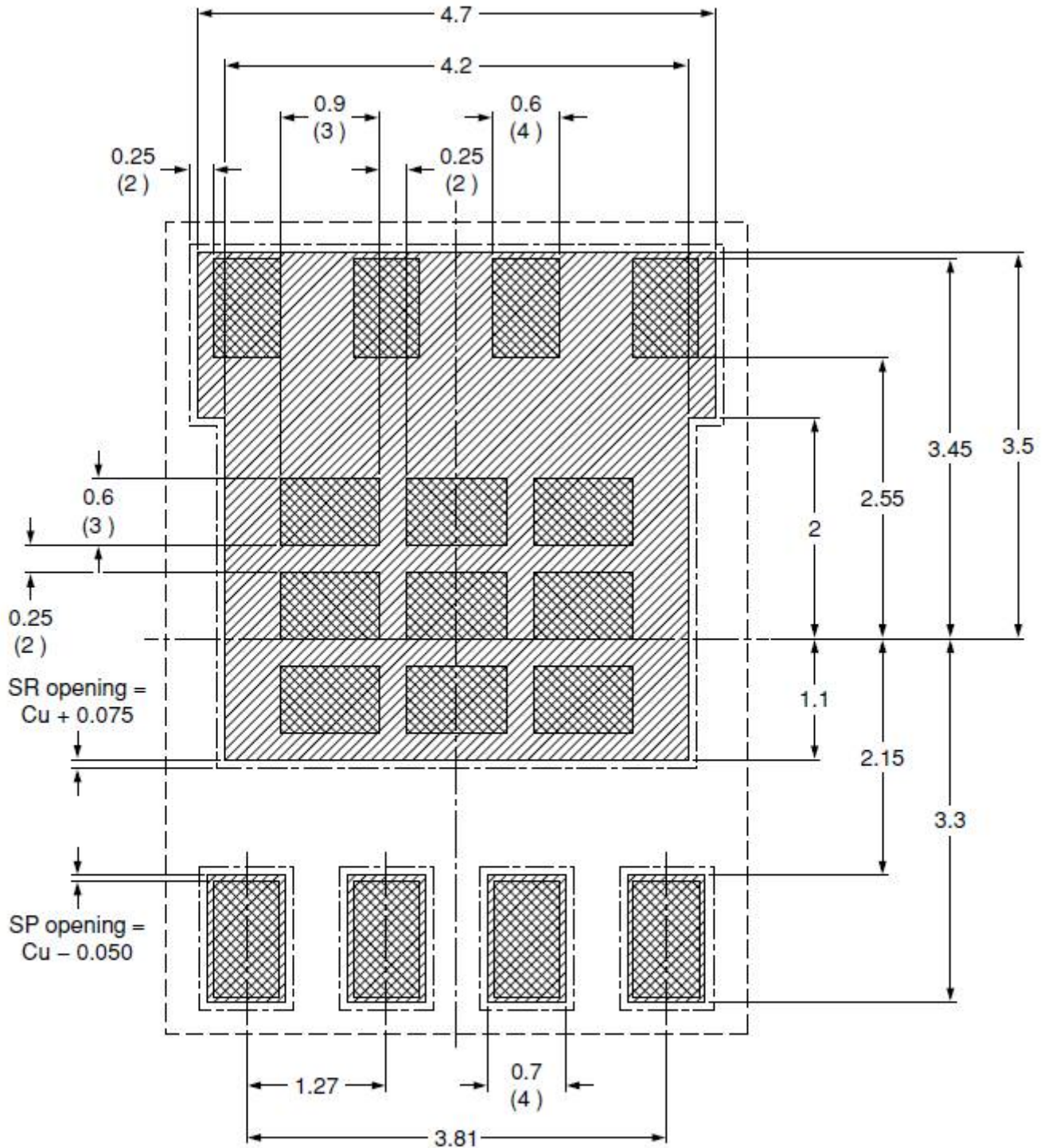
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	40	-	-	V	$V_{GS}=0V, I_D=250\mu A$
$\Delta BV_{DSS}/\Delta T_j$	-	0.03	-	$V/^{\circ}C$	Reference to $25^{\circ}C, I_D=250\mu A$
$V_{GS(th)}$	1.5	-	2.5	V	$V_{DS} = V_{GS}, I_D=250\mu A$
* G_{FS}	-	24	-	S	$V_{DS} = 5V, I_D=11A$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 25V, V_{DS}=0V$
I_{DSS}	-	-	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
	-	-	25		$V_{DS} = 40V, V_{GS} = 0V, T_j=125^{\circ}C$



*R _{DS(ON)}	-	4.7	6	mΩ	V _{GS} =10V, I _D =11A
	-	8	11		V _{GS} =4.5V, I _D =9A
Dynamic					
*Q _g	-	30.2	45	nC	V _{DS} =20V, I _D =16A, V _{GS} =10V
*Q _{gs}	-	6.5	-		
*Q _{gd}	-	7.0	-		
*t _{d(ON)}	-	15.6	24	ns	V _{DS} =20V, I _D =1A, V _{GS} =10V, R _G =6Ω
*t _r	-	19.6	30		
*t _{d(OFF)}	-	49.6	75		
*t _f	-	12.4	19		
C _{iss}	-	1514	1895	pF	V _{GS} =0V, V _{DS} =20V, f=1MHz
C _{oss}	-	188	235		
C _{rss}	-	119	150		
R _g	-	1.7	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	67	A	
*I _{SM}	-	-	200		
*V _{SD}	-	0.81	1.1	V	I _S =10A, V _{GS} =0V
*t _{rr}	-	12	-	ns	V _{GS} =0V, I _F =10A, dI/dt=100A/μs
*Q _{rr}	-	6.5	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint & Stencil Design



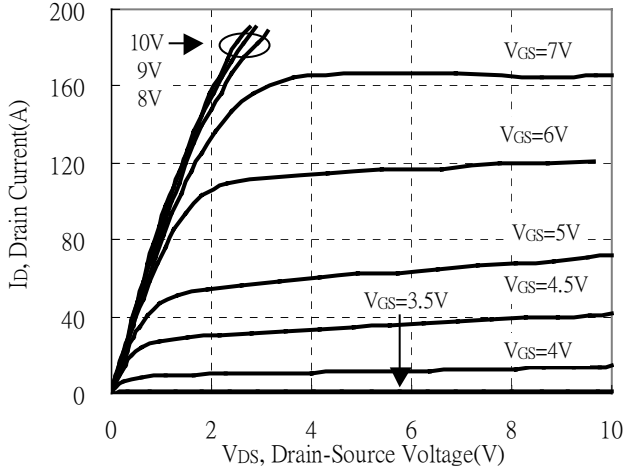
- | | |
|---|--|
|  solder lands |  solder paste
125 μm stencil |
|  solder resist |  occupied area |

unit : mm

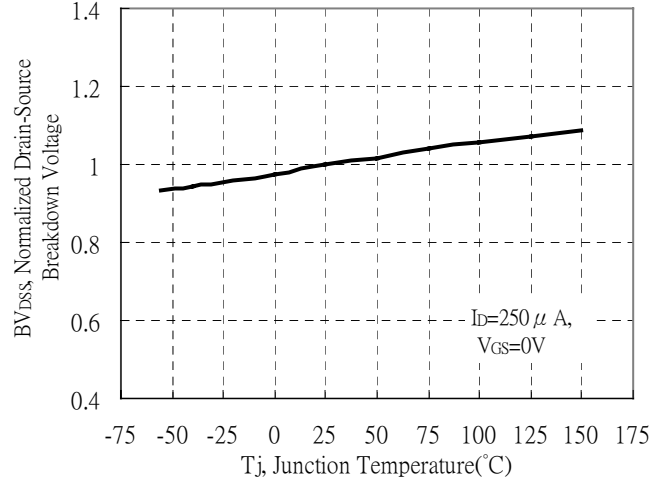


Typical Characteristics

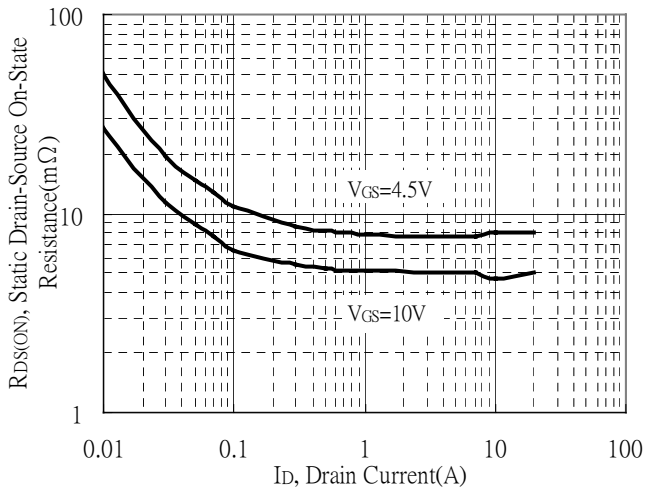
Typical Output Characteristics



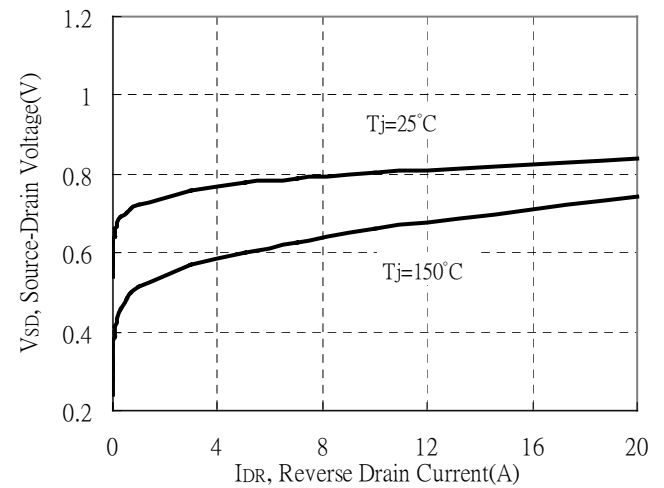
Brekdown Voltage vs Ambient Temperature



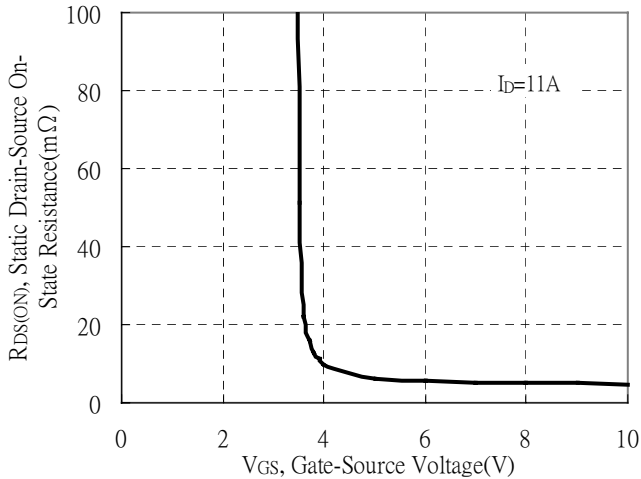
Static Drain-Source On-State resistance vs Drain Current



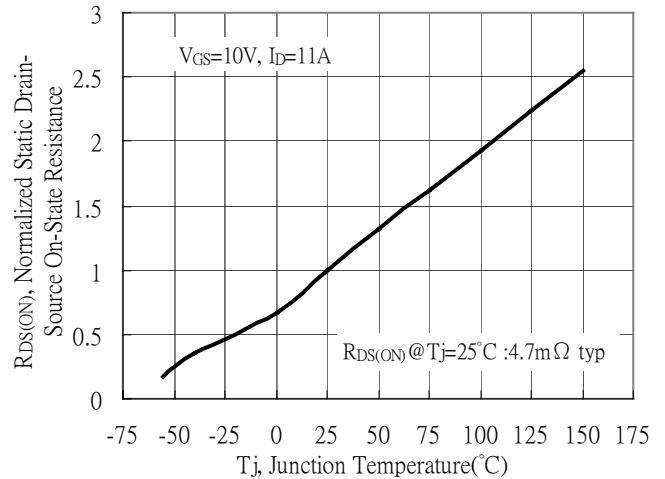
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

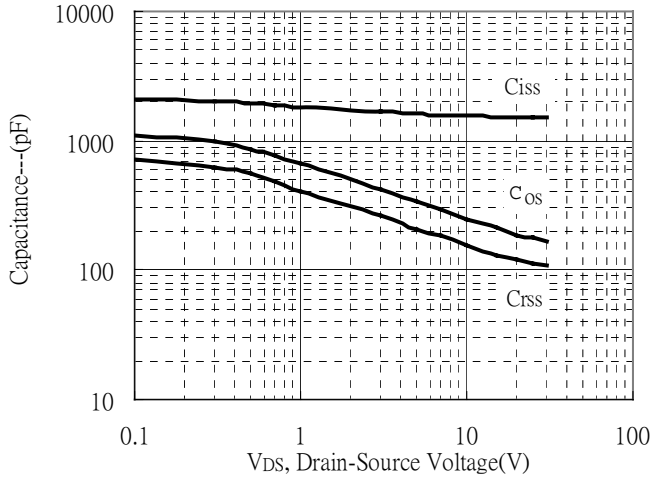


Drain-Source On-State Resistance vs Junction Temperature

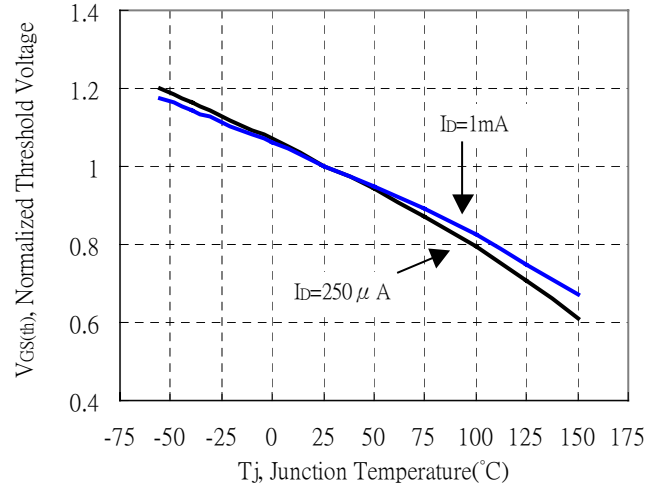


Typical Characteristics(Cont.)

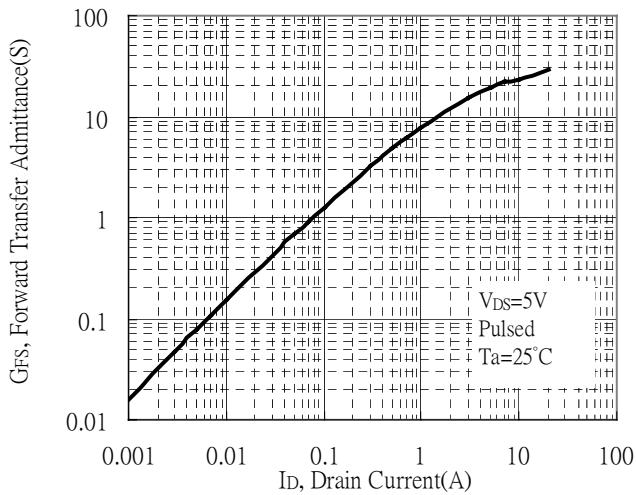
Capacitance vs Drain-to-Source Voltage



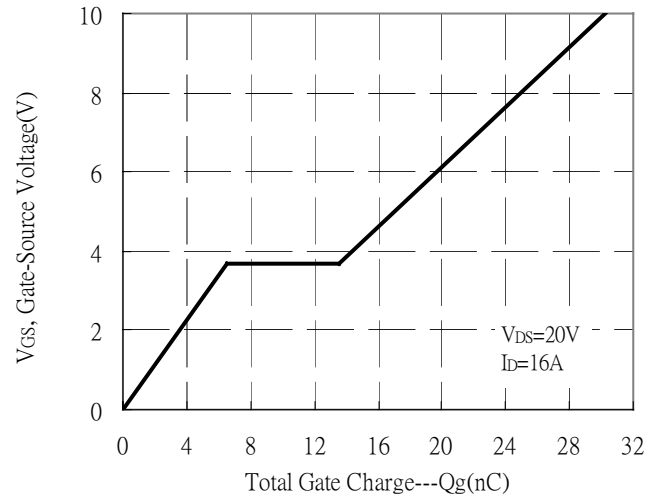
Normalized Threshold Voltage vs Junction Temperature



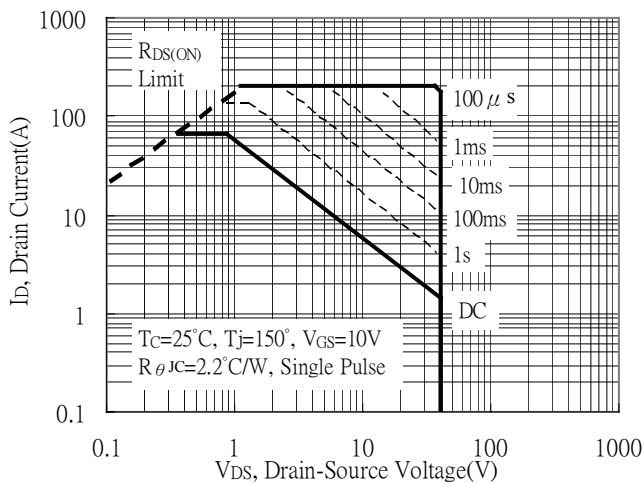
Forward Transfer Admittance vs Drain Current



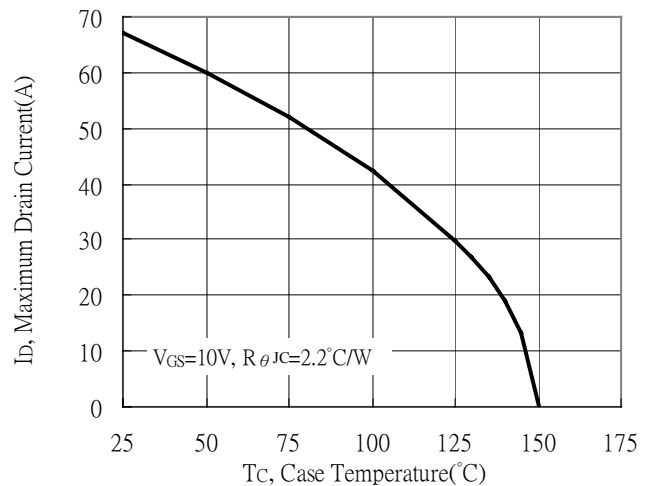
Gate Charge Characteristics



Maximum Safe Operating Area

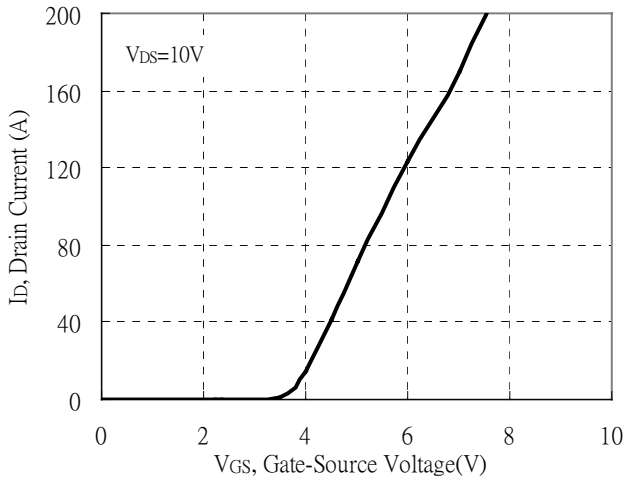


Maximum Drain Current vs Case Temperature

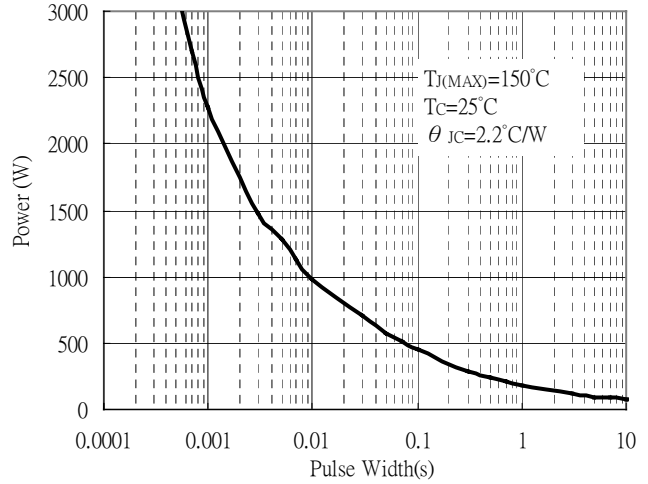


Typical Characteristics(Cont.)

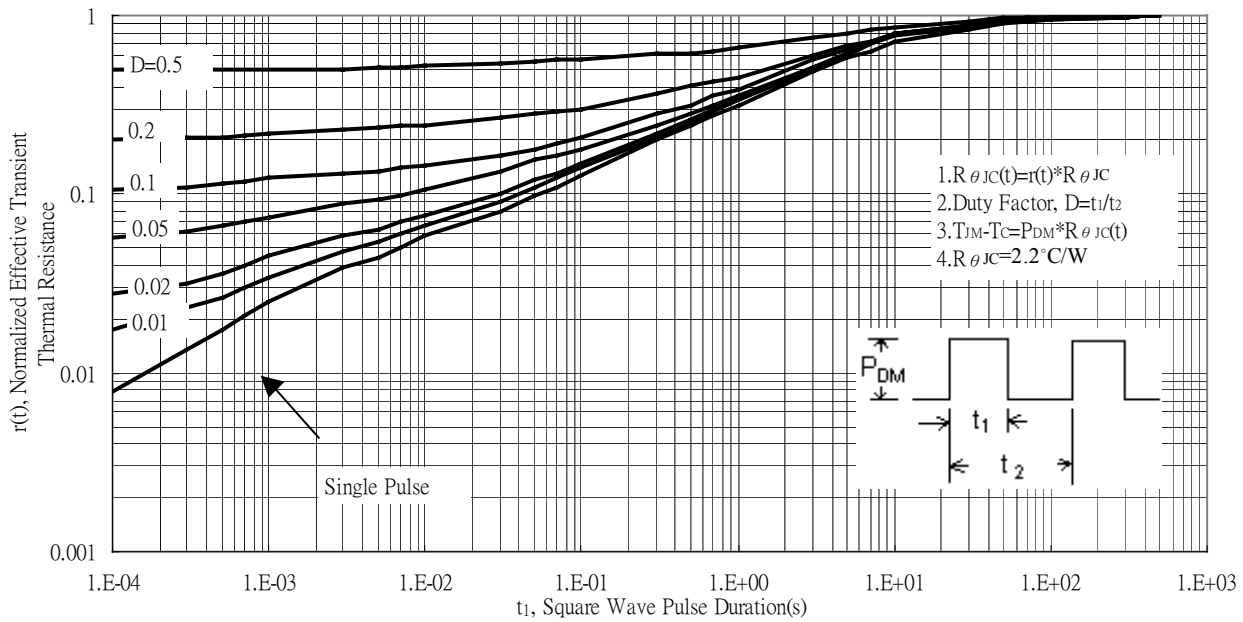
Typical Transfer Characteristics



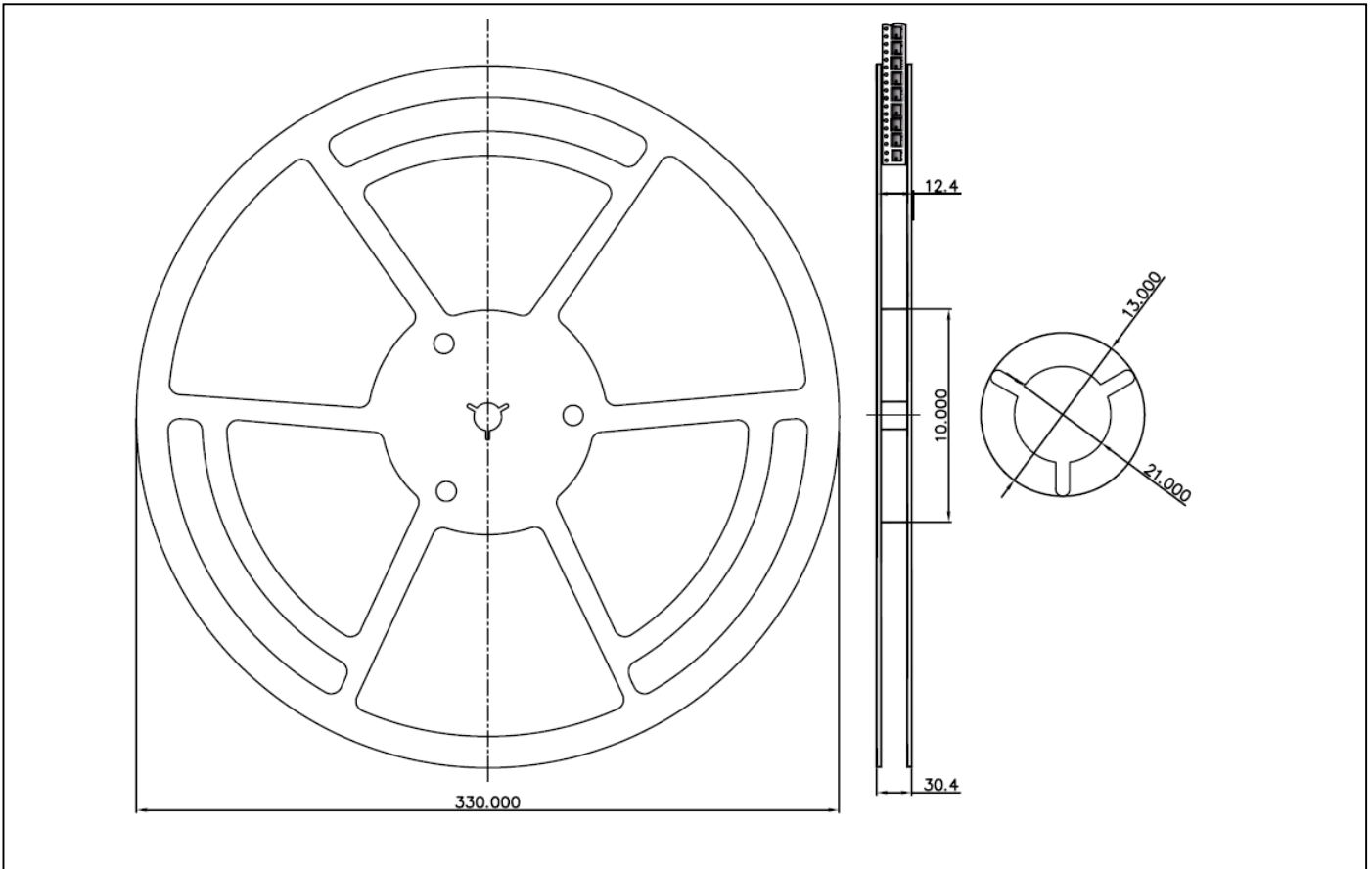
Single Pulse Maximum Power Dissipation



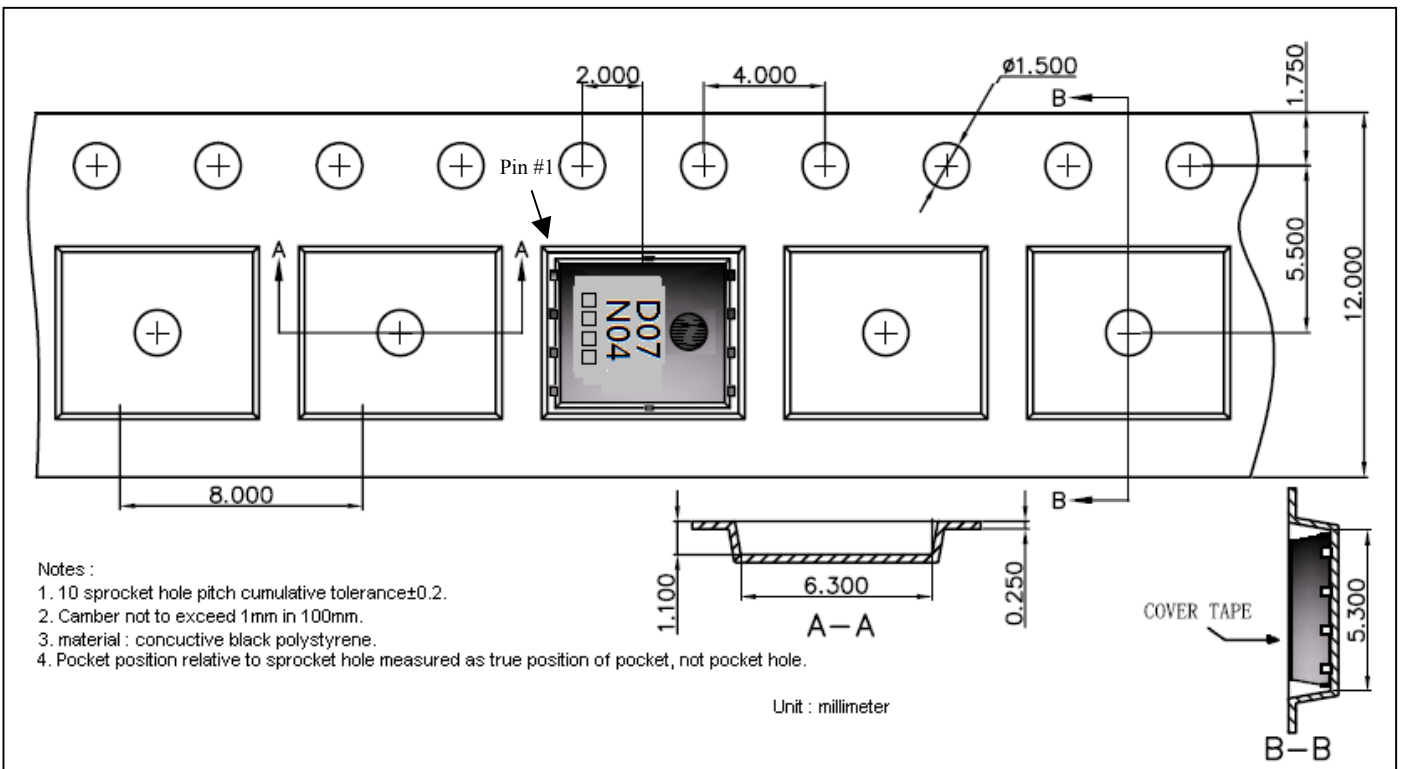
Transient Thermal Response Curves



Reel Dimension



Carrier Tape Dimension



Notes :

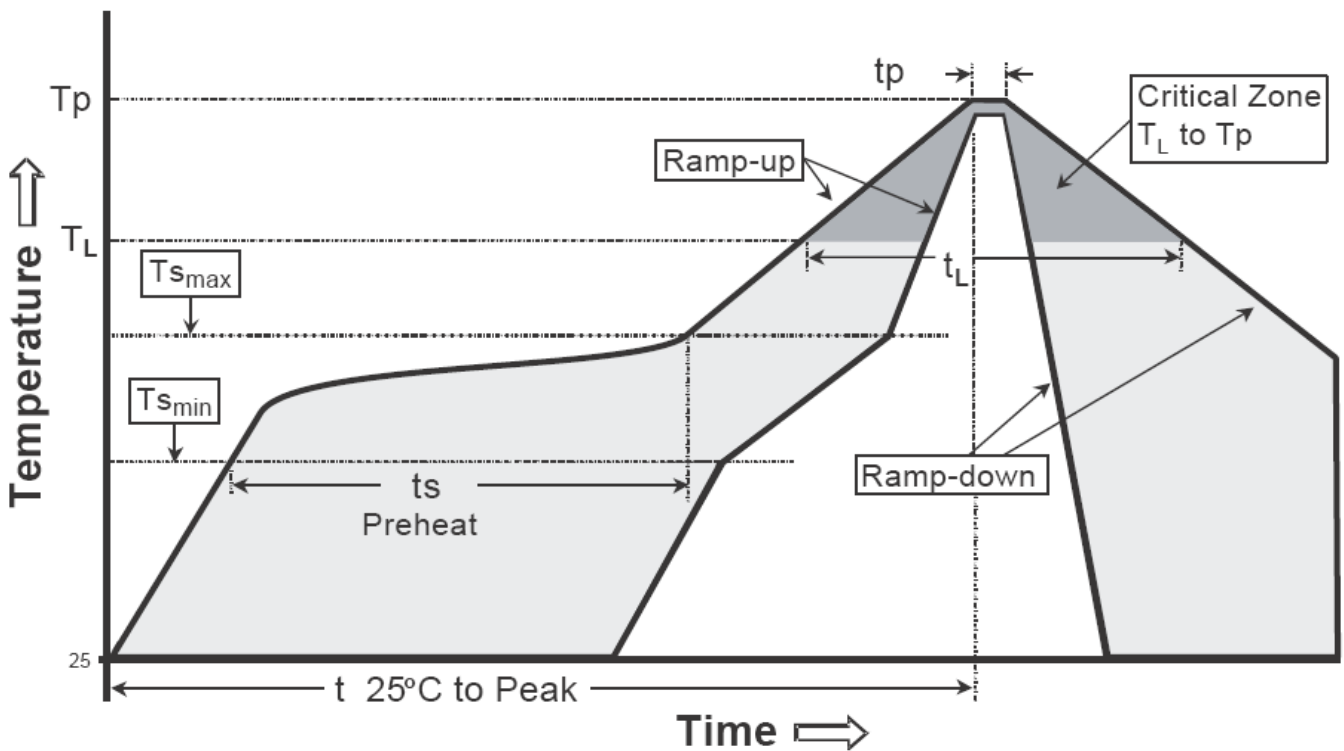
1. 10 sprocket hole pitch cumulative tolerance±0.2.
2. Camber not to exceed 1mm in 100mm.
3. material : conductive black polystyrene.
4. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Unit : millimeter

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

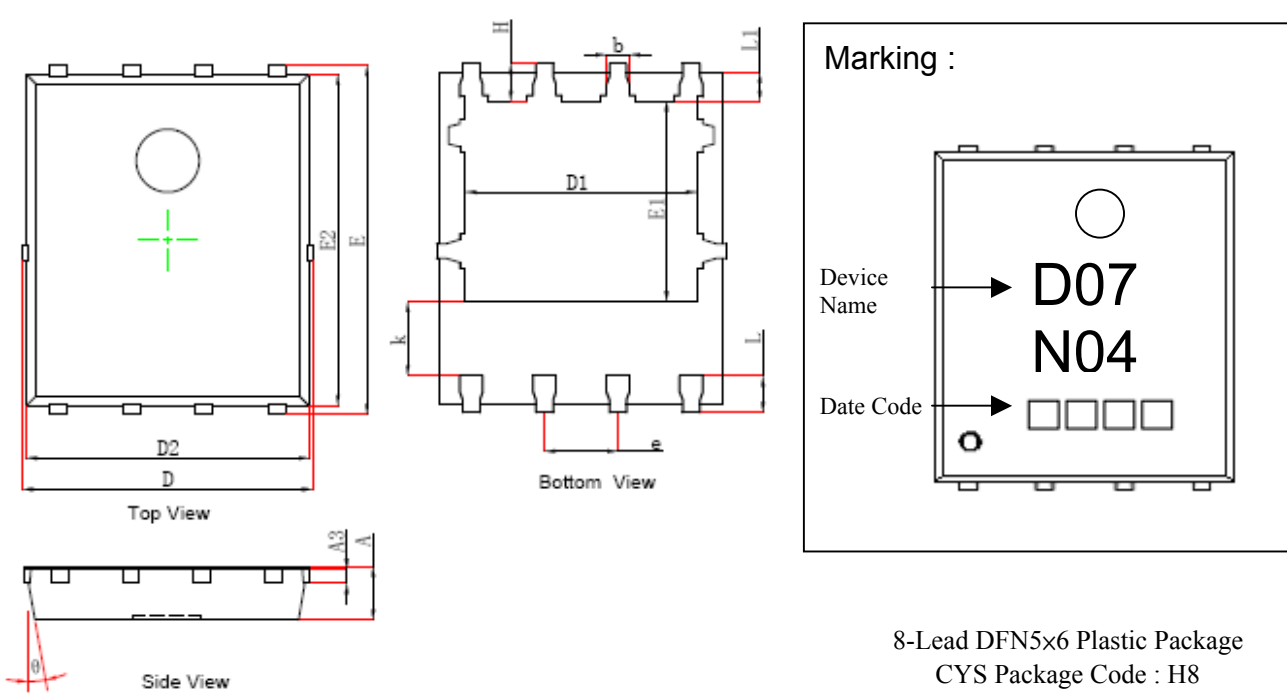
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN5x6 Dimension



Marking :

Device Name → **D07**

Date Code → **N04**

8-Lead DFN5x6 Plastic Package
 CYS Package Code : H8

Date Code(counting from left to right) :

1st code: year code, the last digit of Christian year

2nd code : month code, Jan→A, Feb→B, Mar→C, Apr→D, May→E, Jun→F, Jul→G, Aug→H, Sep→J, Oct→K, Nov→L, Dec→M

3rd and 4th codes : production serial number, 01~99

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039	k	1.190	1.390	0.047	0.055
A3	0.254	REF	0.010	REF	b	0.350	0.450	0.014	0.018
D	4.944	5.096	0.195	0.201	e	1.270	TYP.	0.050	TYP.
E	5.974	6.126	0.235	0.241	L	0.559	0.711	0.022	0.028
D1	3.910	4.110	0.154	0.162	L1	0.424	0.576	0.017	0.023
E1	3.375	3.575	0.133	0.141	H	0.574	0.726	0.023	0.029
D2	4.824	4.976	0.190	0.196	θ	10°	12°	10°	12°
E2	5.674	5.826	0.223	0.229					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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