

# N- AND P-Channel Logic Level Enhancement Mode MOSFET

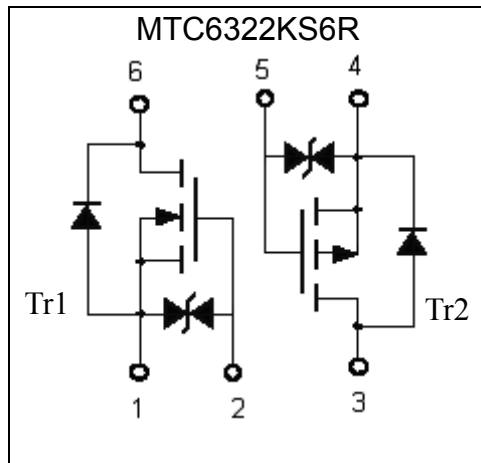
## MTC6322KS6R

### Features

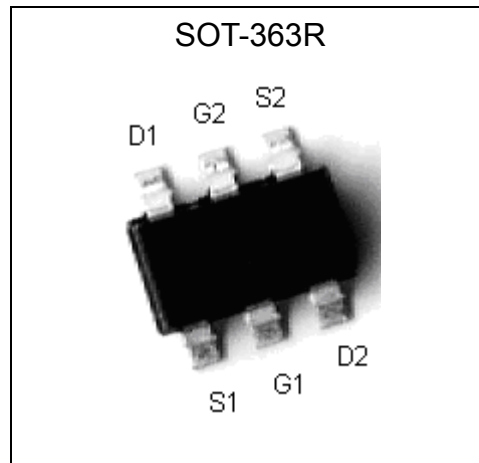
- Low on-resistance
- ESD protected
- High speed switching
- Low-voltage drive
- Pb-free package

	N-CH	P-CH
$BV_{DSS}$	30V	-30V
$I_D@V_{GS}=-4.5V, T_A=25^\circ C$	0.45A	-0.45A
$R_{DSON}(typ.) @V_{GS}=(-)4.5V$	0.86Ω	0.98Ω
$R_{DSON}(typ.) @V_{GS}=(-)2.7V$	1.2Ω	1.44Ω

### Equivalent Circuit

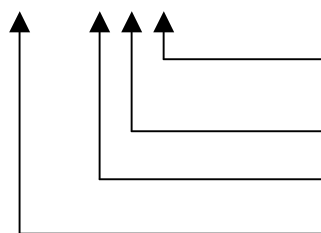


### Outline



### Ordering Information

Device	Package	Shipping
MTC6322S6R-0-T1-G	SOT-363 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel



Environment friendly grade : S for RoHS compliant products, G for RoHS compliant and green compound products

Packing spec, T1 : 3000 pcs / tape & reel, 7" reel

Product rank, zero for no rank products

Product name



**The following characteristics apply to both Tr1 and Tr2**

**Absolute Maximum Ratings** (TA=25°C, unless otherwise noted)

Parameter	Symbol	Limits		Unit
		N-channel	P-channel	
Drain-Source Breakdown Voltage	BVDSS	30	-30	V
Gate-Source Voltage	VGS	±8	±8	V
Continuous Drain Current @TA=25 °C, VGS=4.5V(-4.5V)	ID	0.45	-0.45	A
Continuous Drain Current @TA=70 °C, VGS=4.5V(-4.5V)	ID	0.36	-0.36	A
Pulsed Drain Current (Note 1)	IDM	1.8	-1.8	A
Power Dissipation @TA=25°C	PD	0.30		W
Power Dissipation @TA=70°C		0.18		
Operating Junction and Storage Temperature Range	Tj; Tstg	-55~+150		°C

Note : 1. Pulse width limited by maximum junction temperature.  
 2. Pulse width ≤ 300µs, duty cycle ≤ 2%.  
 3. Surface mounted on minimum pad of FR-4 board, t ≤ 5s.

**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient(PCB mounted) (Note)	Rth,ja	415	°C/W

Note : Surface mounted on minimum pad of FR-4 board, t ≤ 5s.

**N-Channel Electrical Characteristics (Tj=25°C, unless otherwise noted)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BVDSS	30	-	-	V	VGS=0, ID=250µA
VGS(th)	0.5	0.8	1.2		VDS=VGS, ID=250µA
IGSS	-	-	±5	µA	VGS=±8V, VDS=0
IDSS	-	-	1		VDS=30V, VGS=0
	-	-	10		VDS=24V, VGS=0 (Tj=70°C)
*RDS(ON)	-	0.86	1.2	Ω	VGS=4.5V, ID=450mA
	-	1.2	1.6		VGS=2.7V, ID=300mA
*GFS	-	0.6	-	S	VDS=5V, ID=450mA
<b>Dynamic</b>					
Ciss	-	33.5	-	pF	VDS=15V, VGS=0, f=1MHz
Coss	-	6.1	-		
Crss	-	2.5	-		
td(ON)	-	3	-	ns	VDS=15V, ID=450mA, VGS=4.5V, RG=50Ω
tr	-	5	-		
td(OFF)	-	9	-		
tf	-	5	-		



Qg	-	0.51	-	nC	V <sub>DS</sub> =15V, I <sub>D</sub> =450mA, V <sub>GS</sub> =4.5V
Qgs	-	0.05	-		
Qgd	-	0.18	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	0.45	A	
*I <sub>SM</sub>	-	-	1.8		
*V <sub>SD</sub>	-	0.9	1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =450mA

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

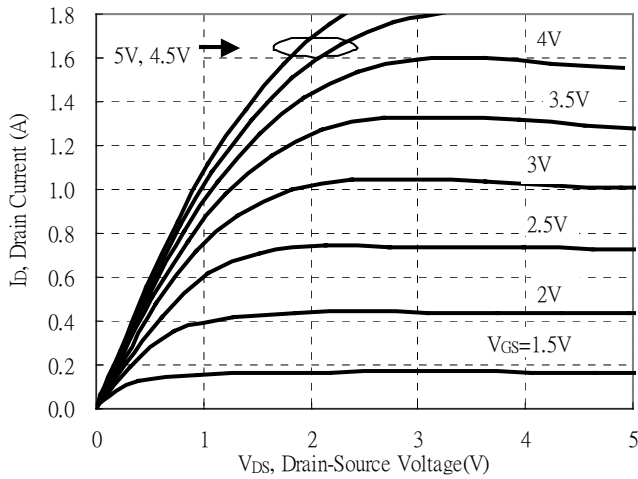
**P-Channel Electrical Characteristics (T<sub>j</sub>=25°C, unless otherwise noted)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-0.5	-0.9	-1.2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
I <sub>GSS</sub>	-	-	±5	μA	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	-1		V <sub>DS</sub> =-30V, V <sub>GS</sub> =0
	-	-	-10		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0 (T <sub>j</sub> =70°C)
*R <sub>DS(ON)</sub>	-	0.98	1.3	Ω	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-450mA
	-	1.44	1.9		V <sub>GS</sub> =-2.7V, I <sub>D</sub> =-300mA
*G <sub>FS</sub>	-	0.6	-	S	V <sub>DS</sub> =-5V, I <sub>D</sub> =-450mA
<b>Dynamic</b>					
C <sub>iss</sub>	-	55.6	-	pF	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	9.3	-		
C <sub>rss</sub>	-	5.7	-		
t <sub>d(ON)</sub>	-	5	-	ns	V <sub>DS</sub> =-15V, I <sub>D</sub> =-450mA, V <sub>GS</sub> =-4.5V, R <sub>G</sub> =50Ω
t <sub>r</sub>	-	6	-		
t <sub>d(OFF)</sub>	-	15	-		
t <sub>f</sub>	-	11	-		
Qg	-	0.75	-	nC	V <sub>DS</sub> =-15V, I <sub>D</sub> =-450mA, V <sub>GS</sub> =-4.5V
Qgs	-	0.09	-		
Qgd	-	0.25	-		
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	-0.45	A	
*I <sub>SM</sub>	-	-	-1.8		
*V <sub>SD</sub>	-	-0.89	-1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =-450mA

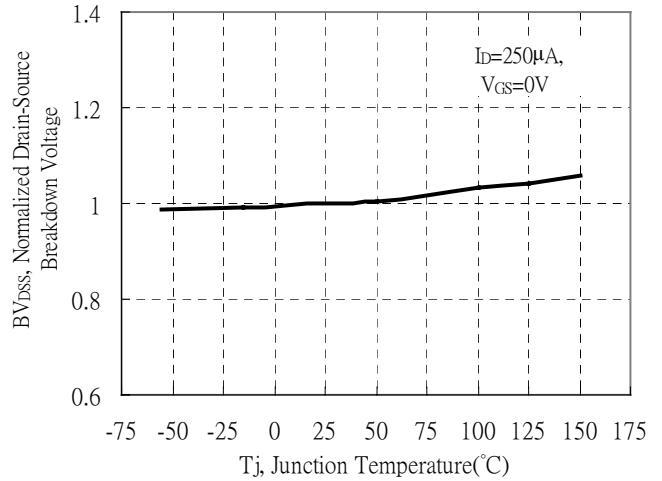


## N-Channel Typical Characteristics

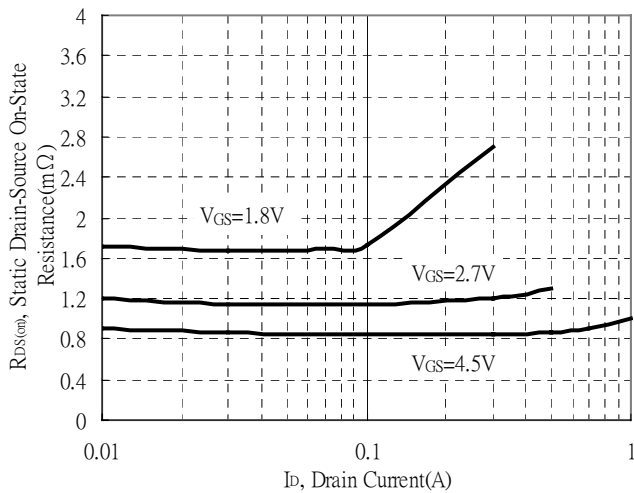
Typical Output Characteristics



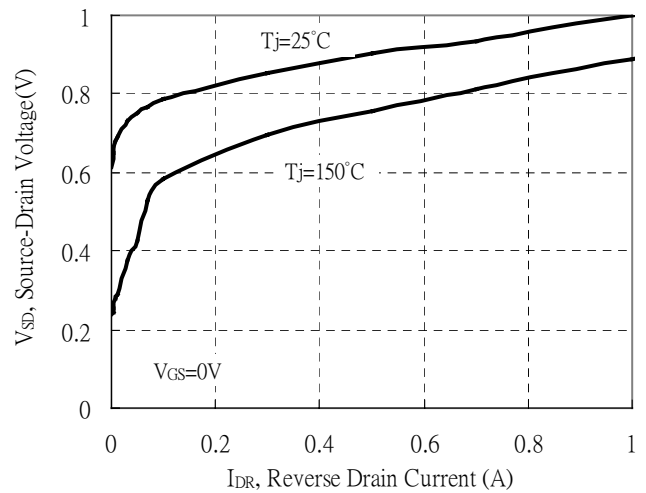
Breakdown Voltage vs Ambient Temperature



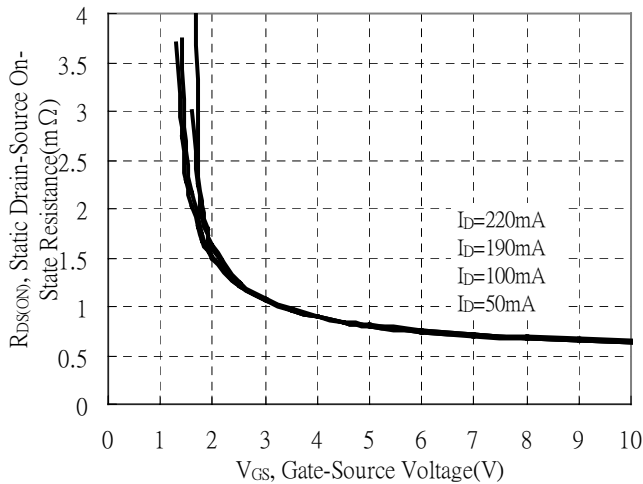
Static Drain-Source On-State resistance vs Drain Current



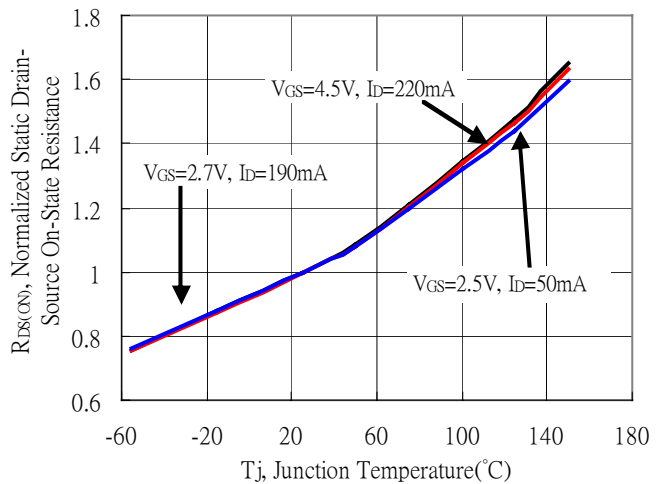
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



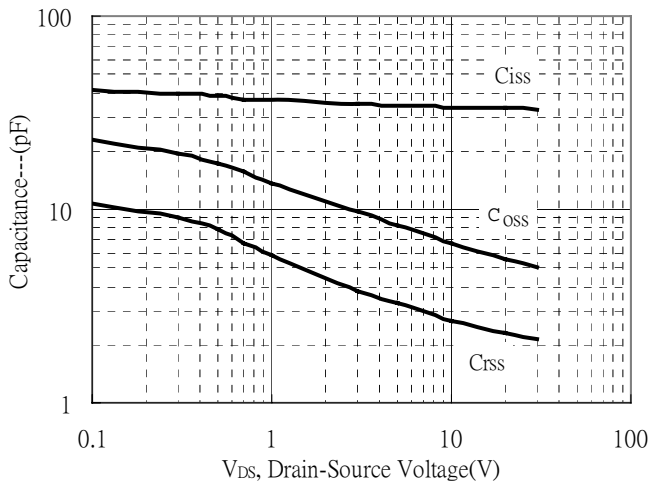
Drain-Source On-State Resistance vs Junction Temperature



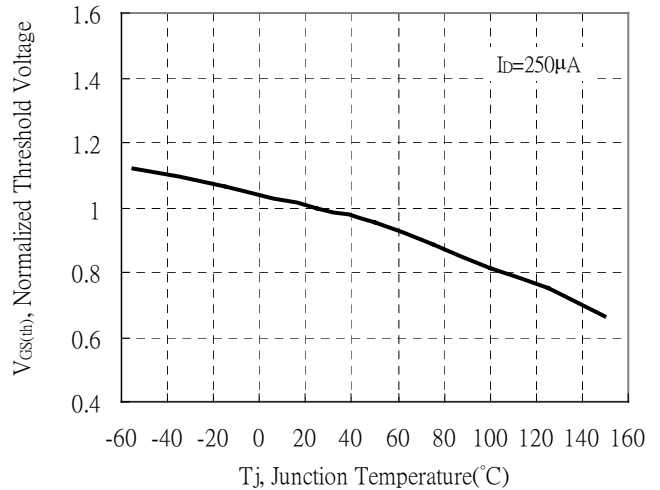


**N-Channel Typical Characteristics(Cont.)**

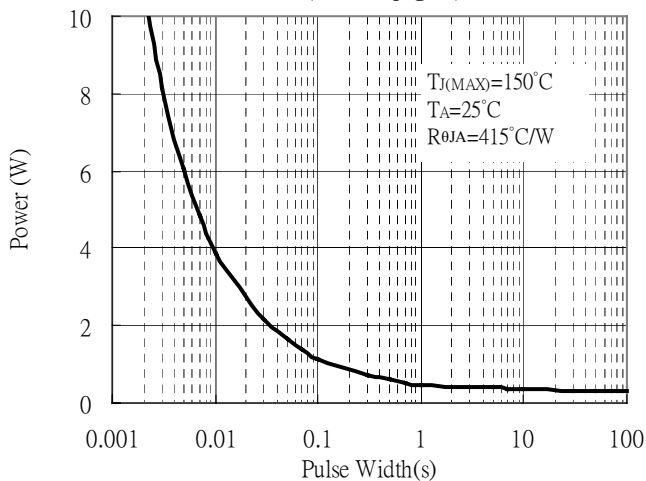
Capacitance vs Drain-to-Source Voltage



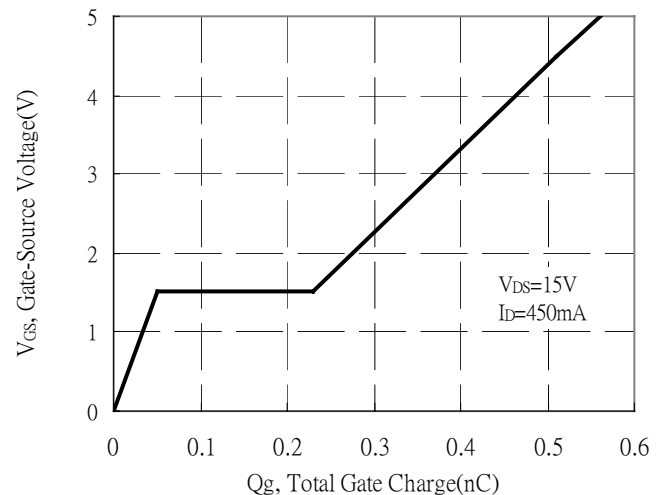
Threshold Voltage vs Junction Temperature



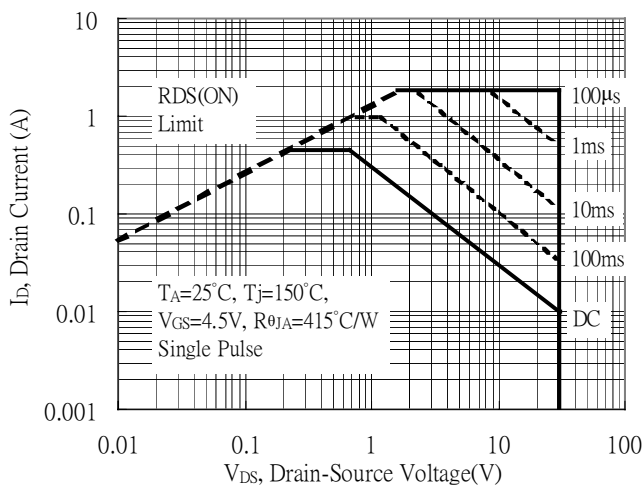
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



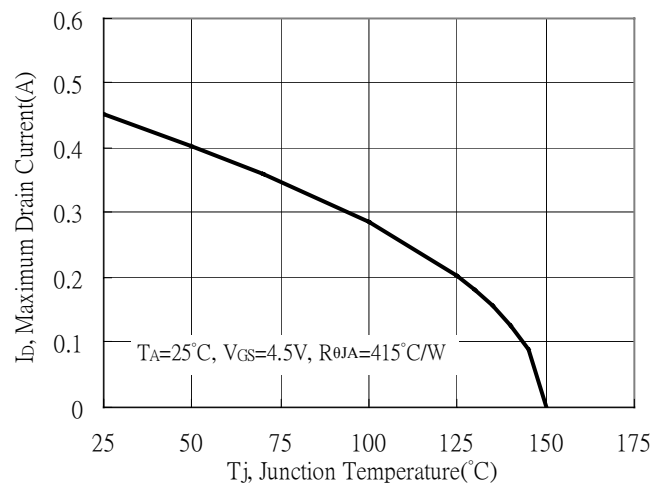
Gate Charge Characteristics



Maximum Safe Operating Area



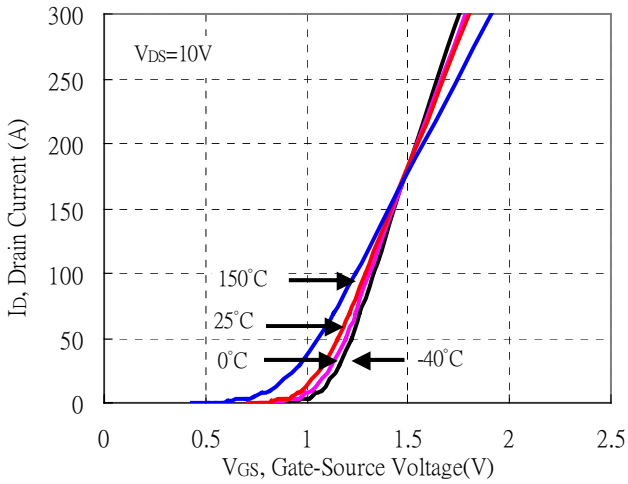
Maximum Drain Current vs Junction Temperature



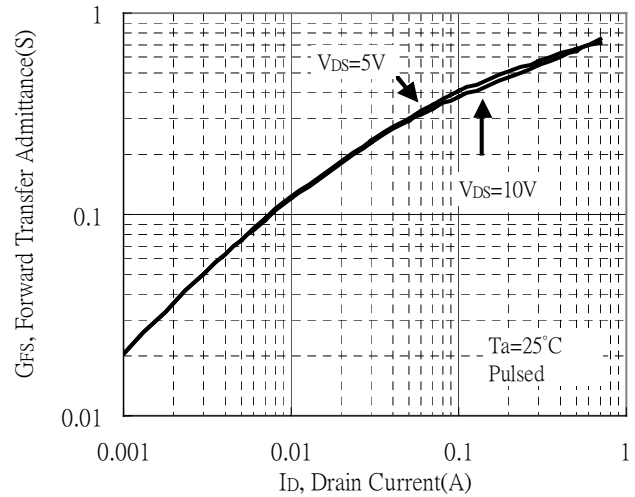


**N-Channel Typical Characteristics(Cont.)**

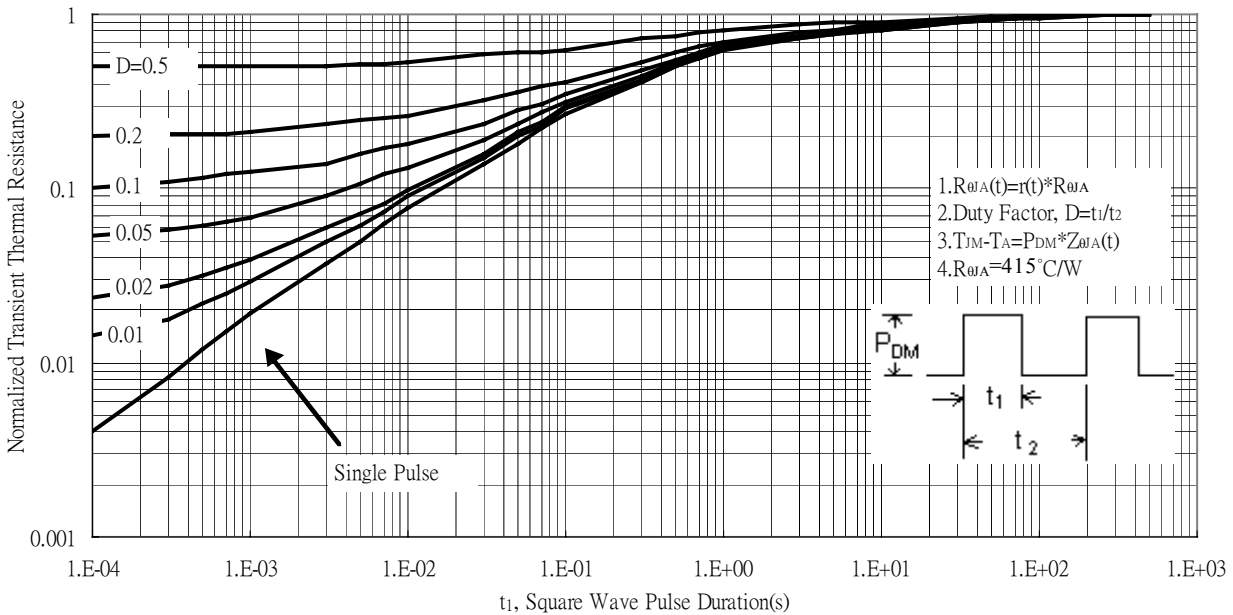
Typical Transfer Characteristics



Forward Transfer Admittance vs Drain Current

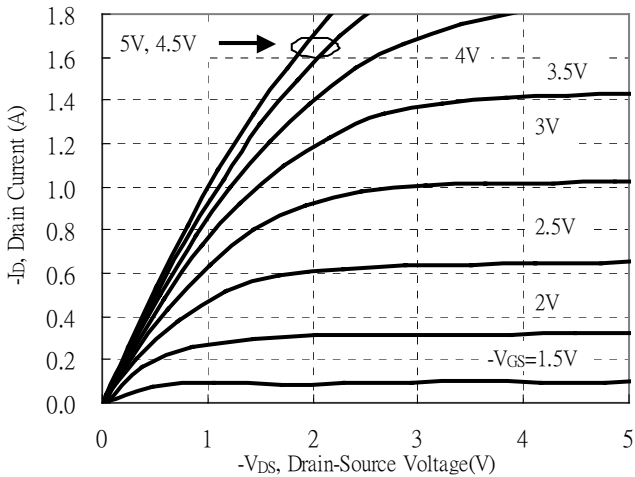


Transient Thermal Response Curves

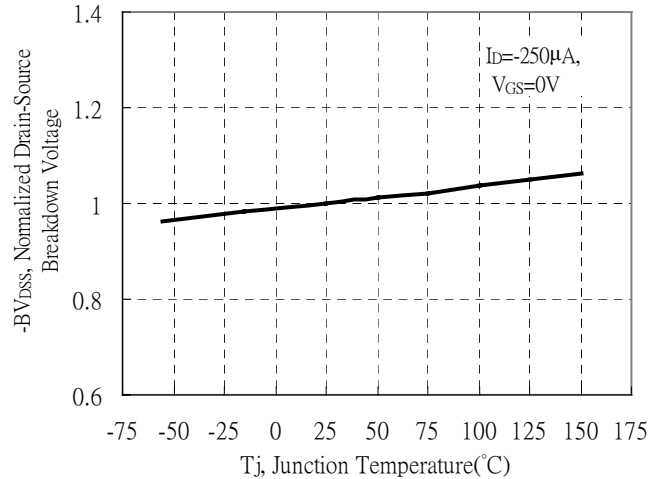


## P-Channel Typical Characteristics

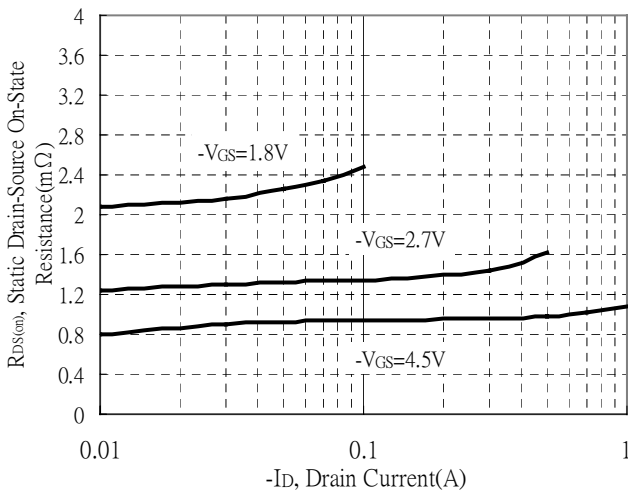
Typical Output Characteristics



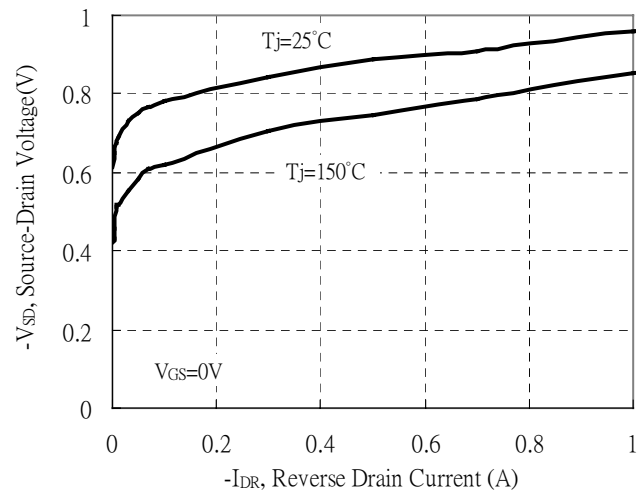
Breakdown Voltage vs Ambient Temperature



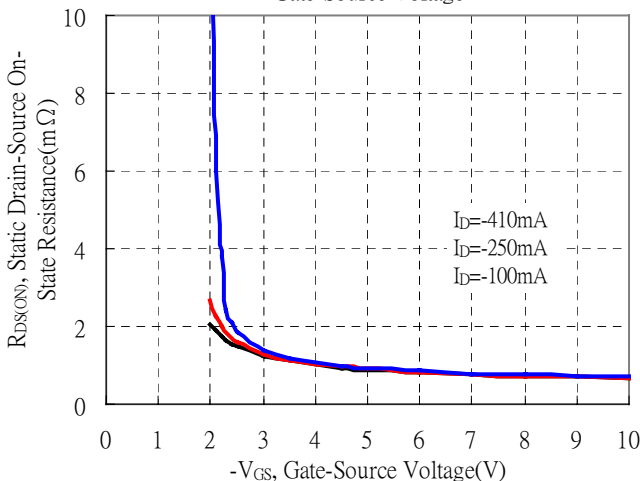
Static Drain-Source On-State resistance vs Drain Current



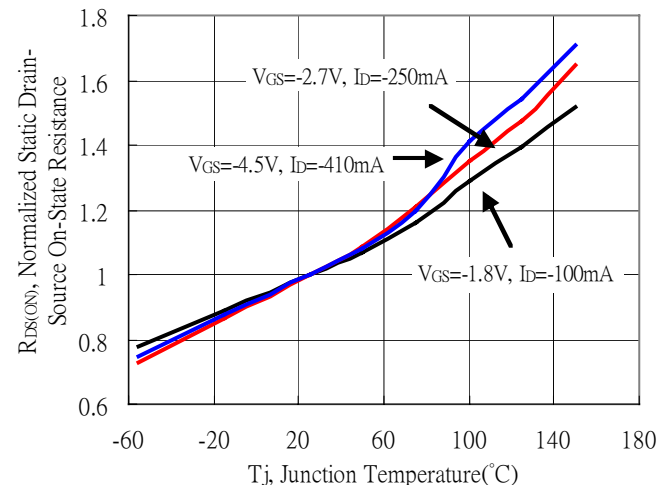
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage



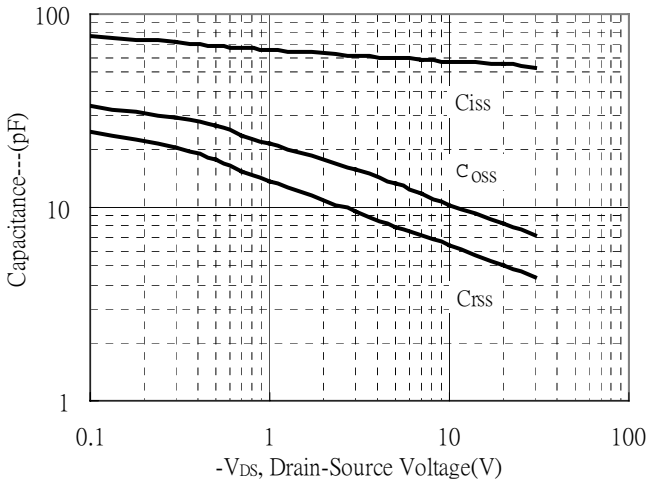
Drain-Source On-State Resistance vs Junction Temperature



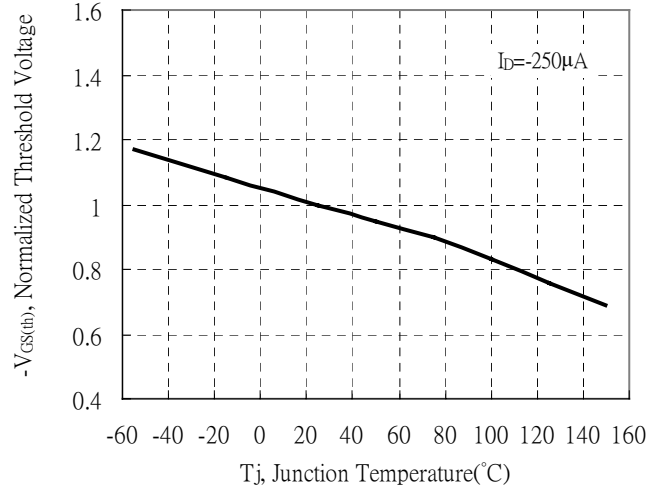


**P-Channel Typical Characteristics(Cont.)**

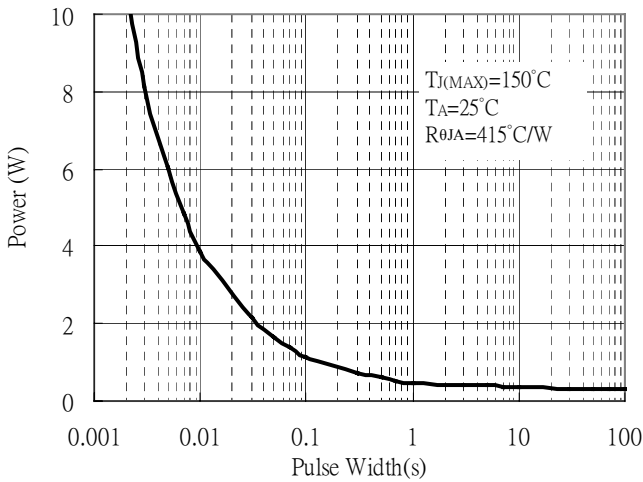
Capacitance vs Drain-to-Source Voltage



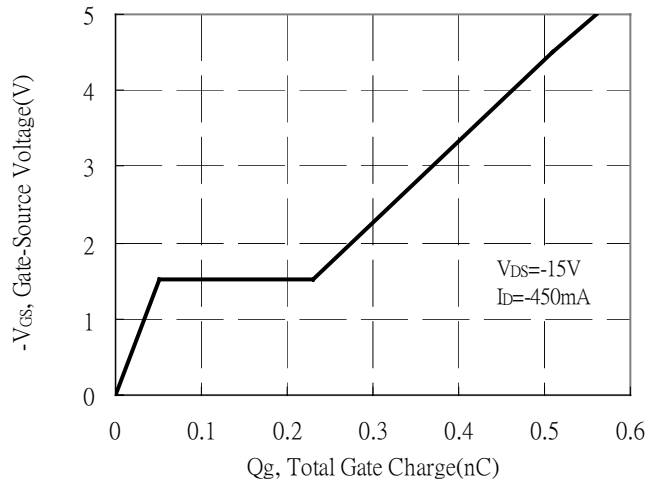
Threshold Voltage vs Junction Temperature



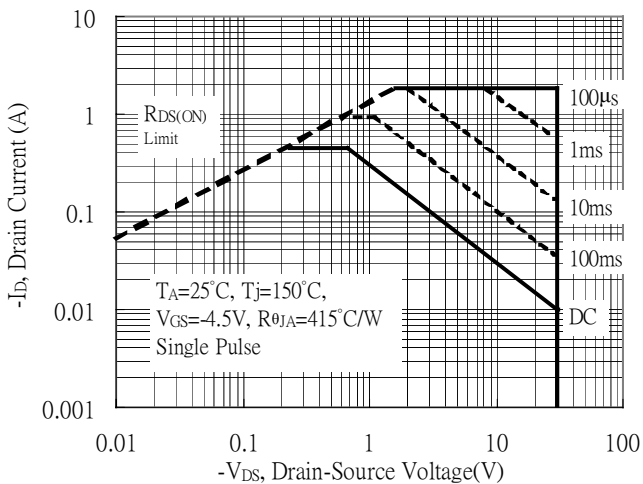
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



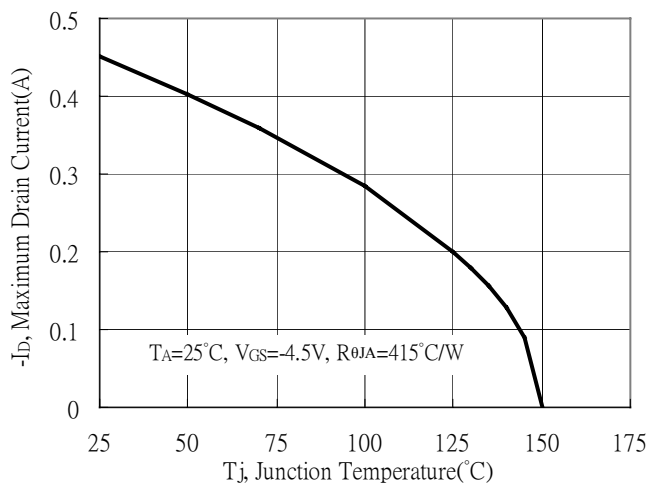
Gate Charge Characteristics



Maximum Safe Operating Area



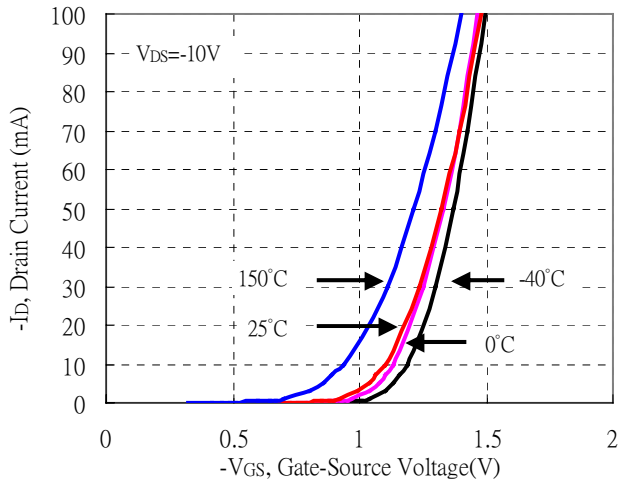
Maximum Drain Current vs Junction Temperature



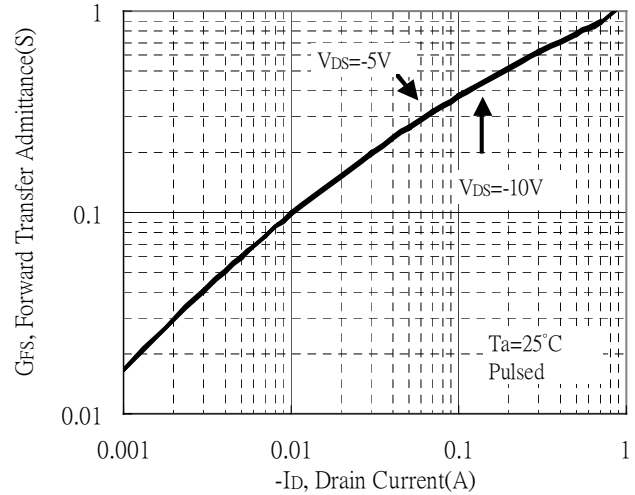


## P-Channel Typical Characteristics(Cont.)

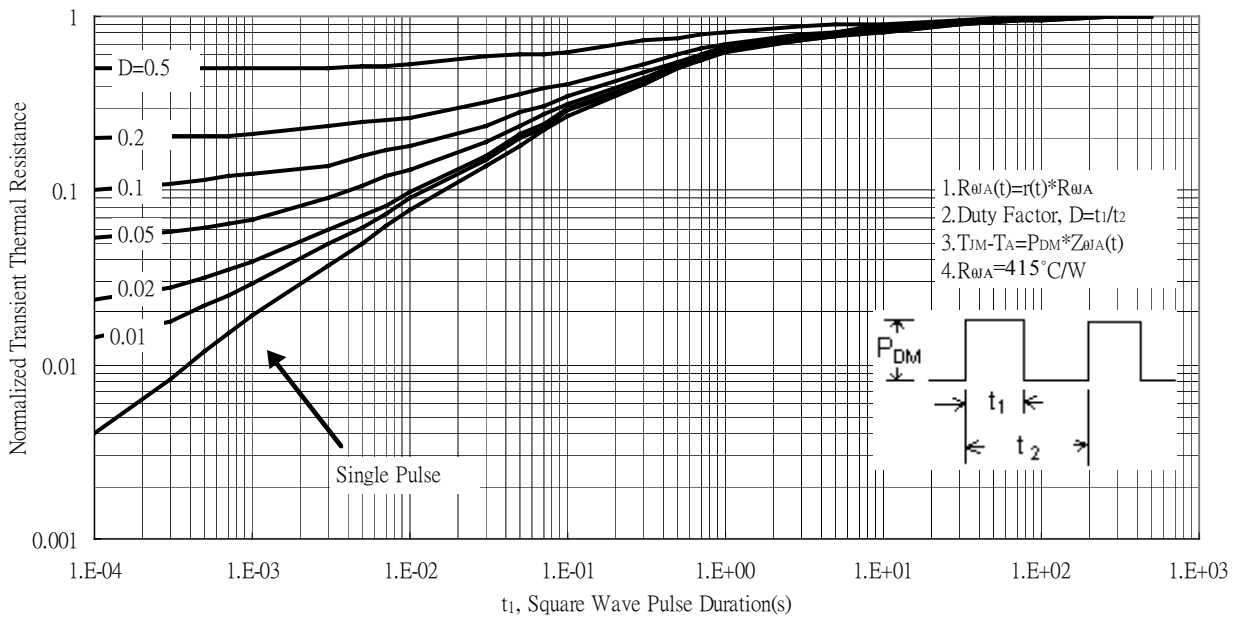
Typical Transfer Characteristics



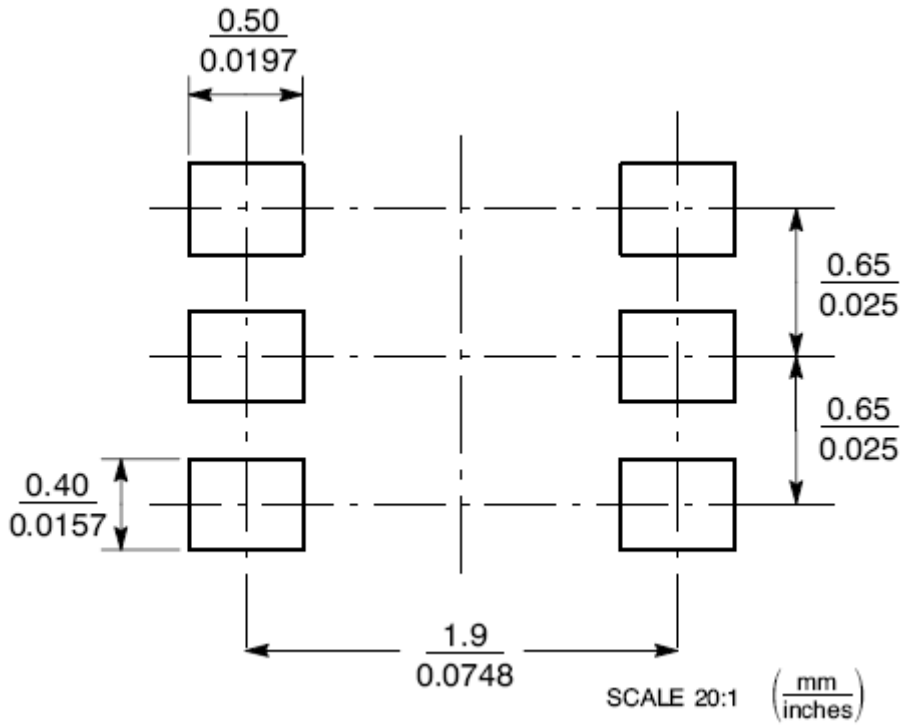
Forward Transfer Admittance vs Drain Current



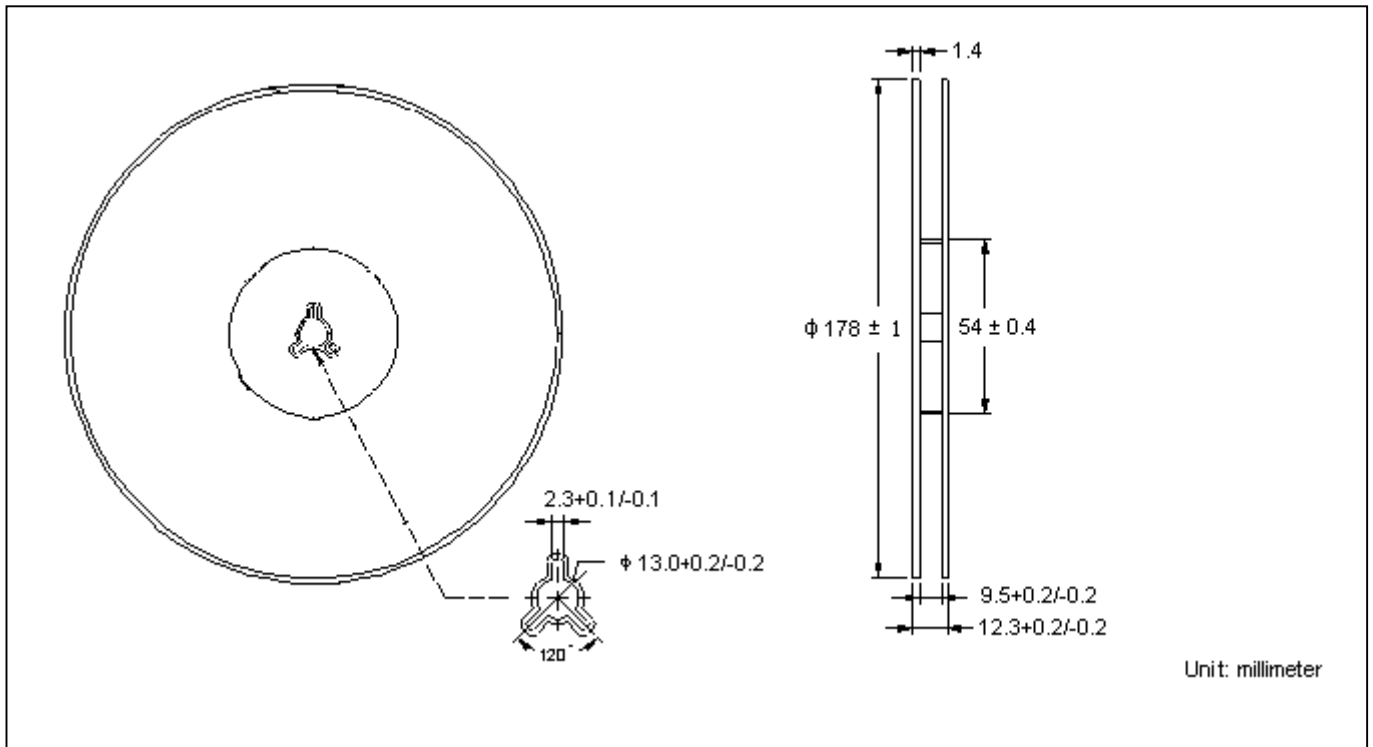
Transient Thermal Response Curves



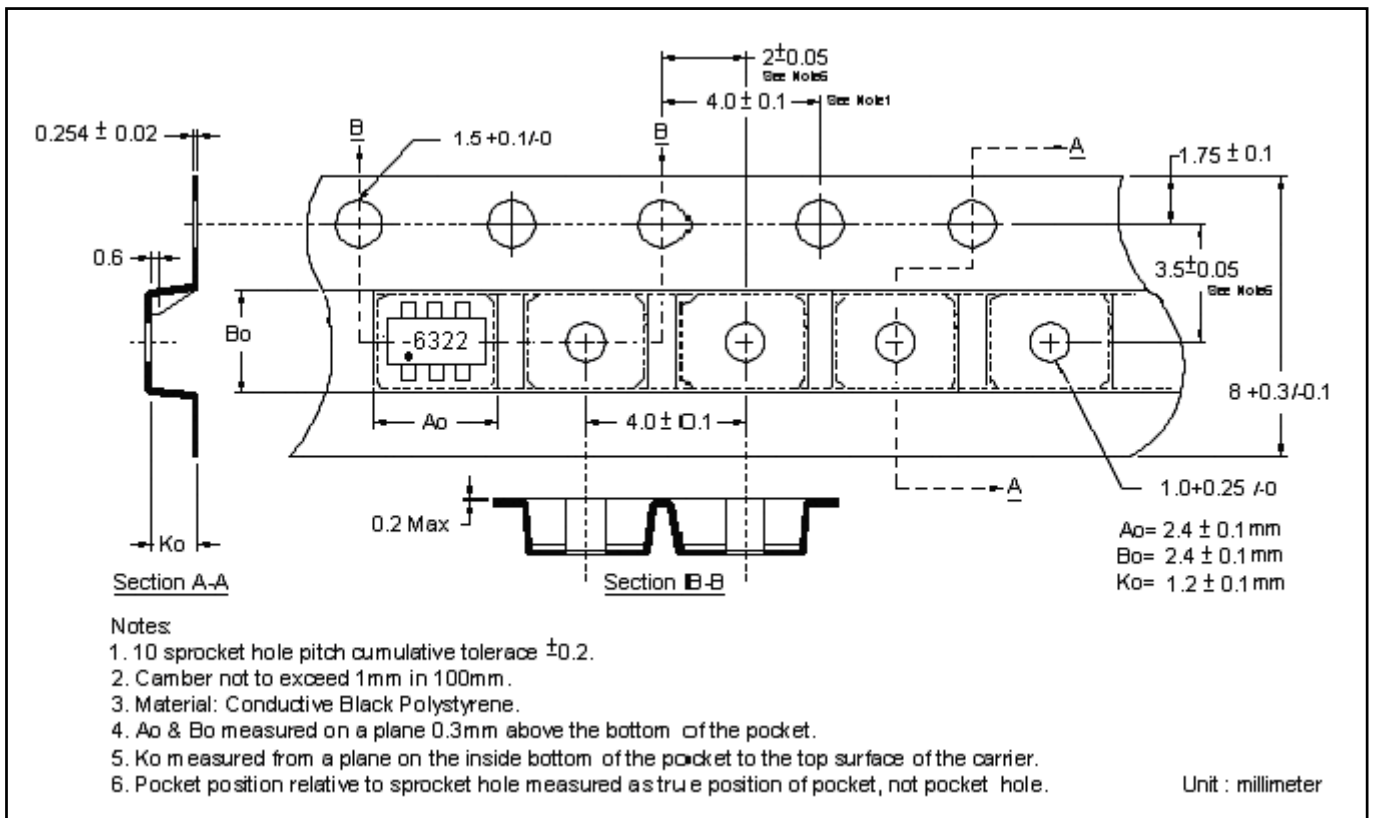
### Recommended Soldering Footprint



**Reel Dimension**



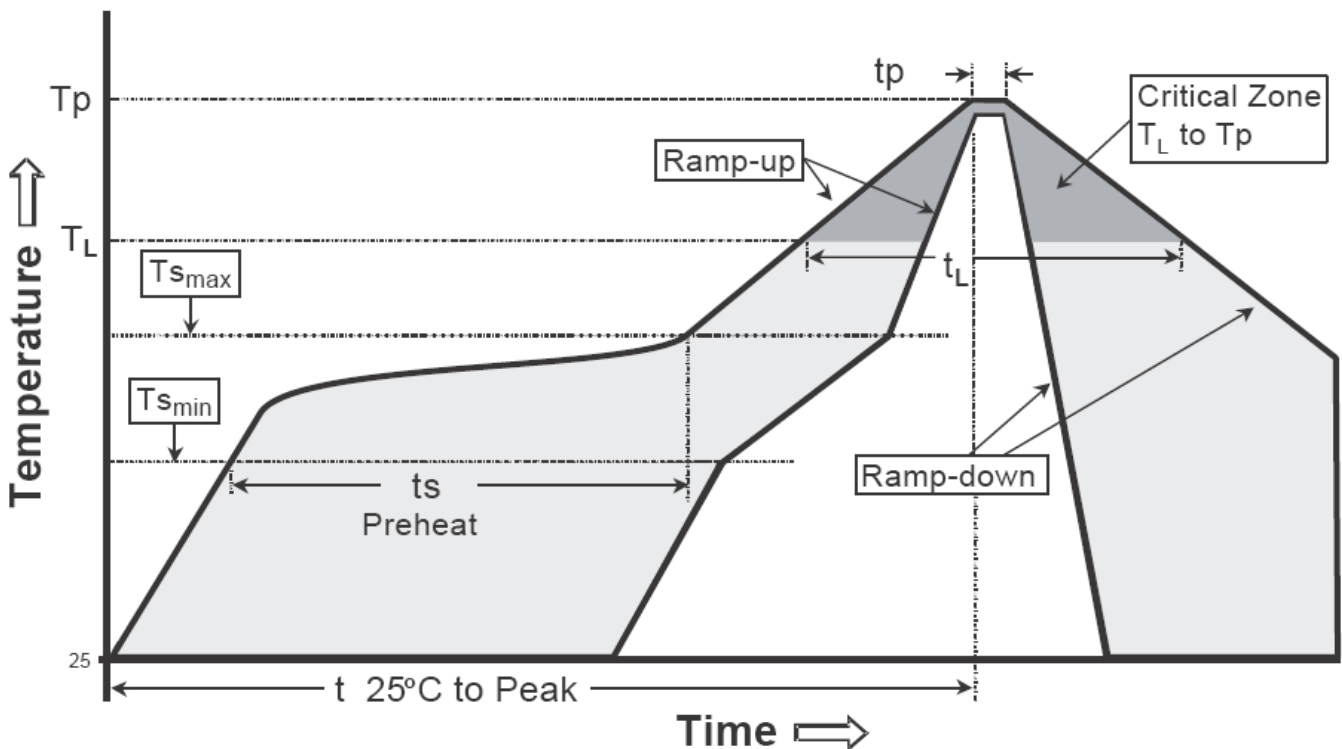
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

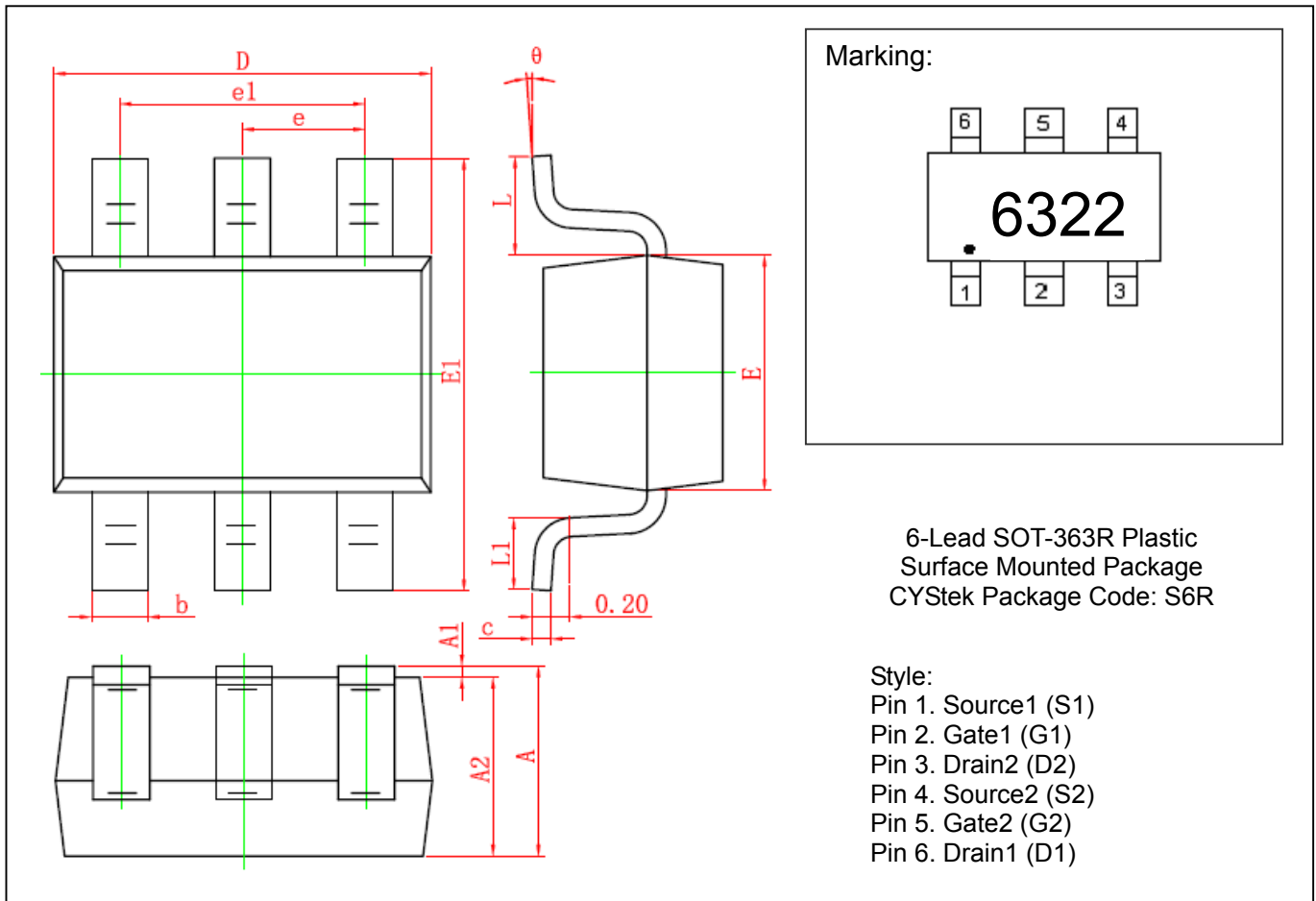
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**SOT-363 Dimension**



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043	E1	2.150	2.450	0.085	0.096
A1	0.000	0.100	0.000	0.004	e	0.650 TYP		0.026 TYP	
A2	0.900	1.000	0.035	0.039	e1	1.200	1.400	0.047	0.055
b	0.150	0.350	0.006	0.014	L	0.525 REF		0.021 REF	
c	0.080	0.150	0.003	0.006	L1	0.260	0.460	0.010	0.018
D	2.000	2.200	0.079	0.087	θ	0°	8°	0°	8°
E	1.150	1.350	0.045	0.053					

**Notes :** 1.Controlling dimension : millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

**Important Notice:**

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.