

**N- AND P-Channel Enhancement Mode MOSFET**

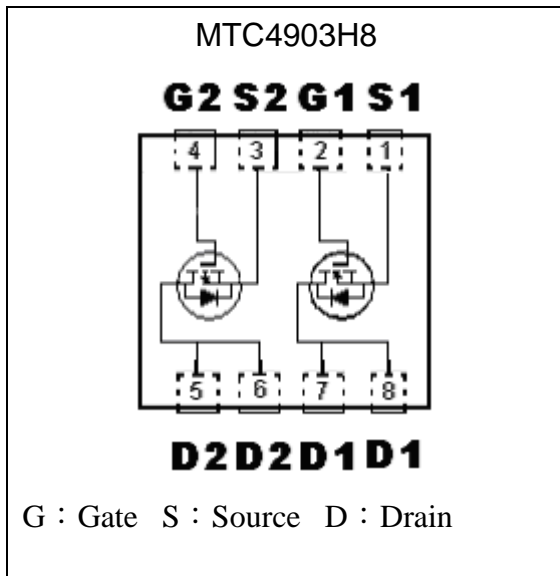
# MTC4903H8

	N-CH	P-CH
BV <sub>DSS</sub>	30V	-30V
I <sub>D</sub> @V <sub>GS</sub> =10V(-10V), T <sub>A</sub> =25°C	13.2A	-10.2A
I <sub>D</sub> @V <sub>GS</sub> =10V(-10V), T <sub>C</sub> =25°C	41.7A	-32.2A
R <sub>DS(on)(typ)</sub> @V <sub>GS</sub> =10V(-10V)	6.5mΩ	10.8mΩ
R <sub>DS(on)(typ)</sub> @V <sub>GS</sub> =4.5V(-4.5V)	8.0mΩ	21.9mΩ

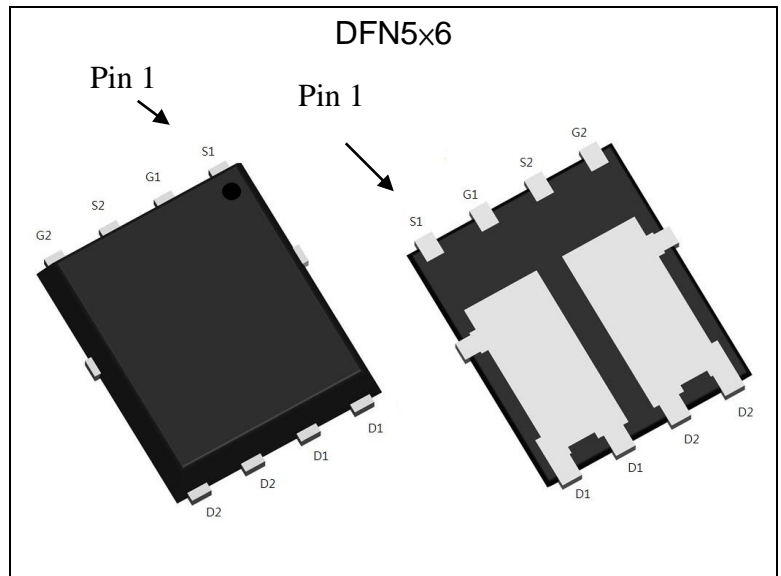
**Features**

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

**Equivalent Circuit**

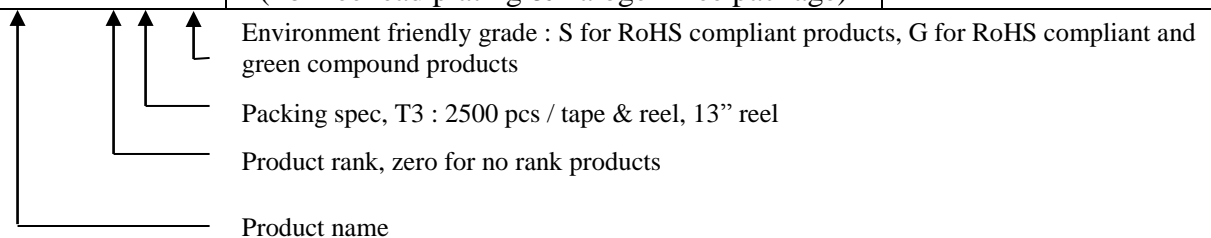


**Outline**



**Ordering Information**

Device	Package	Shipping
MTC4903H8-0-T6-G	DFN 5 x6 (Pb-free lead plating & halogen-free package)	3000 pcs / Tape & Reel





**Absolute Maximum Ratings** (T<sub>C</sub>=25°C, unless otherwise noted)

Parameter		Symbol	Limits		Unit
			N-channel	P-channel	
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	30	-30	V
Gate-Source Voltage		V <sub>GS</sub>	±20	±20	
Continuous Drain Current	T <sub>A</sub> =25 °C, V <sub>GS</sub> =10V (-10V)	I <sub>DSM</sub>	13.2	-10.2	A
	T <sub>A</sub> =70 °C, V <sub>GS</sub> =10V (-10V)		10.6	-8.2	
	T <sub>C</sub> =25 °C, V <sub>GS</sub> =10V (-10V)	I <sub>D</sub>	41.7	-32.2	
	T <sub>C</sub> =100 °C, V <sub>GS</sub> =10V (-10V)		26.4	-20.4	
Pulsed Drain Current (Note 1 & 2)		I <sub>DM</sub>	58	-45	
Single Pulse Avalanche Current @ L=0.1mH		I <sub>AS</sub>	40	45	
Single Pulse Avalanche Energy (Note 4)		E <sub>AS</sub>	72	200	mJ
Power Dissipation	T <sub>A</sub> =25 °C	P <sub>DSM</sub>	2.5 (Note 3)		W
	T <sub>A</sub> =70 °C		1.6 (Note 3)		
	T <sub>C</sub> =25 °C	P <sub>D</sub>	25		
	T <sub>C</sub> =100 °C		10		
Operating Junction and Storage Temperature Range		T <sub>j</sub> ; T <sub>stg</sub>	-55~+150		°C

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>θJC</sub>	5	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>θJA</sub>	50 (Note 3)	

- Note : 1. Pulse width limited by maximum junction temperature  
 2. Duty cycle ≤ 1%  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board, t ≤ 10s; 125°C/W when mounted on minimum copper pad.  
 4. For N-channel, L=1mH, V<sub>DD</sub>=15V, V<sub>GS</sub>=10V, I<sub>AS</sub>=12A; for P-channel, L=1mH, V<sub>DD</sub>=-15V, V<sub>GS</sub>=-10V, I<sub>AS</sub>=-20A

**N-Channel Electrical Characteristics** (T<sub>C</sub>=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1.0	-	2.5		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V
	-	-	25		V <sub>DS</sub> =24V, V <sub>GS</sub> =0, T <sub>j</sub> =125°C
*R <sub>DSON</sub>	-	6.5	9.3	mΩ	I <sub>D</sub> =6A, V <sub>GS</sub> =10V
	-	8.0	12		I <sub>D</sub> =4A, V <sub>GS</sub> =4.5V
*G <sub>FS</sub>	-	14	-	S	V <sub>DS</sub> =10V, I <sub>D</sub> =6A
<b>Dynamic</b>					
C <sub>iss</sub>	-	1496	2100	pF	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	226	-		
C <sub>rss</sub>	-	157	-		



*td(ON)	-	17.2	-	ns	V <sub>DS</sub> =15V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V, R <sub>G</sub> =1 Ω
*tr	-	87	-		
*td(OFF)	-	54.2	-		
*tf	-	9.4	-		
*Qg	-	18.1	25	nC	V <sub>DS</sub> =25V, I <sub>D</sub> =6A, V <sub>GS</sub> =4.5V
*Qgs	-	3.9	-		
*Qgd	-	9.4	-		
Rg	-	1.6	-	Ω	f=1MHz
<b>Body Diode</b>					
*V <sub>SD</sub>	-	0.79	1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =6A
*trr	-	19.2	-	ns	I <sub>F</sub> =6A, dI <sub>F</sub> /dt=100A/μs
*Qrr	-	11.4	-	nC	

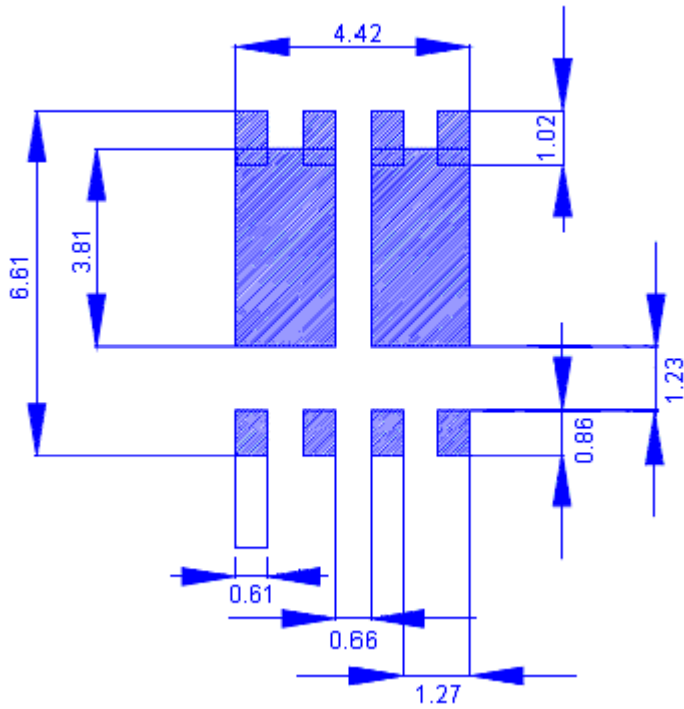
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

**P-Channel Electrical Characteristics (T<sub>c</sub>=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-30	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1.5	-	-3.0		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	-1	μA	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V
	-	-	-25		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>j</sub> =125°C
*R <sub>Ds(ON)</sub>	-	10.8	15.5	mΩ	I <sub>D</sub> =-6A, V <sub>GS</sub> =-10V
	-	21.9	30		I <sub>D</sub> =-4A, V <sub>GS</sub> =-4.5V
*G <sub>FS</sub>	-	16.5	-	S	V <sub>DS</sub> =-10V, I <sub>D</sub> =-6A
<b>Dynamic</b>					
C <sub>iss</sub>	-	2402	3123	pF	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz
C <sub>oss</sub>	-	300	-		
C <sub>rss</sub>	-	261	-		
*td(ON)	-	17	-	ns	V <sub>DS</sub> =-15V, I <sub>D</sub> =-6A, V <sub>GS</sub> =-10V, R <sub>G</sub> =1 Ω
*tr	-	22.2	-		
*td(OFF)	-	68.4	-		
*tf	-	14.2	-		
*Qg	-	27.2	36	nC	V <sub>DS</sub> =-25V, I <sub>D</sub> =-6A, V <sub>GS</sub> =-4.5V
*Qgs	-	8	-		
*Qgd	-	14.5	-		
Rg	-	3.5	-	Ω	f=1MHz
<b>Body Diode</b>					
*V <sub>SD</sub>	-	-0.81	-1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =-6A
*trr	-	16.3	-	ns	I <sub>F</sub> =-6A, dI <sub>F</sub> /dt=100A/μs
*Qrr	-	8.1	-	nC	

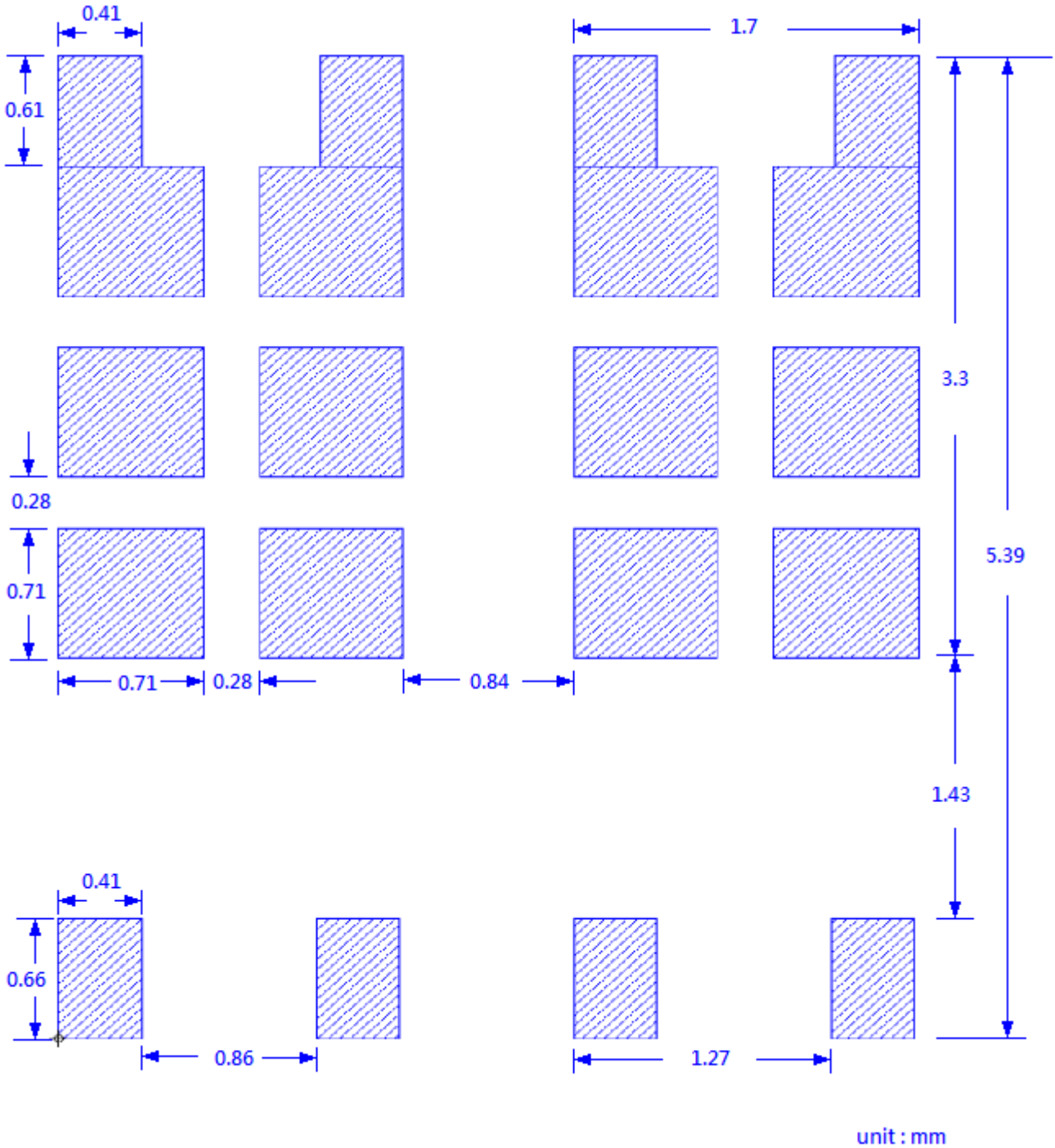
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

### Recommended Soldering Footprint



unit : mm

**Recommended Stencil Design**

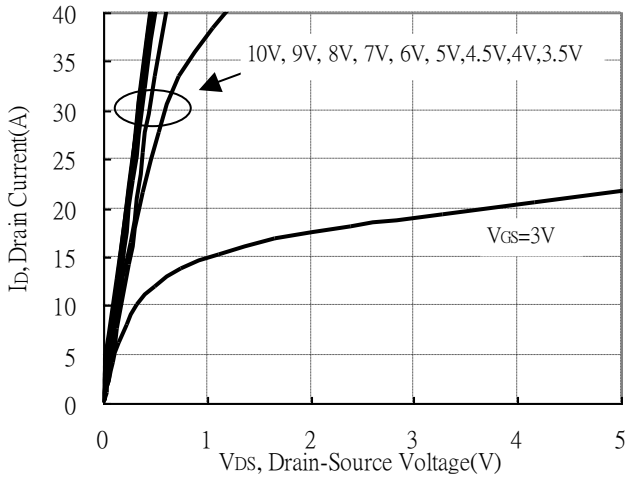


- Note :**
1. Stencil thickness 5 mil (0.127mm)
  2. May need to be adjusted to specific requirements.

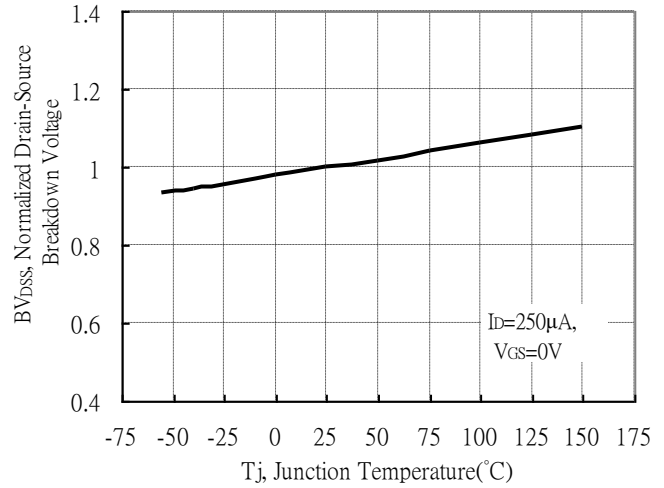


### Typical Characteristics : Q1( N-channel )

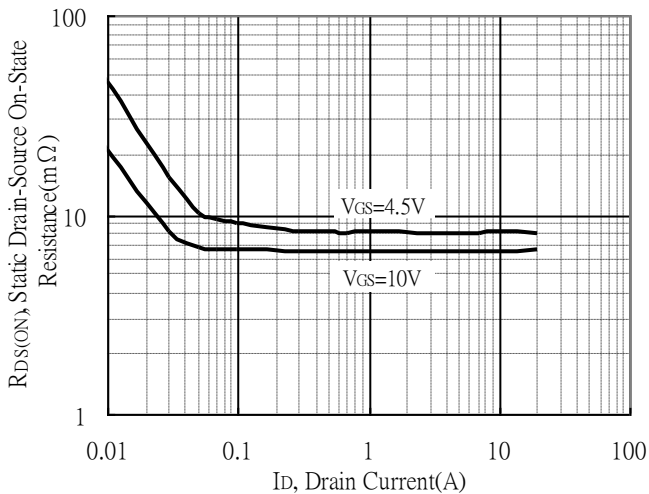
Typical Output Characteristics



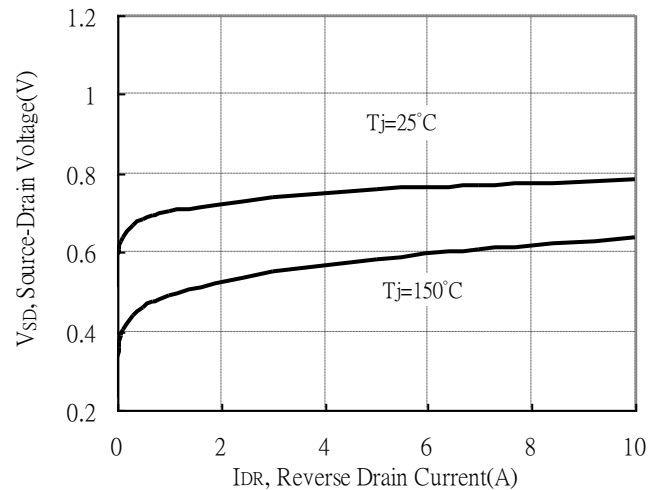
Brekdown Voltage vs Ambient Temperature



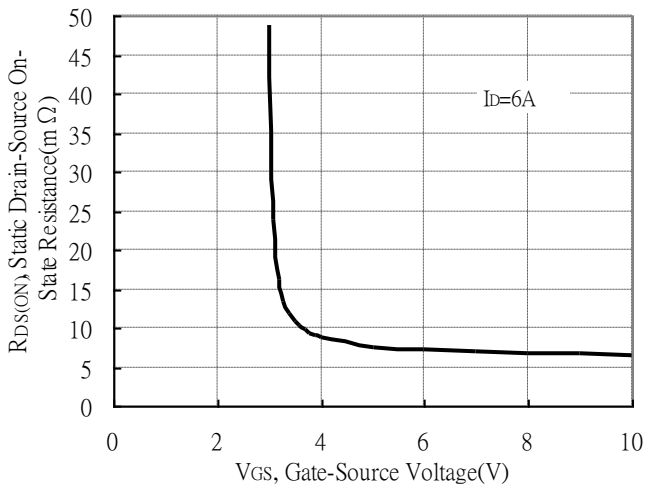
Static Drain-Source On-State resistance vs Drain Current



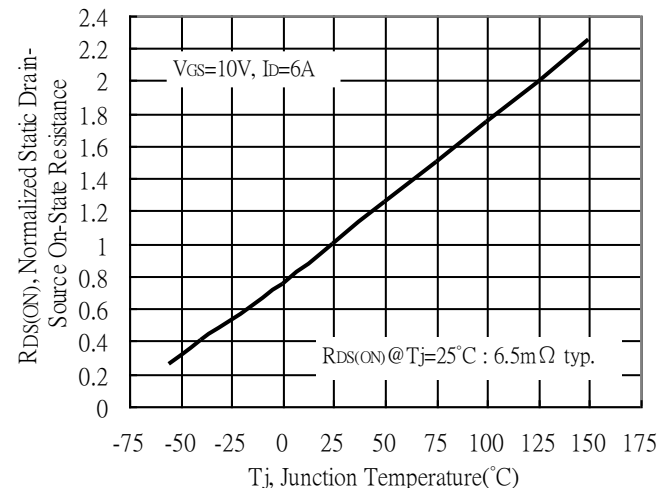
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

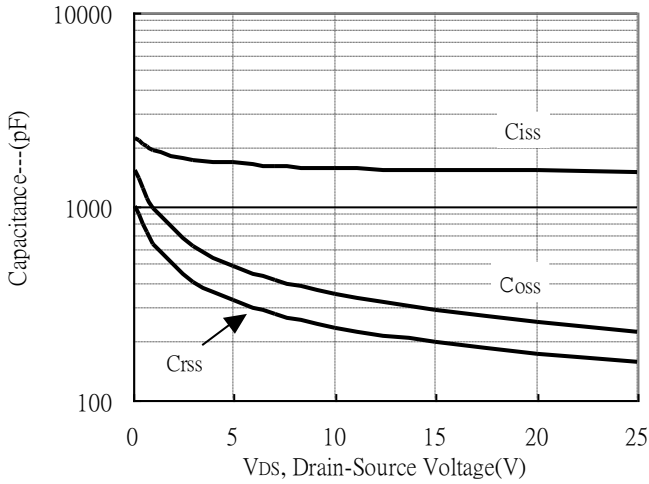


Drain-Source On-State Resistance vs Junction Temperature

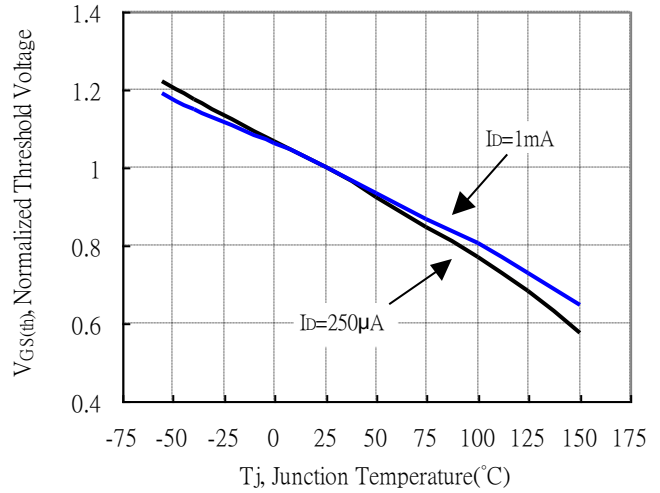


**Typical Characteristics(Cont.) : Q1( N-channel)**

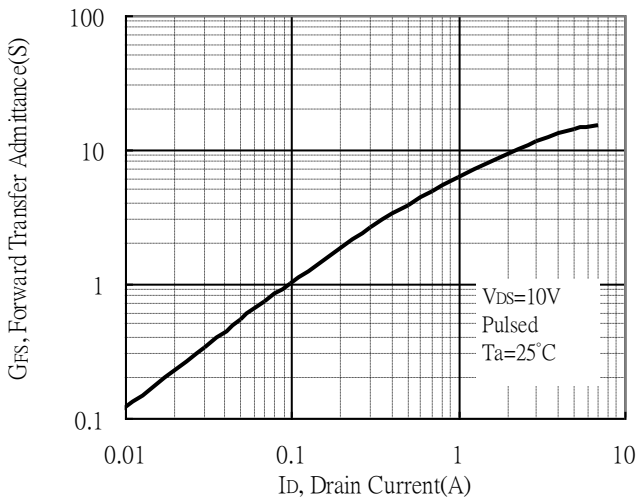
Capacitance vs Drain-to-Source Voltage



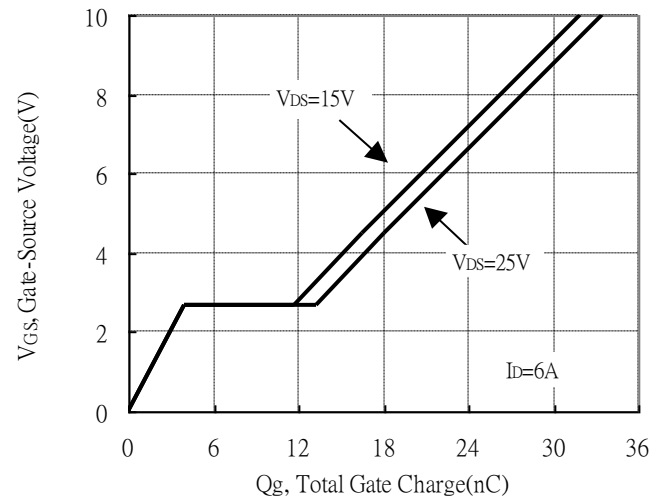
Threshold Voltage vs Junction Temperature



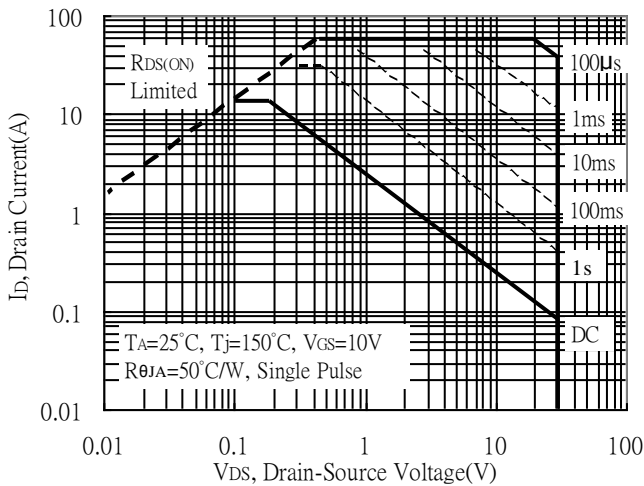
Forward Transfer Admittance vs Drain Current



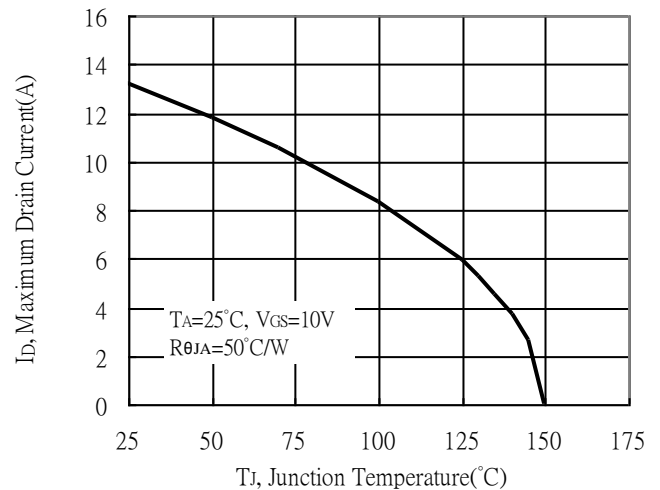
Gate Charge Characteristics



Maximum Safe Operating Area



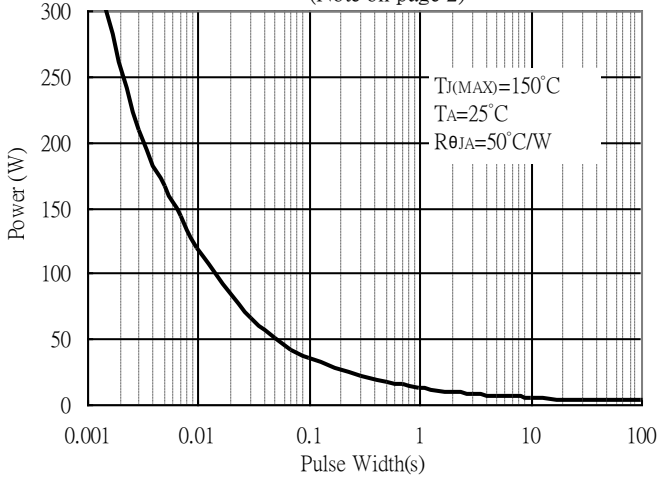
Maximum Drain Current vs Junction Temperature



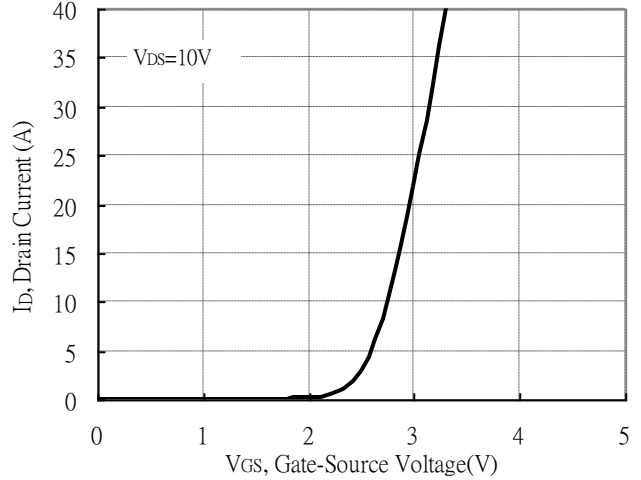


**Typical Characteristics(Cont.) : Q1( N-channel)**

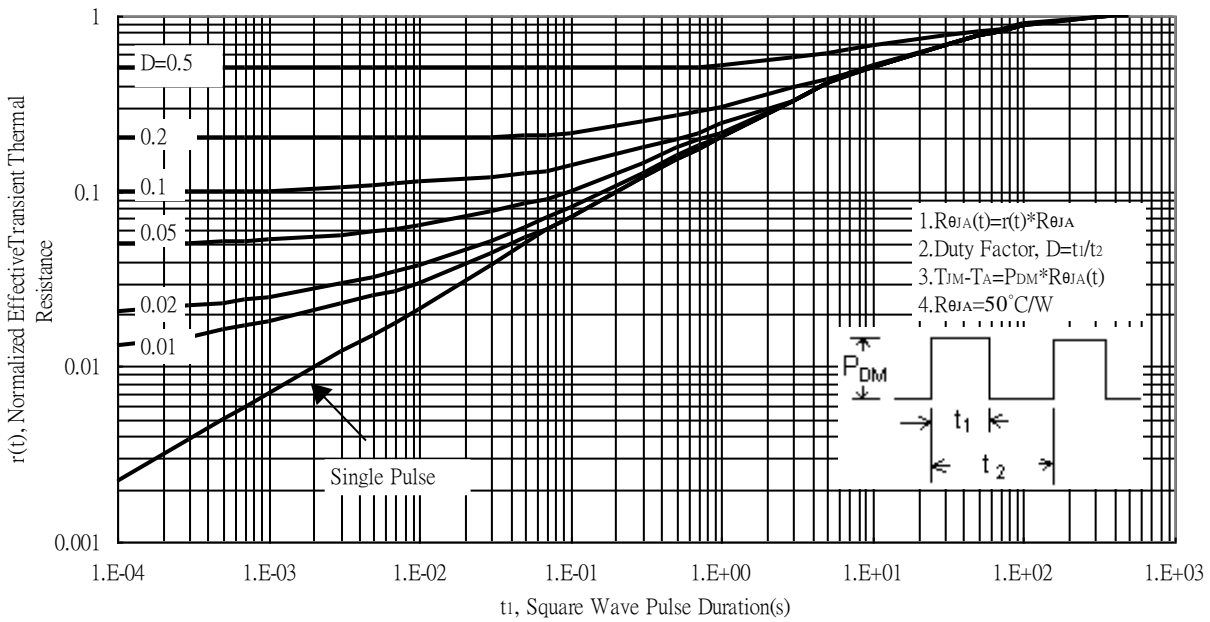
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



Typical Transfer Characteristics



Transient Thermal Response Curves

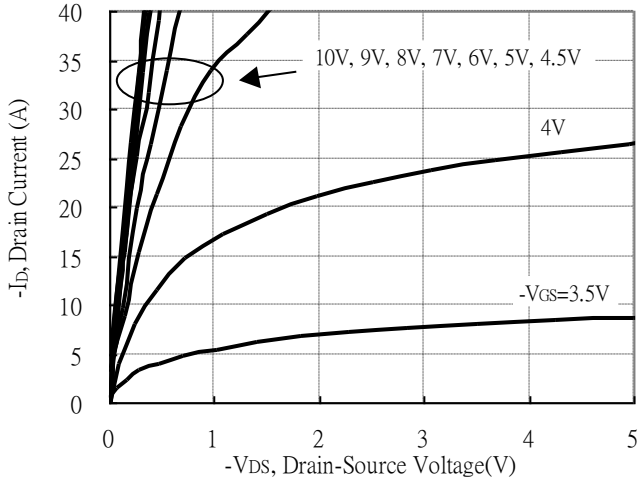




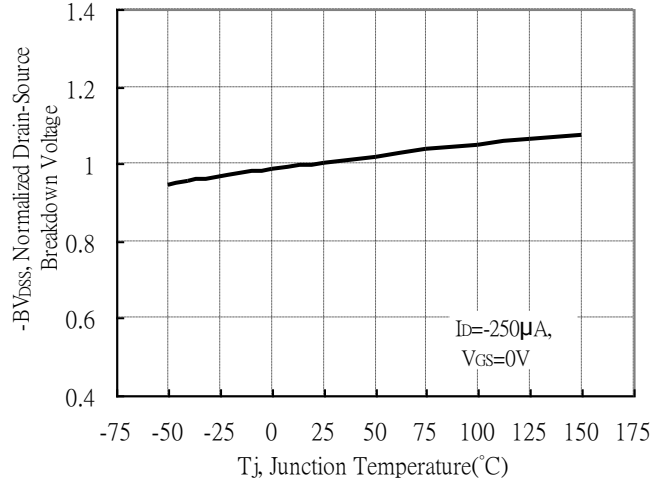


**Typical Characteristics : Q2( P-channel)**

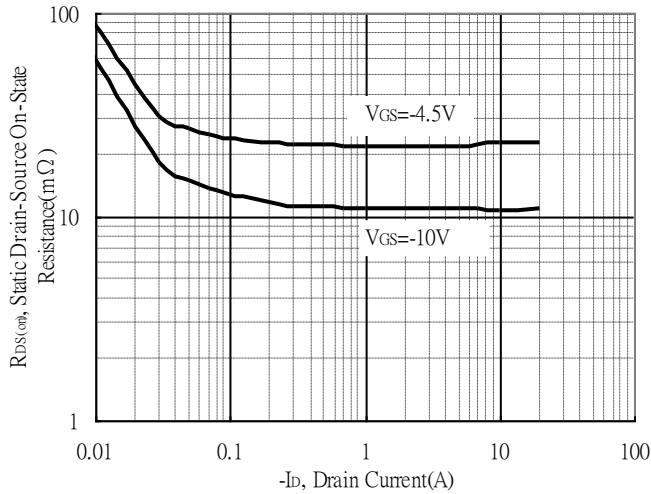
Typical Output Characteristics



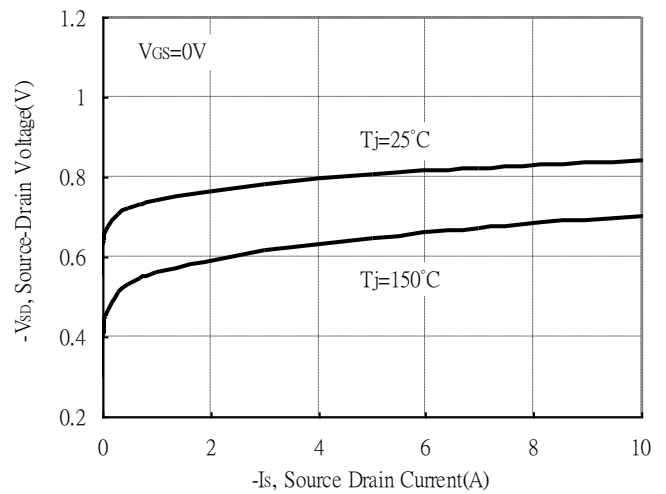
Brekdown Voltage vs Ambient Temperature



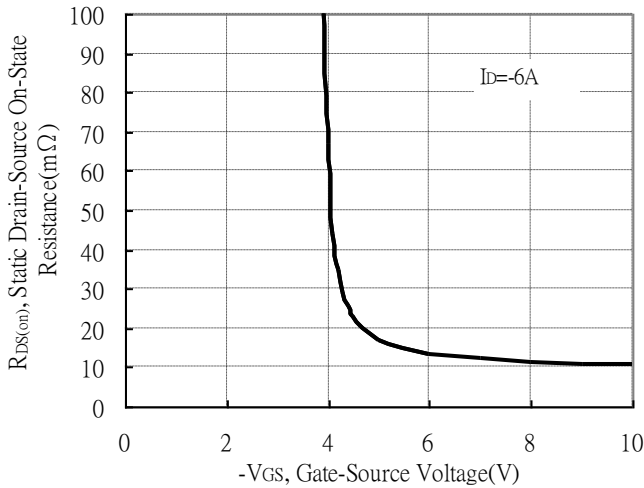
Static Drain-Source On-State resistance vs Drain Current



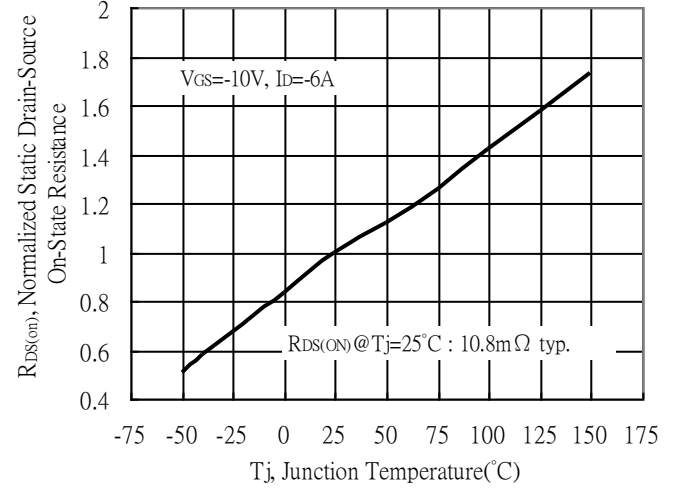
Source Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

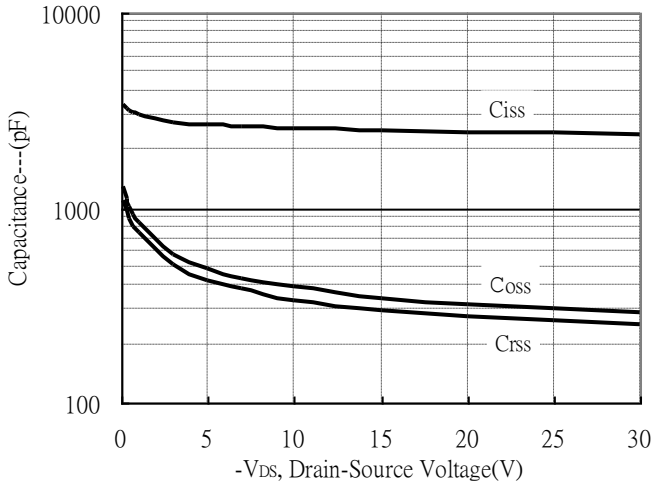


Drain-Source On-State Resistance vs Junction Temperature

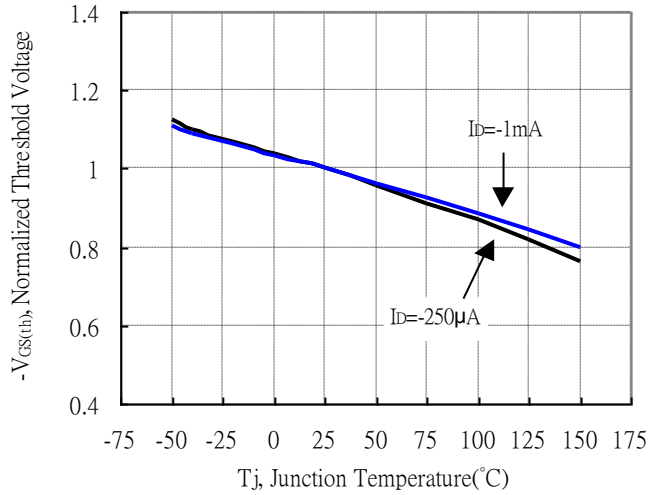


**Typical Characteristics(Cont.) : Q2(P-channel)**

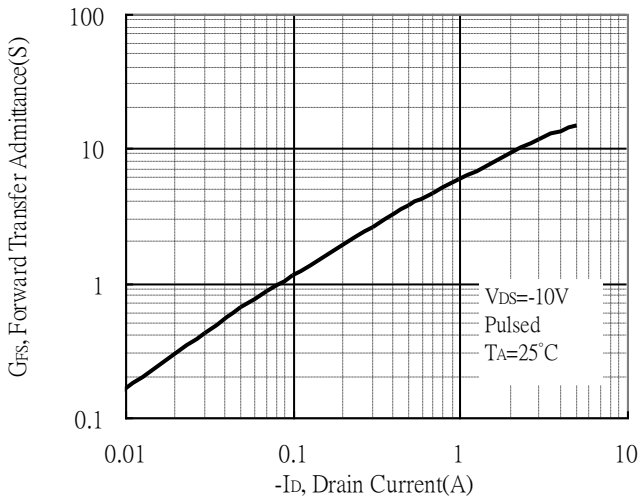
Capacitance vs Drain-to-Source Voltage



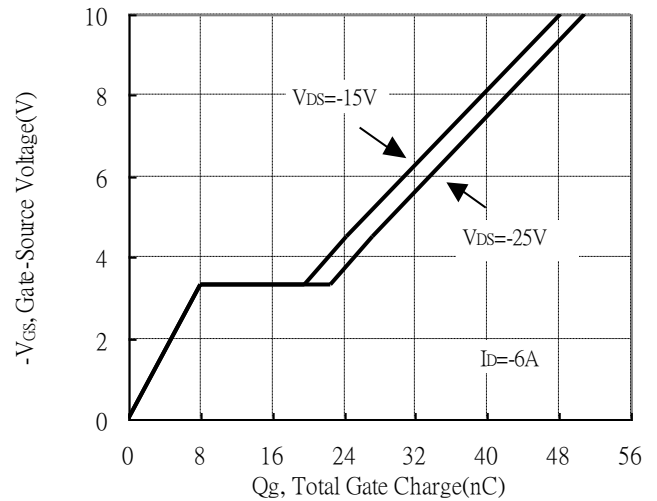
Threshold Voltage vs Junction Temperature



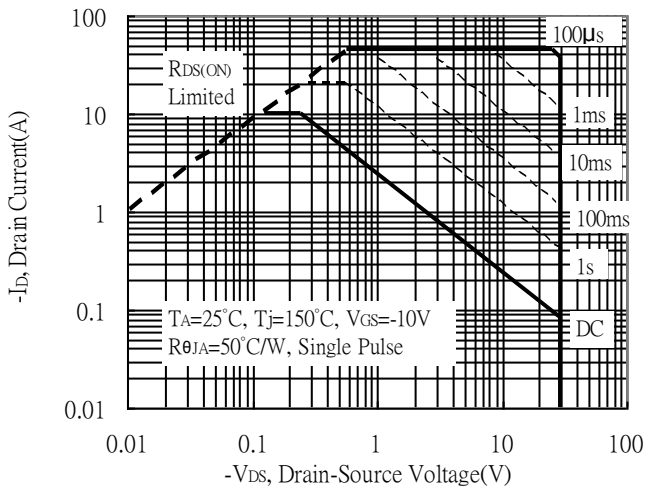
Forward Transfer Admittance vs Drain Current



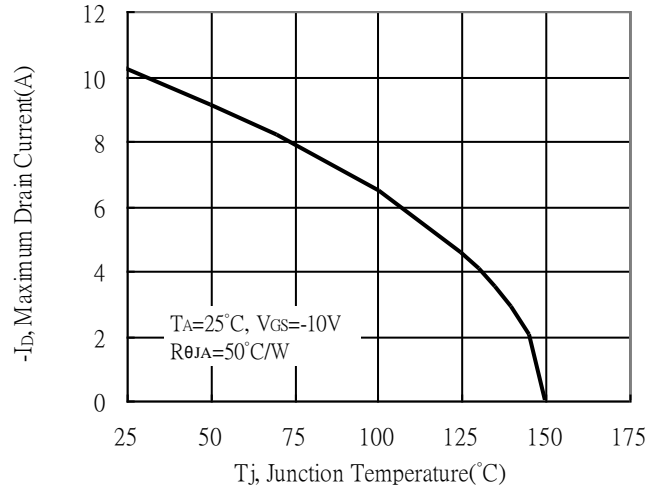
Gate Charge Characteristics



Maximum Safe Operating Area

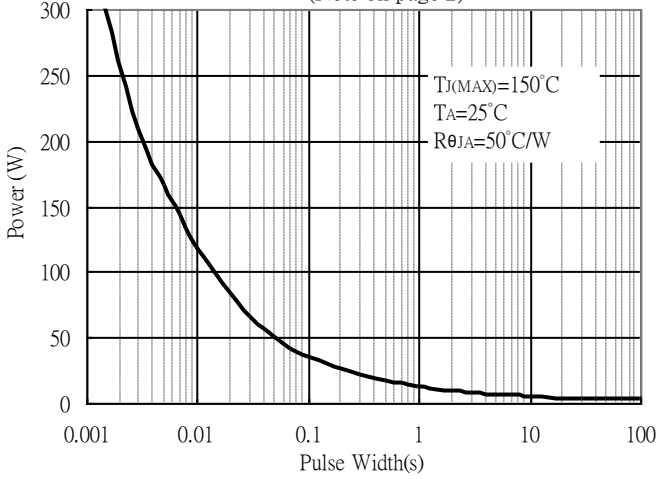


Maximum Drain Current vs Junction Temperature

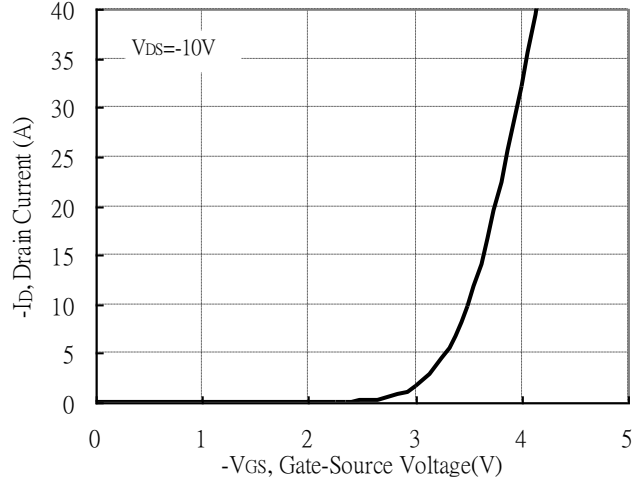


**Typical Characteristics(Cont.) : Q2(P-channel)**

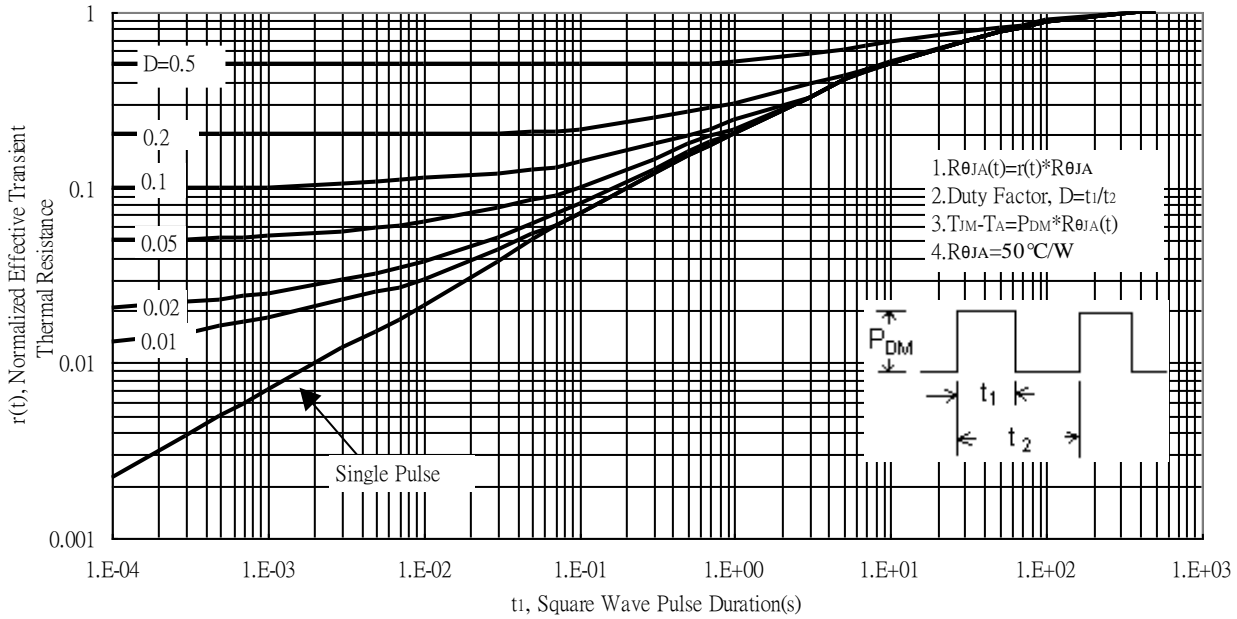
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



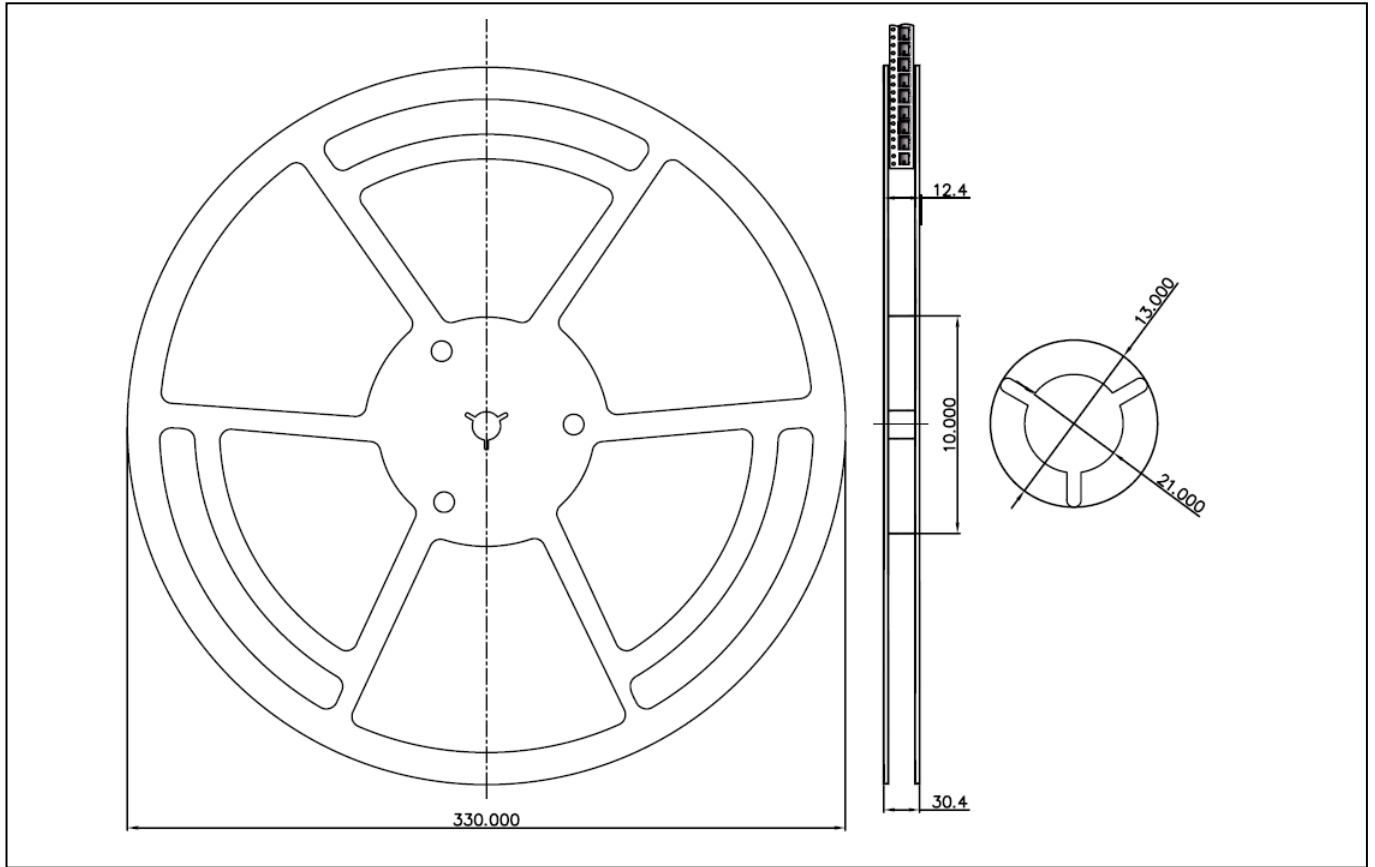
Typical Transfer Characteristics



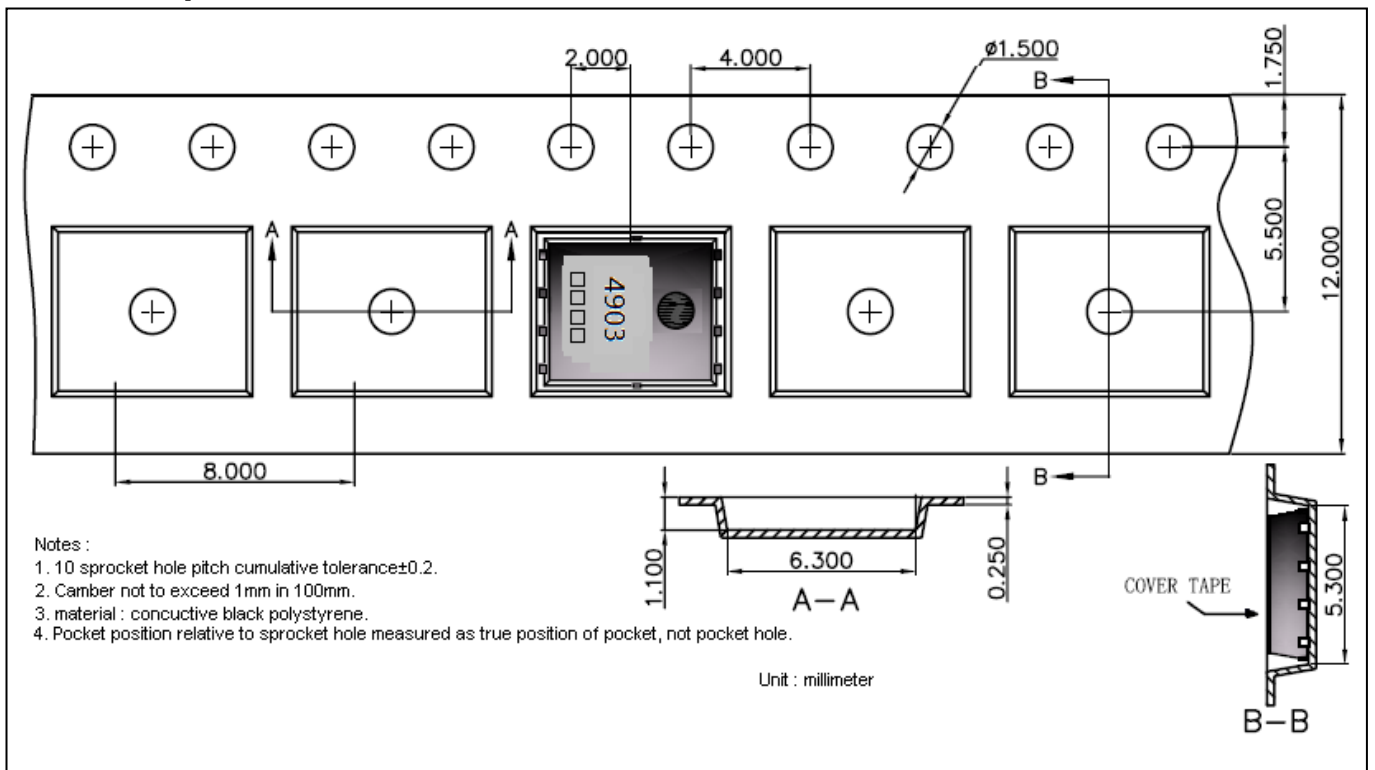
Transient Thermal Response Curves



**Reel Dimension**



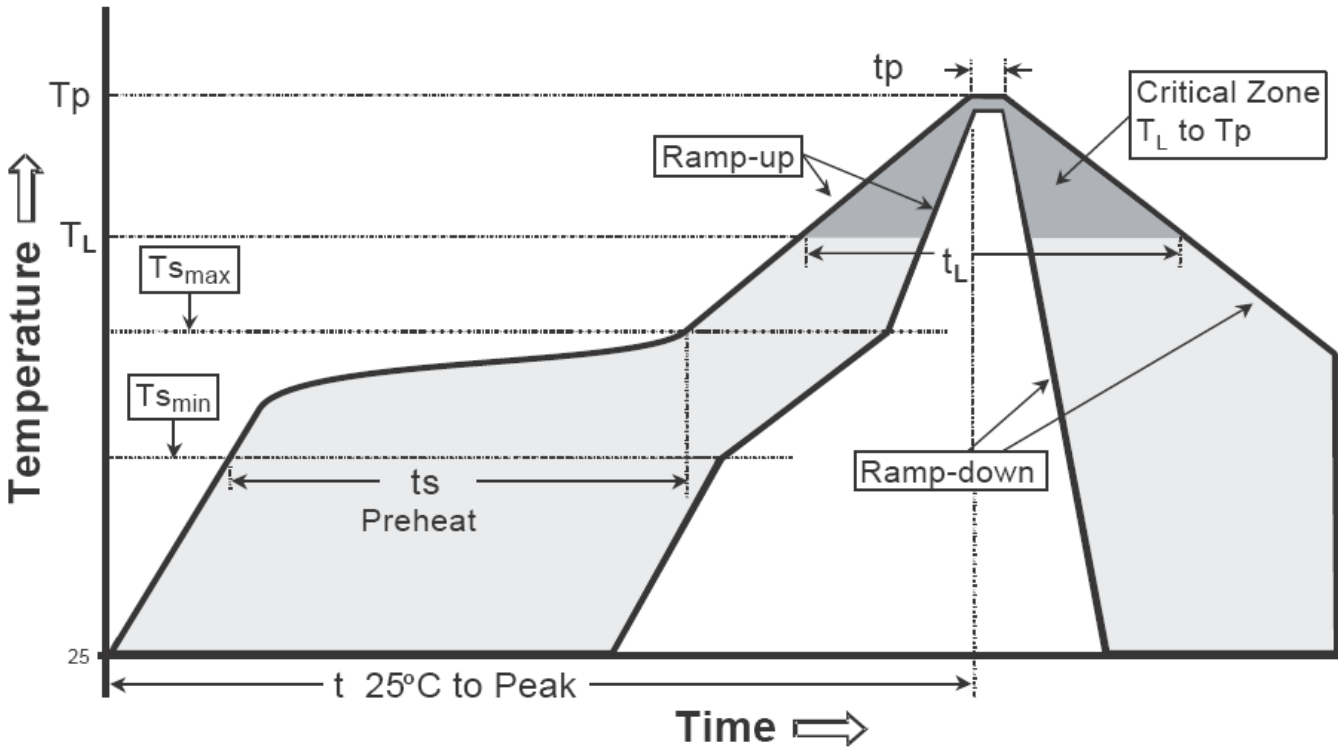
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

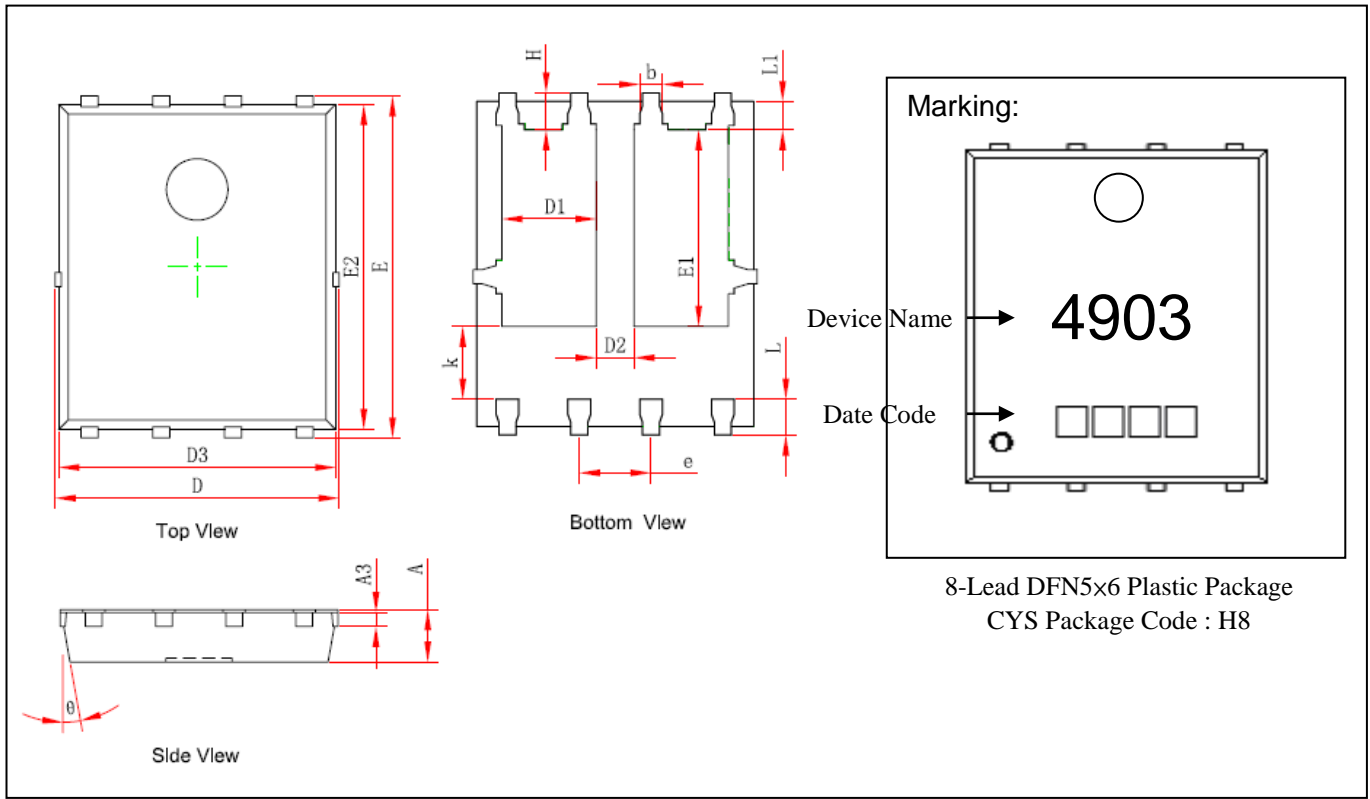
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (Tl)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(Tp)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note :1. All temperatures refer to topside of the package, measured on the package body surface.  
 2.For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.

**DFN5x6 Dimension**



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039	E2	5.674	5.826	0.223	0.229
A3	0.254	REF	0.010	REF	k	1.190	1.390	0.047	0.055
D	4.944	5.096	0.195	0.201	b	0.350	0.450	0.014	0.018
E	5.974	6.126	0.235	0.241	e	1.270	TYP	0.050	TYP
D1	1.470	1.870	0.058	0.074	L	0.559	0.711	0.022	0.028
D2	0.470	0.870	0.019	0.034	L1	0.424	0.576	0.017	0.023
E1	3.375	3.575	0.133	0.141	H	0.574	0.726	0.023	0.029
D3	4.824	4.976	0.190	0.196	θ	10°	12°	10°	12°

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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