Dual N-Channel Logic Level Enhancement Mode Power MOSFET

MTBA5A10Q8

BV_{DSS}	100V
I_D	3A
$R_{DSON(MAX)}$	150mΩ

Description

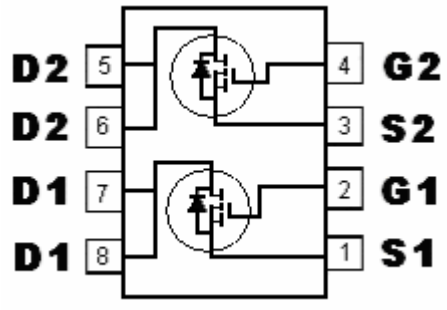
The MTBA5A10Q8 provides the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost effectiveness.
The SOP-8 package is universally preferred for all commercial-industrial surface mount applications.

Features

- $R_{DS(ON)}$=150mΩ(max.)@V_{GS}=10V, I_D=2.5A
- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Dual N-ch MOSFET package
- Pb-free lead plating & Halogen-free package

Equivalent Circuit

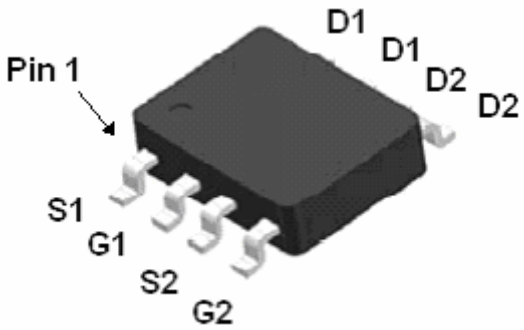
MTBA5A10Q8



G：Gate
S：Source
D：Drain

Outline

SOP-8



Absolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	
Continuous Drain Current, T_C=25 °C		I_D	3	A
Continuous Drain Current, T_C=100 °C		I_D	2.1	
Pulsed Drain Current (Note 1)		I_{DM}	12	
Power Dissipation	T_A=25°C (Note 3)	P_D	2.4	W
	T_A=100°C		1.3	
Operating Junction and Storage Temperature Range		Tj ; Tstg	-55~+175	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	25	°C/W
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	62.5 *3	°C/W

Note : 1. Pulse width limited by maximum junction temperature
2. Duty cycle≤1%
3. Surface mounted on 1 in² copper pad of FR-4 board, 125°C/W when mounted on minimum copper pad

Characteristics (Tj=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	100	-	-	V	V_{GS}=0, ID=250μA
$V_{GS(th)}$	1	1.5	3	V	V_{DS} = V_{GS}, I_D=250μA
G_{FS} *1	-	8	-	S	V_{DS} =5V, I_D=2.5A
I_{GSS}	-	-	±100	nA	V_{GS}=±20
I_{DSS}	-	-	1	μA	V_{DS} =80V, V_{GS} =0
	-	-	25		V_{DS} =70V, V_{GS} =0, Tj=125°C
$I_{D(ON)}$ *1	3	-	-	A	V_{DS} =5V, V_{GS} =10V
*$R_{DS(ON)}$ *1	-	125	150	mΩ	V_{GS} =10V, I_D=2.5A
	-	168	225		V_{GS} =5V, I_D=2A
Dynamic					
Qg *1, 2	-	18.8	-	nC	I_D=2.5A, V_{DS}=80V, V_{GS}=10V
Qgs *1, 2	-	3.8	-		
Qgd *1, 2	-	4.5	-		
$t_{d(ON)}$ *1, 2	-	15	-	ns	V_{DS}=50V, I_D=1A, V_{GS}=10V, R_G=6Ω
t_r *1, 2	-	35	-		
$t_{d(OFF)}$ *1, 2	-	25	-		
t_f *1, 2	-	25	-		
Ciss	-	740	-	pF	V_{GS}=0V, V_{DS}=20V, f=1MHz
Coss	-	62	-		
Crss	-	50	-		

Source-Drain Diode					
I_S *1	-	-	3	A	
I_{SM} *3	-	-	12		
V_{SD} *1	-	-	1.3	V	I_F= I_S, V_{GS}=0V

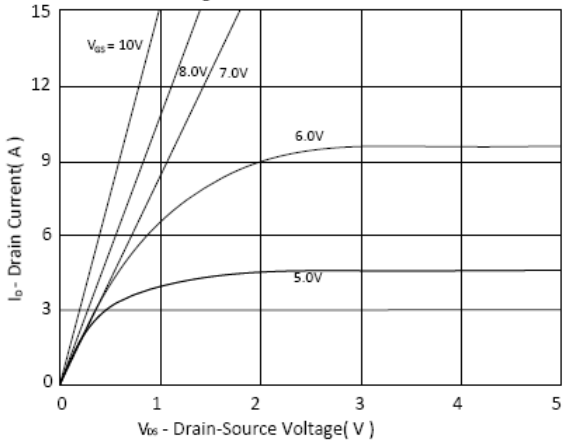
Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
*2.Independent of operating temperature
*3.Pulse width limited by maximum junction temperature.

Ordering Information

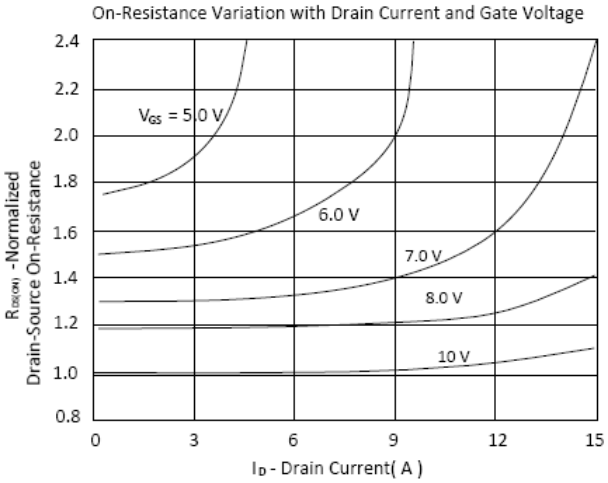
Device	Package	Shipping	Marking
MTBA5A10Q8	SOP-8 (Pb-free lead plating & Halogen-free package)	2500 pcs / Tape & Reel	BA5A10

Typical Characteristics

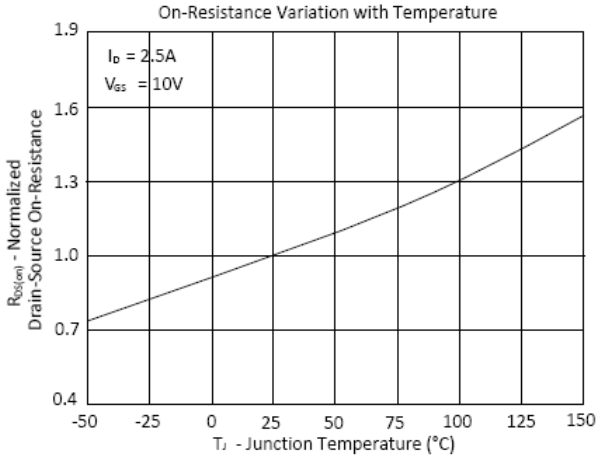
On-Region Characteristics



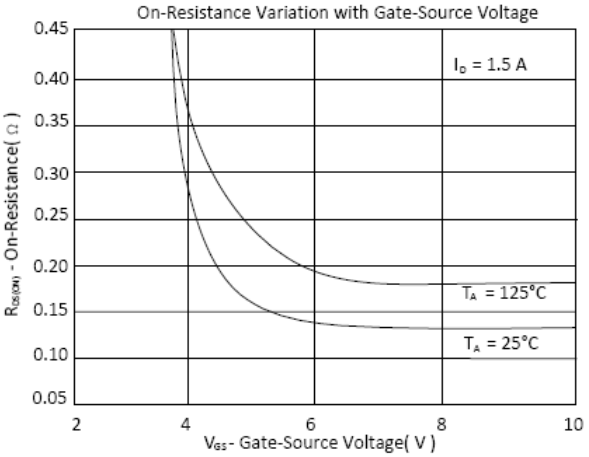
On-Resistance Variation with Drain Current and Gate Voltage



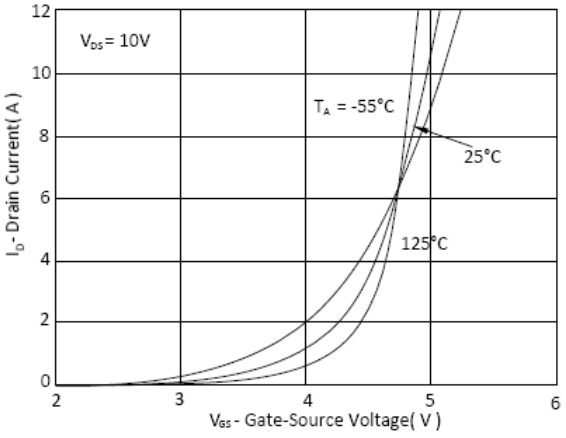
On-Resistance Variation with Temperature



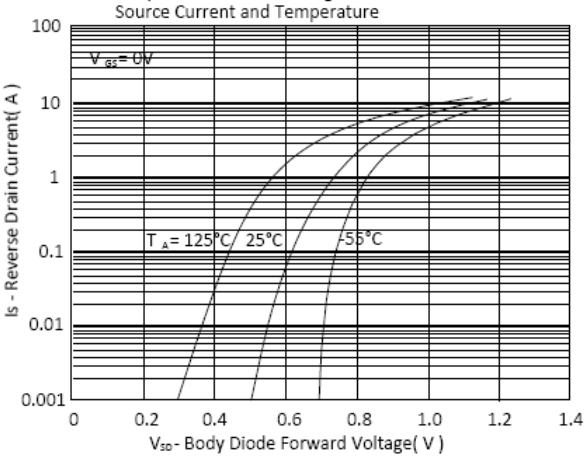
On-Resistance Variation with Gate-Source Voltage



Transfer Characteristics

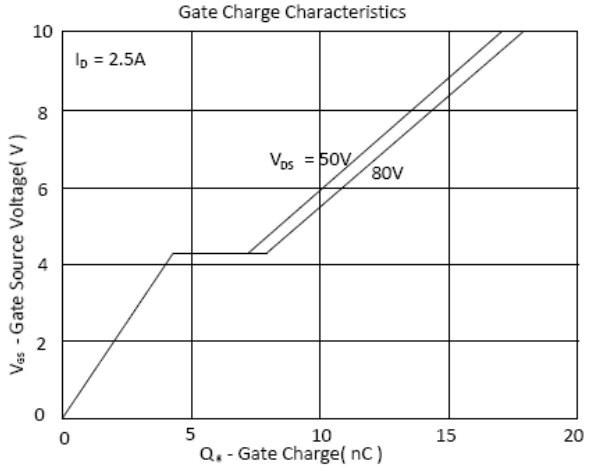


Body Diode Forward Voltage Variation with Source Current and Temperature

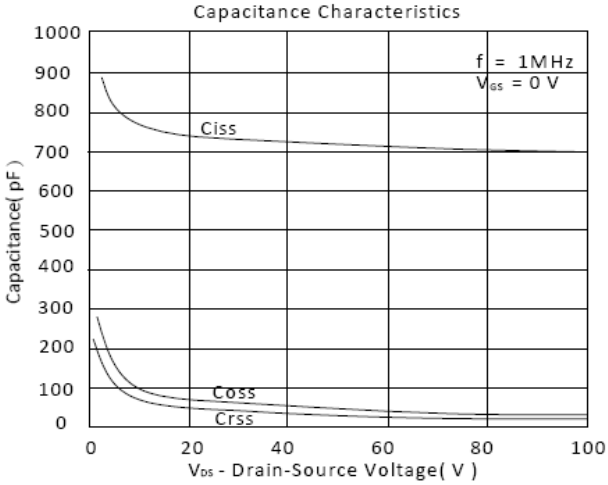


Typical Characteristics(Cont.)

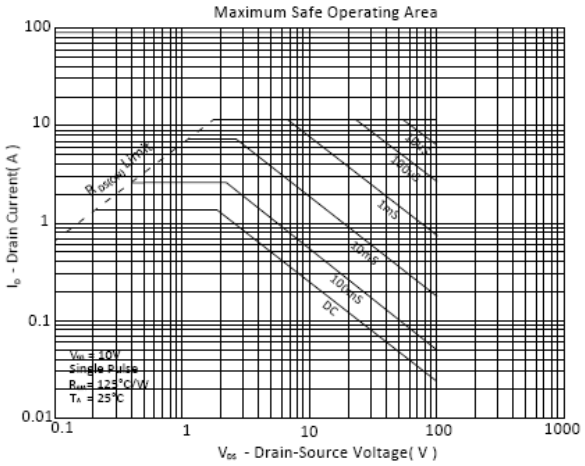
Gate Charge Characteristics



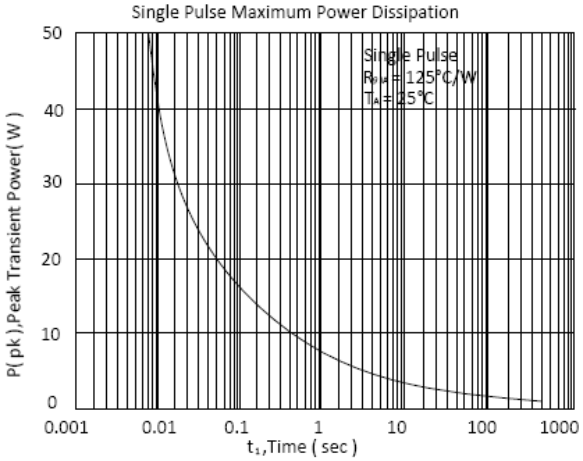
Capacitance Characteristics



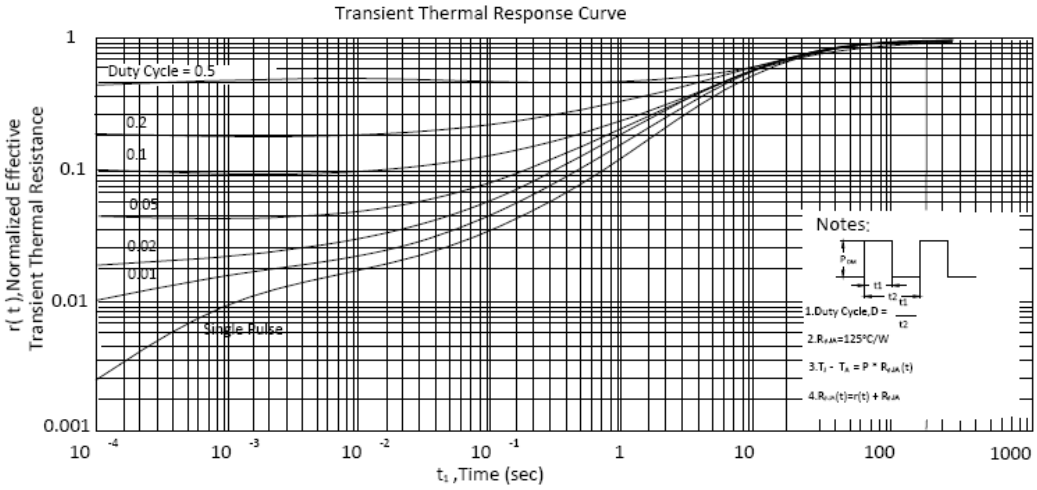
Maximum Safe Operating Area



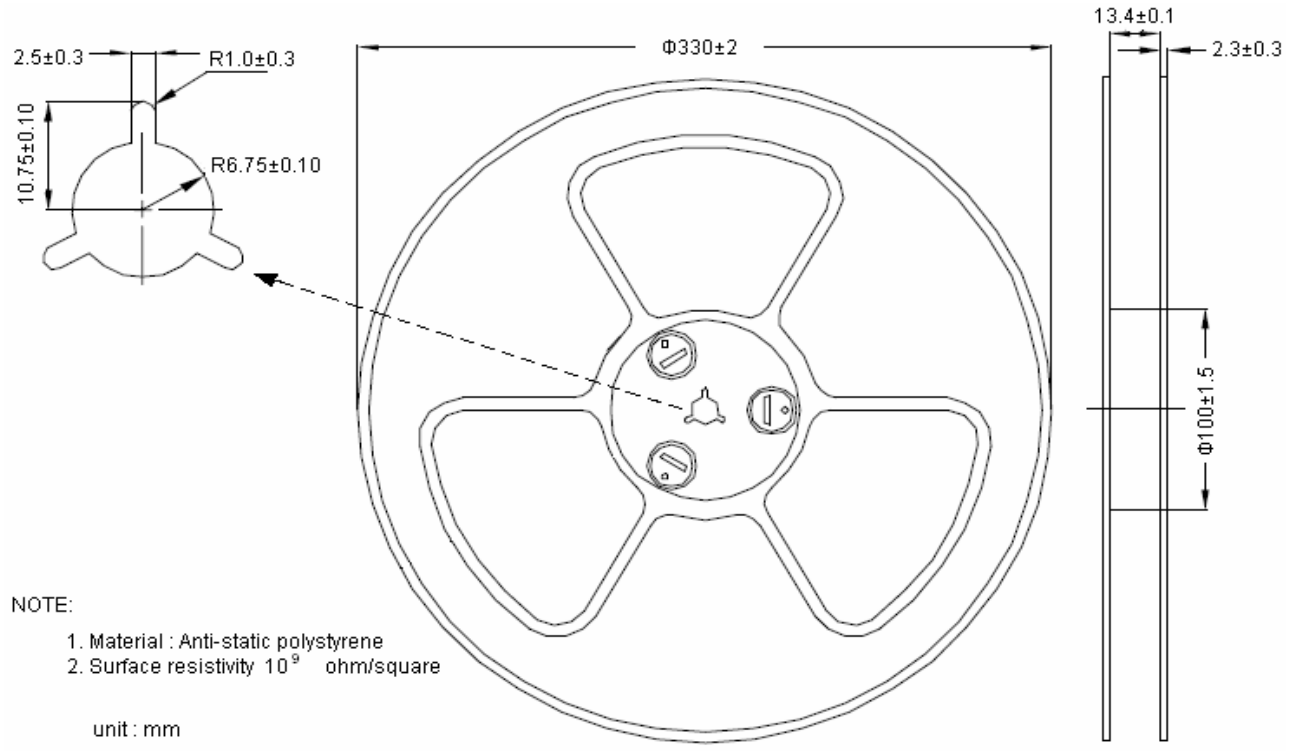
Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve



Reel Dimension

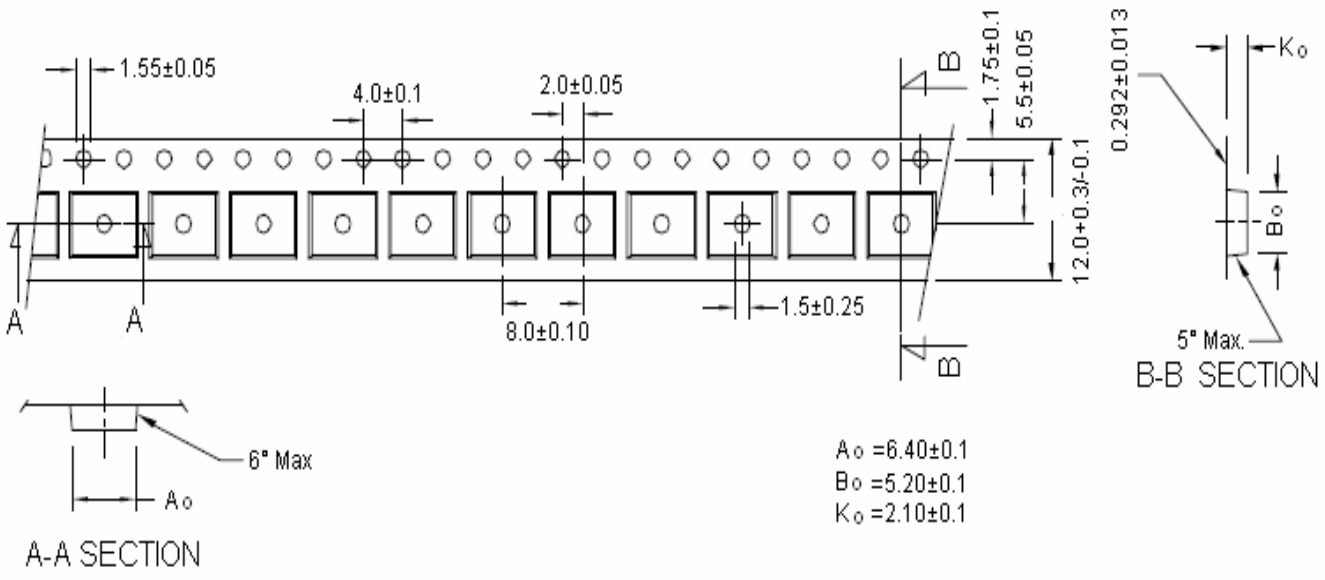


NOTE:

1. Material : Anti-static polystyrene
2. Surface resistivity 10^9 ohm/square

unit : mm

Carrier Tape Dimension



Notes:

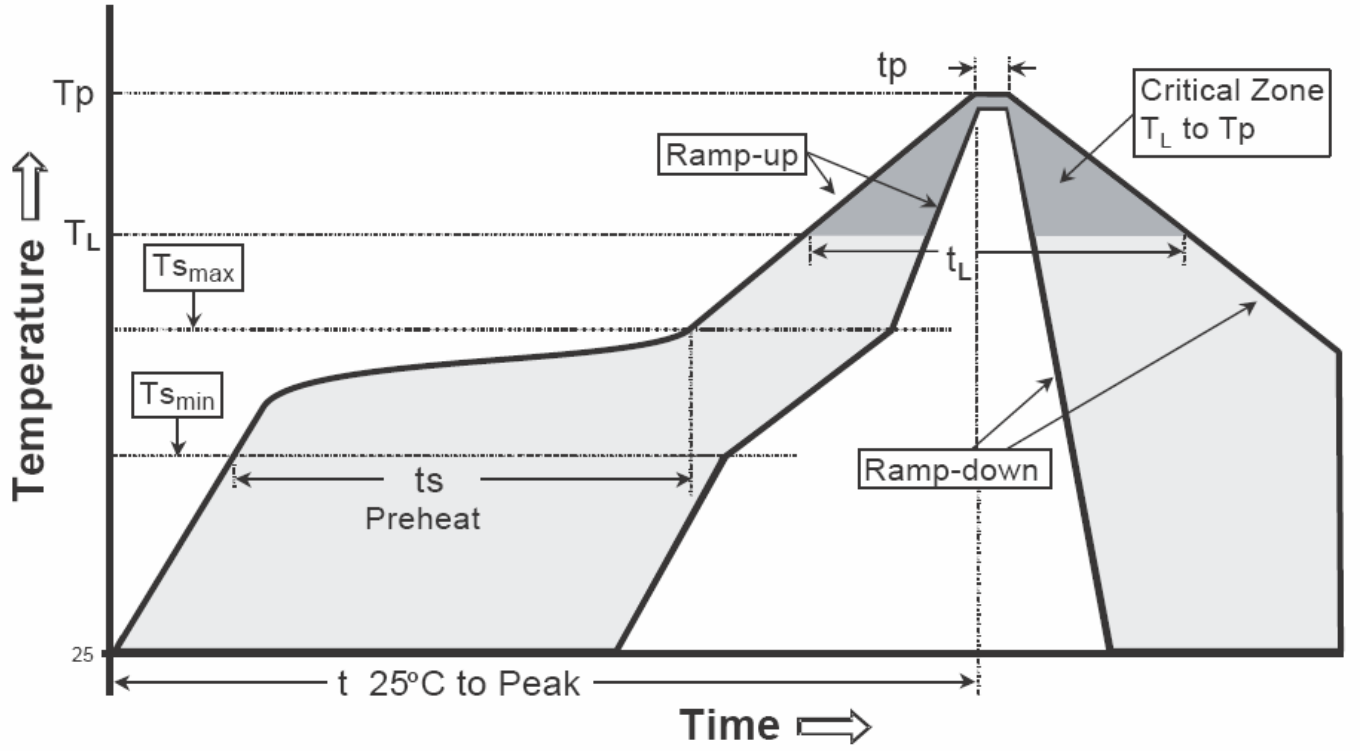
1. 10 sprocket hole pitch cumulative tolerance ±0.2.
2. Camber not to exceed 1mm in 100mm.
3. Material: conductive black polystyrene
4. Ao & Bo measured on a plane 0.3mm above the bottom of the pocket.
5. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

Uni : millimeter

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

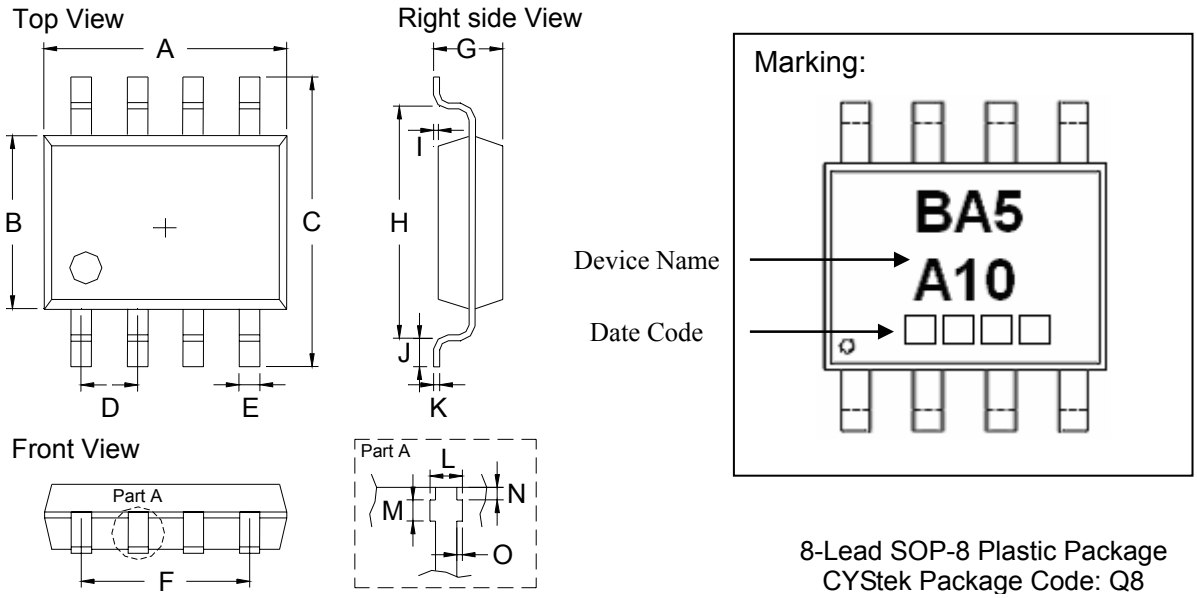
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat −Temperature Min(T_S min) −Temperature Max(T_S max) −Time($t_{s\ min}$ to $t_{s\ max}$)	 100°C 150°C 60-120 seconds	 150°C 200°C 60-180 seconds
Time maintained above: −Temperature (T_L) − Time (t_L)	 183°C 60-150 seconds	 217°C 60-150 seconds
Peak Temperature(T_P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

SOP-8 Dimension



8-Lead SOP-8 Plastic Package
CYStek Package Code: Q8

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1850	0.2007	4.70	5.10	I	0.0031	0.0110	0.08	0.28
B	0.1457	0.1614	3.70	4.10	J	0.0157	0.0323	0.40	0.83
C	0.2283	0.2441	5.80	6.20	K	0.0074	0.0102	0.19	0.26
D	0.0500*		1.27*		L	0.0145	0.0204	0.37	0.52
E	0.0130	0.0201	0.33	0.51	M	0.0118	0.0197	0.30	0.50
F	0.1472	0.1527	3.74	3.88	N	0.0031	0.0051	0.08	0.13
G	0.0472	0.0638	1.20	1.62	O	0.0000	0.0059	0.00	0.15
H	0.1889	0.2007	4.80	5.10					

Notes: 1.Controlling dimension: millimeters.
2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.