

# N-Channel Enhancement Mode Power MOSFET

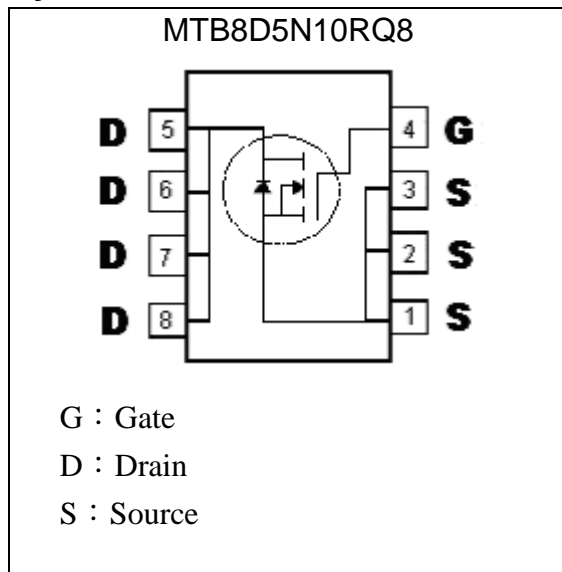
## MTB8D5N10RQ8

### Features

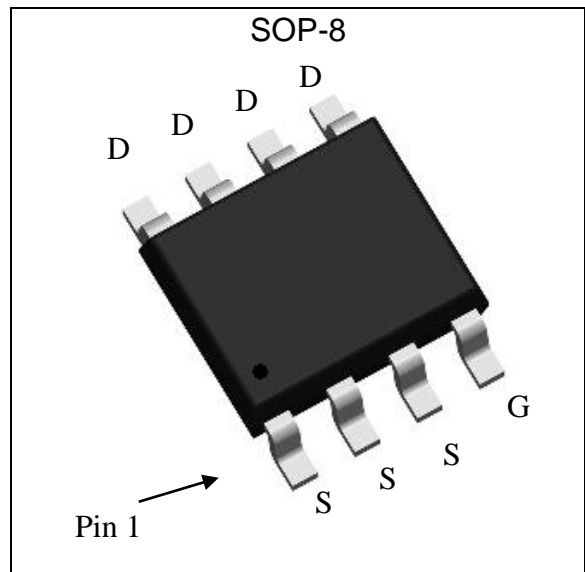
- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- RoHS compliant package

<b>BV<sub>DSS</sub></b>	<b>100V</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>c</sub>=25°C</b>	<b>25A</b>
<b>I<sub>D</sub>@V<sub>GS</sub>=10V, T<sub>A</sub>=25°C</b>	<b>9A</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=10V, I<sub>D</sub>=6A</b>	<b>7.5mΩ (typ)</b>
<b>R<sub>DS(ON)</sub>@V<sub>GS</sub>=4.5V, I<sub>D</sub>=4A</b>	<b>12mΩ (typ)</b>

### Symbol

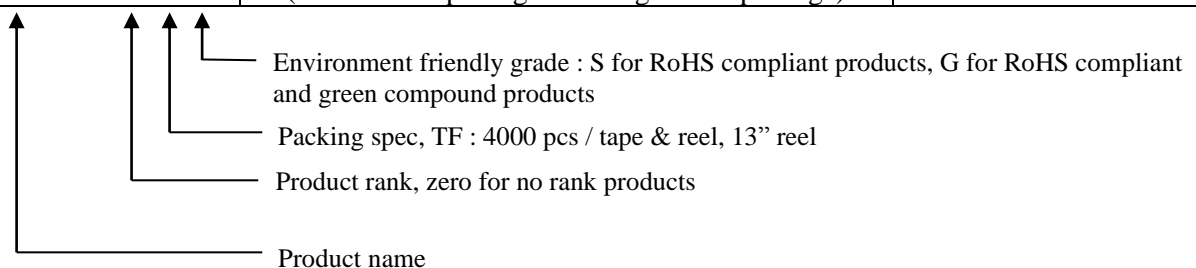


### Outline



### Ordering Information

Device	Package	Shipping
MTB8D5N10RQ8-0-TF-G	SOP-8 (Pb-free lead plating and halogen-free package)	4000 pcs / tape & reel





**Absolute Maximum Ratings (Ta=25°C)**

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	±20	
Continuous Drain Current @ T <sub>C</sub> =25°C, V <sub>GS</sub> =10V		I <sub>D</sub>	25	A
Continuous Drain Current @ T <sub>C</sub> =100°C, V <sub>GS</sub> =10V			15.8	
Continuous Drain Current @ T <sub>A</sub> =25°C, V <sub>GS</sub> =10V			9 *2	
Continuous Drain Current @ T <sub>A</sub> =70°C, V <sub>GS</sub> =10V			7.2 *2	
Pulsed Drain Current		I <sub>DM</sub>	100 *3	
Avalanche Current @ L=0.1mH		I <sub>AS</sub>	24	
Avalanche Energy @ L=0.5mH, I <sub>D</sub> =16A, V <sub>DD</sub> =50V		E <sub>AS</sub>	64 *5	mJ
Total Power Dissipation	T <sub>C</sub> =25°C	P <sub>D</sub>	16 *1	W
	T <sub>C</sub> =100°C		6.4 *1	
	T <sub>A</sub> =25°C		2.1 *2,4	
	T <sub>A</sub> =70°C		1.3 *2,4	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C

**Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R <sub>θJC</sub>	7.8	°C/W
Thermal Resistance, Junction-to-ambient, max	R <sub>θJA</sub>	60 *4	

- Note : 1. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2 oz. copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user's specific board design. The power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> and the maximum allowed junction temperature of 150°C.
3. Ratings are based on low frequency and low duty cycles to keep initial T<sub>J</sub>=25°C.
4. When mounted on 1 in<sup>2</sup> copper pad of FR-4 board ; 125°C/W when mounted on minimum copper pad.
5. 100% tested by conditions of L=0.5mH, I<sub>AS</sub>=6A, V<sub>GS</sub>=10V, V<sub>DD</sub>=50V

**Characteristics (Tj=25°C, unless otherwise specified)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA
ΔBV <sub>DSS</sub> /ΔT <sub>j</sub>	-	0.06	-	V/°C	Reference to 25°C, I <sub>D</sub> =250μA
V <sub>GS(th)</sub>	1	-	2.5	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA
*G <sub>FS</sub>	-	38	-	S	V <sub>DS</sub> =5V, I <sub>D</sub> =20A
I <sub>GSS</sub>	-	-	±100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
I <sub>DSS</sub>	-	-	1	μA	V <sub>DS</sub> =80V, V <sub>GS</sub> =0V
	-	-	5		V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>j</sub> =55°C
*R <sub>DS(ON)</sub>	-	7.5	9	mΩ	V <sub>GS</sub> =10V, I <sub>D</sub> =6A



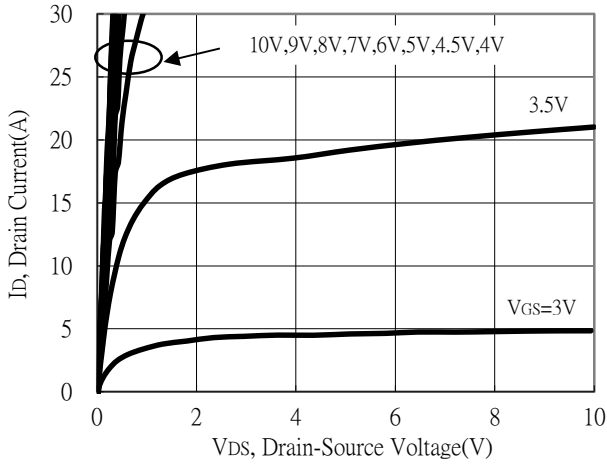
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
*R <sub>DS(ON)</sub>	-	12	17	mΩ	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A
<b>Dynamic</b>					
*Q <sub>g</sub>	-	44	-	nC	V <sub>DS</sub> =50V, I <sub>D</sub> =6A, V <sub>GS</sub> =10V
*Q <sub>gs</sub>	-	8.6	-		
*Q <sub>gd</sub>	-	8.3	-		
*t <sub>d(ON)</sub>	-	20	-	ns	V <sub>DS</sub> =50V, I <sub>D</sub> =4A, V <sub>GS</sub> =10V, R <sub>GS</sub> =1Ω
*tr	-	18	-		
*t <sub>d(OFF)</sub>	-	53	-		
*tf	-	8.8	-		
C <sub>iss</sub>	-	2712	-	pF	V <sub>GS</sub> =0V, V <sub>DS</sub> =50V, f=1MHz
C <sub>oss</sub>	-	283	-		
C <sub>rss</sub>	-	23	-		
R <sub>g</sub>	-	0.64	-	Ω	f=1MHz
<b>Source-Drain Diode</b>					
*I <sub>S</sub>	-	-	13	A	
*I <sub>SM</sub>	-	-	52		
*V <sub>SD</sub>	-	0.78	1.2	V	I <sub>S</sub> =6A, V <sub>GS</sub> =0V
*trr	-	40	-	ns	V <sub>GS</sub> =0V, I <sub>F</sub> =4A, dI <sub>F</sub> /dt=100A/μs
*Q <sub>rr</sub>	-	56	-	nC	

\*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

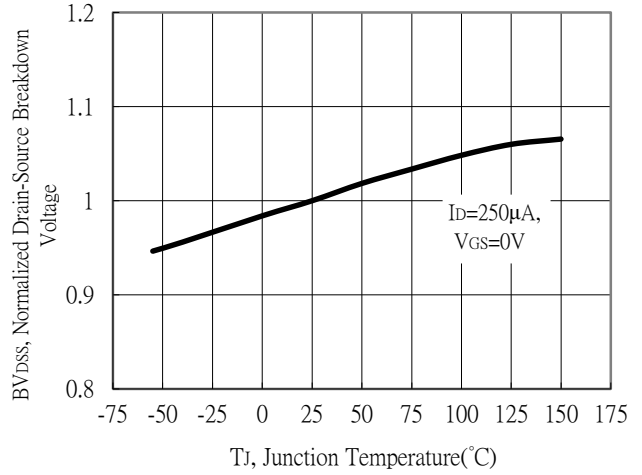


### Typical Characteristics

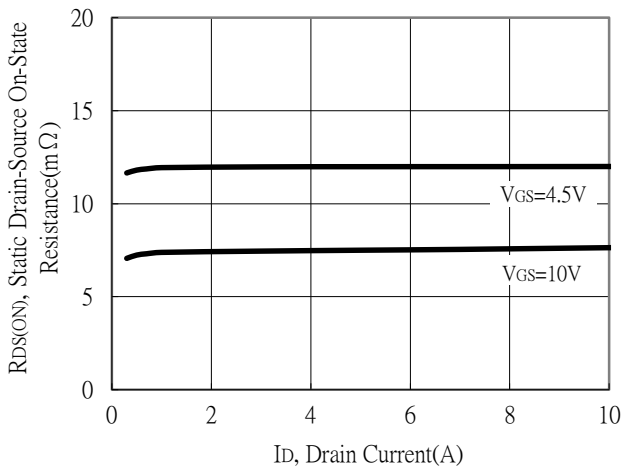
Typical Output Characteristics



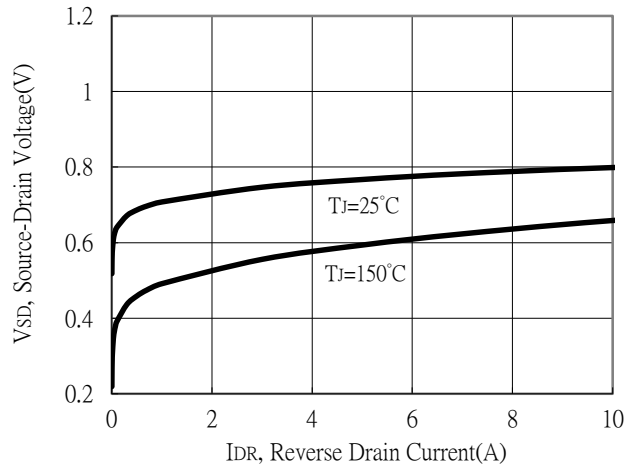
Brekdown Voltage vs Ambient Temperature



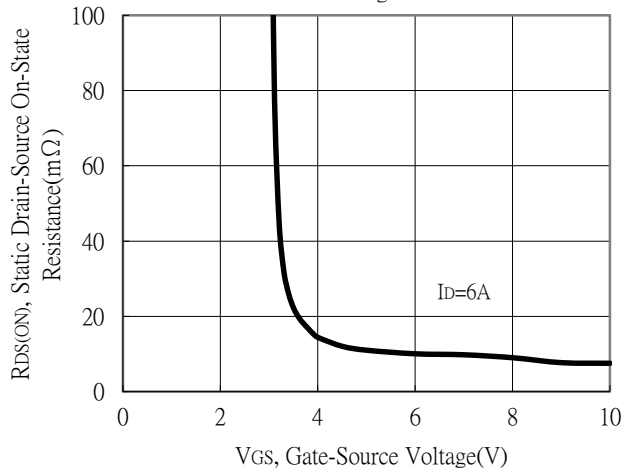
Static Drain-Source On-State resistance vs Drain Current



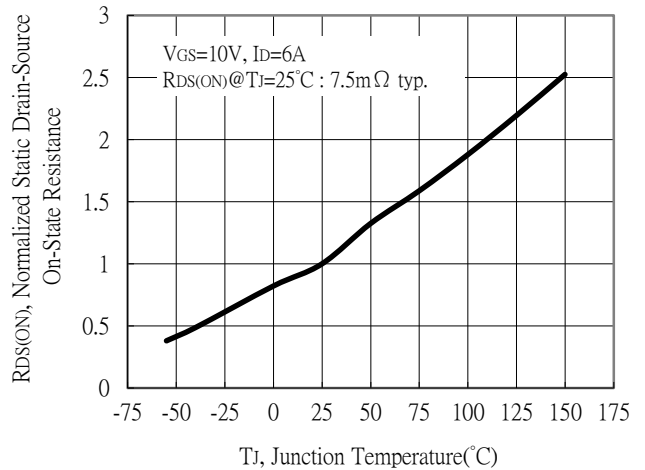
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

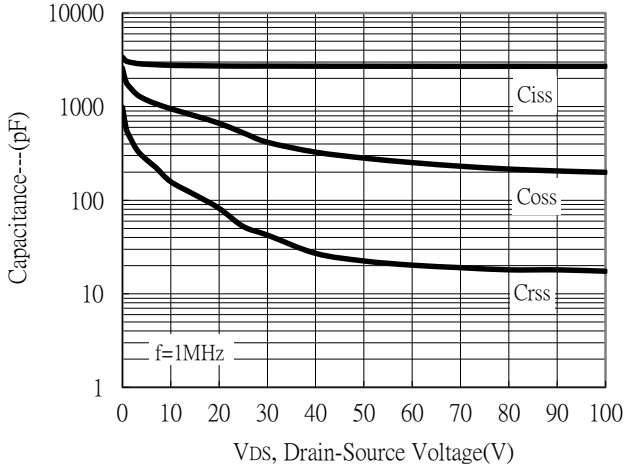


Drain-Source On-State Resistance vs Junction Temperature

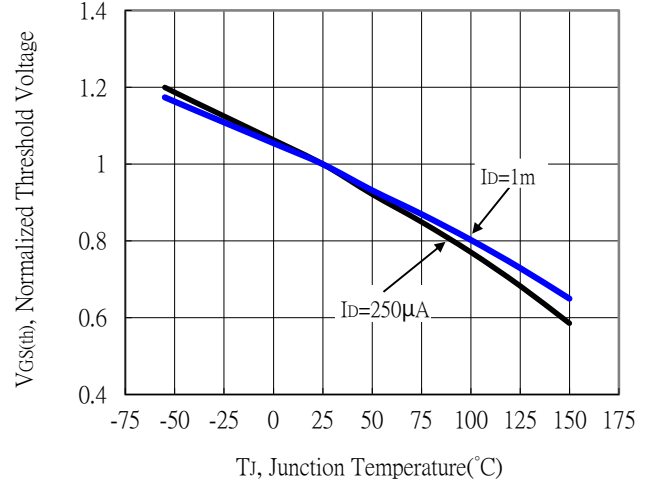


**Typical Characteristics(Cont.)**

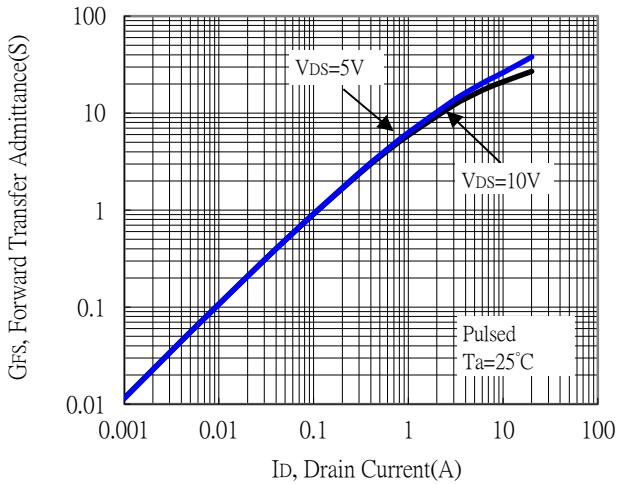
Capacitance vs Drain-to-Source Voltage



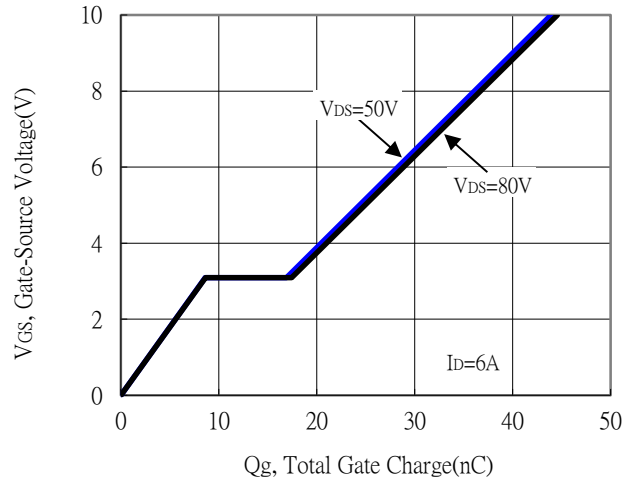
Threshold Voltage vs Junction Temperature



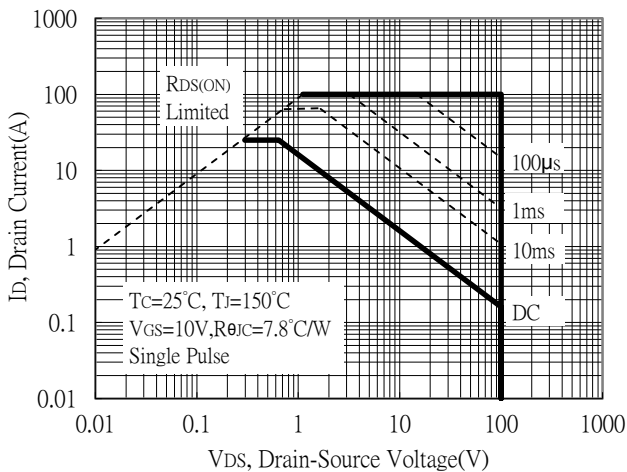
Forward Transfer Admittance vs Drain Current



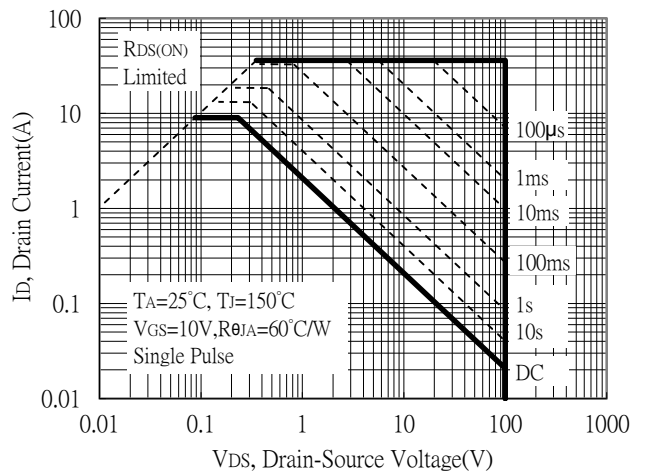
Gate Charge Characteristics



Maximum Safe Operating Area

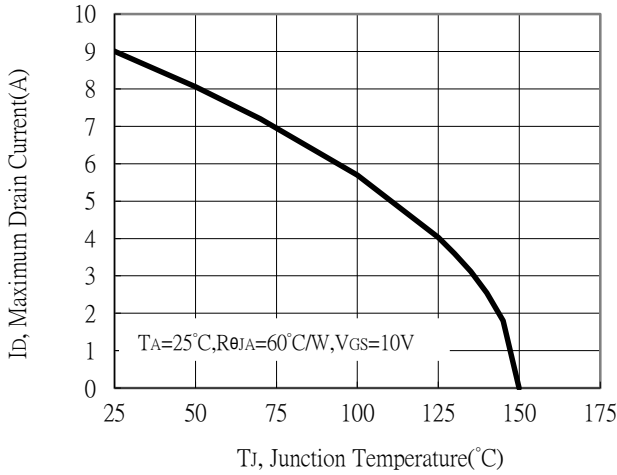


Maximum Safe Operating Area

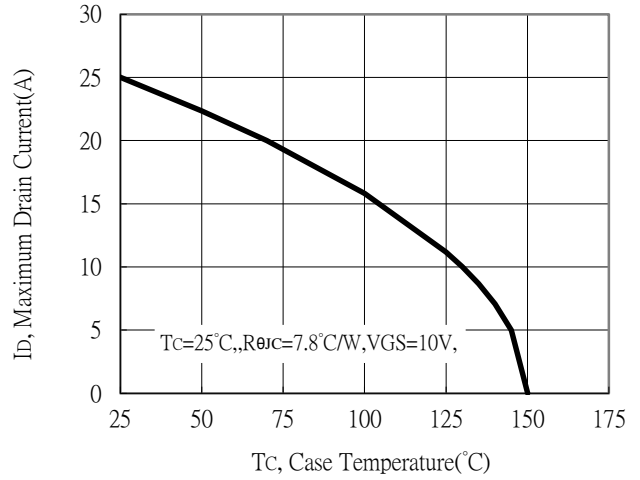


**Typical Characteristics (Cont.)**

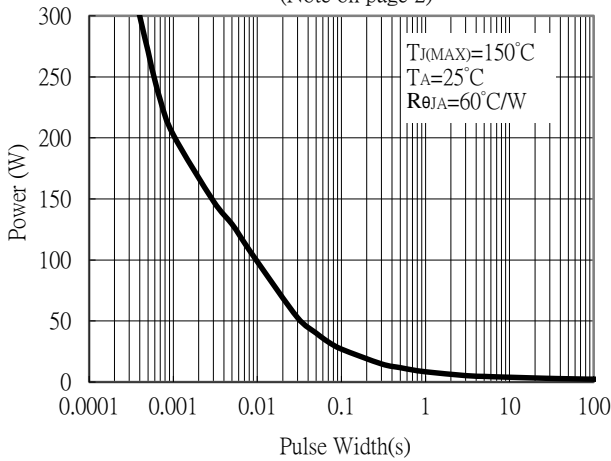
Maximum Drain Current vs Junction Temperature



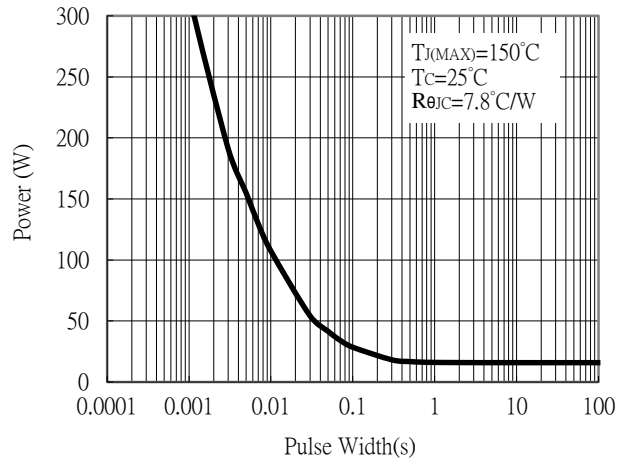
Maximum Drain Current vs Case Temperature



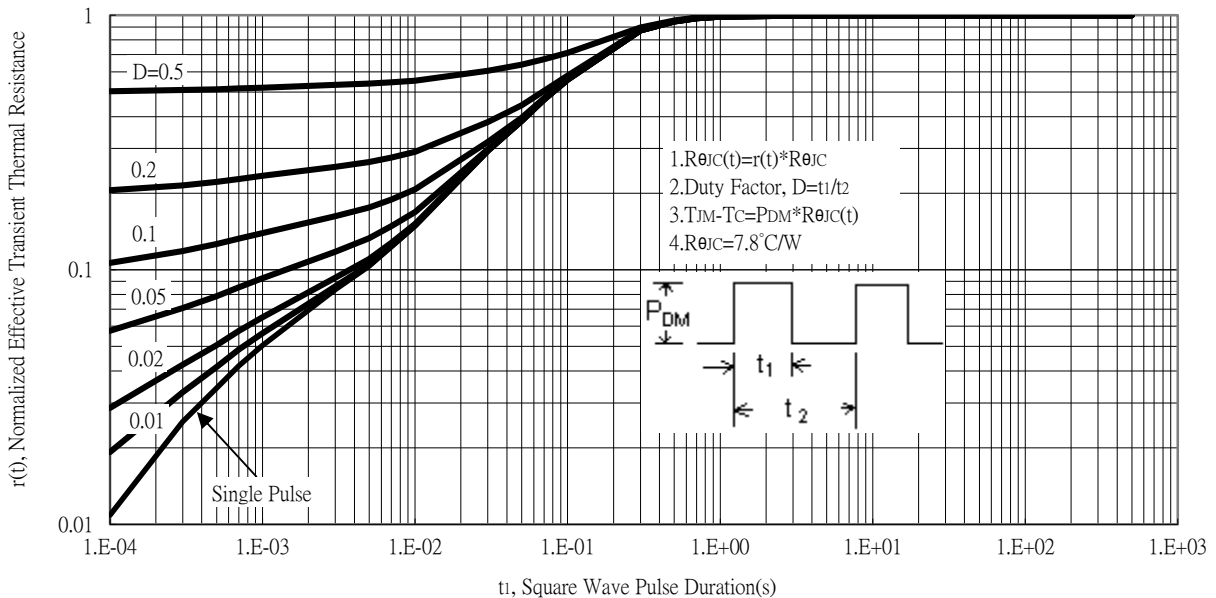
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 2)



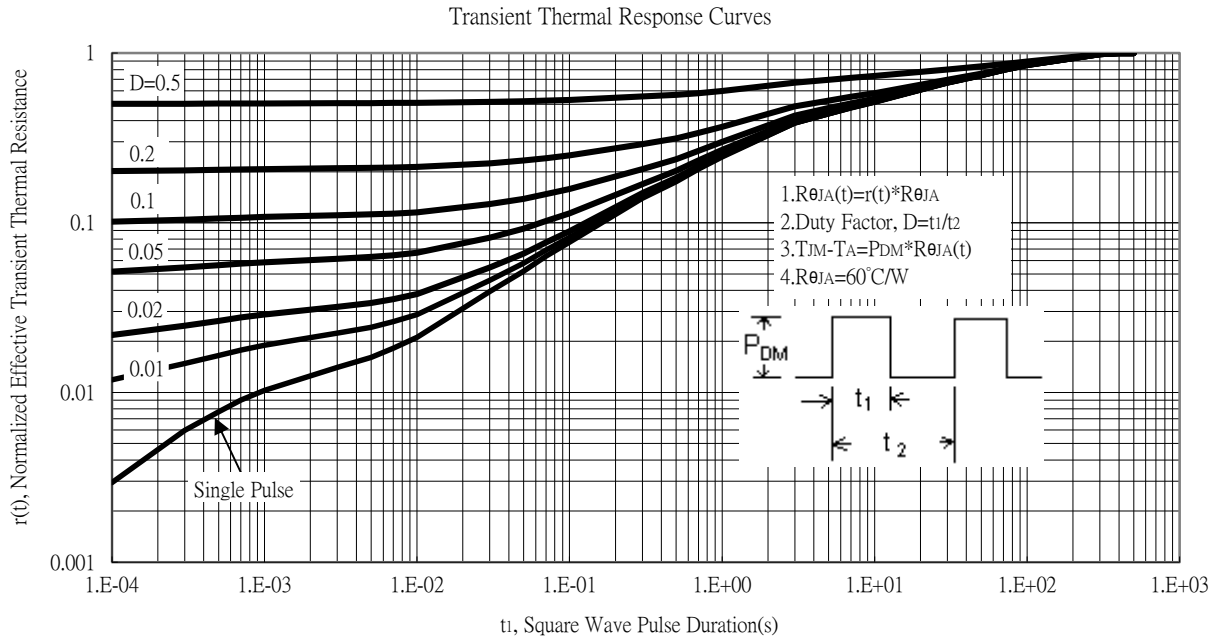
Single Pulse Power Rating, Junction to Case



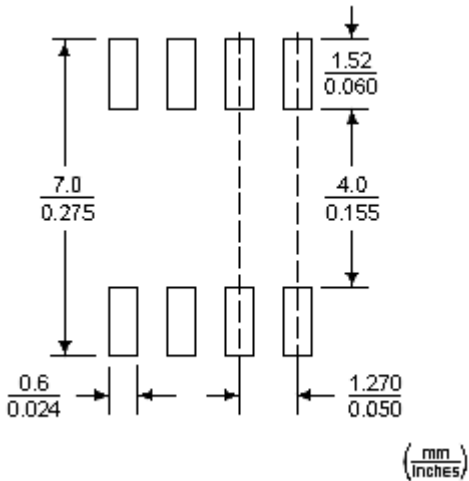
Transient Thermal Response Curves



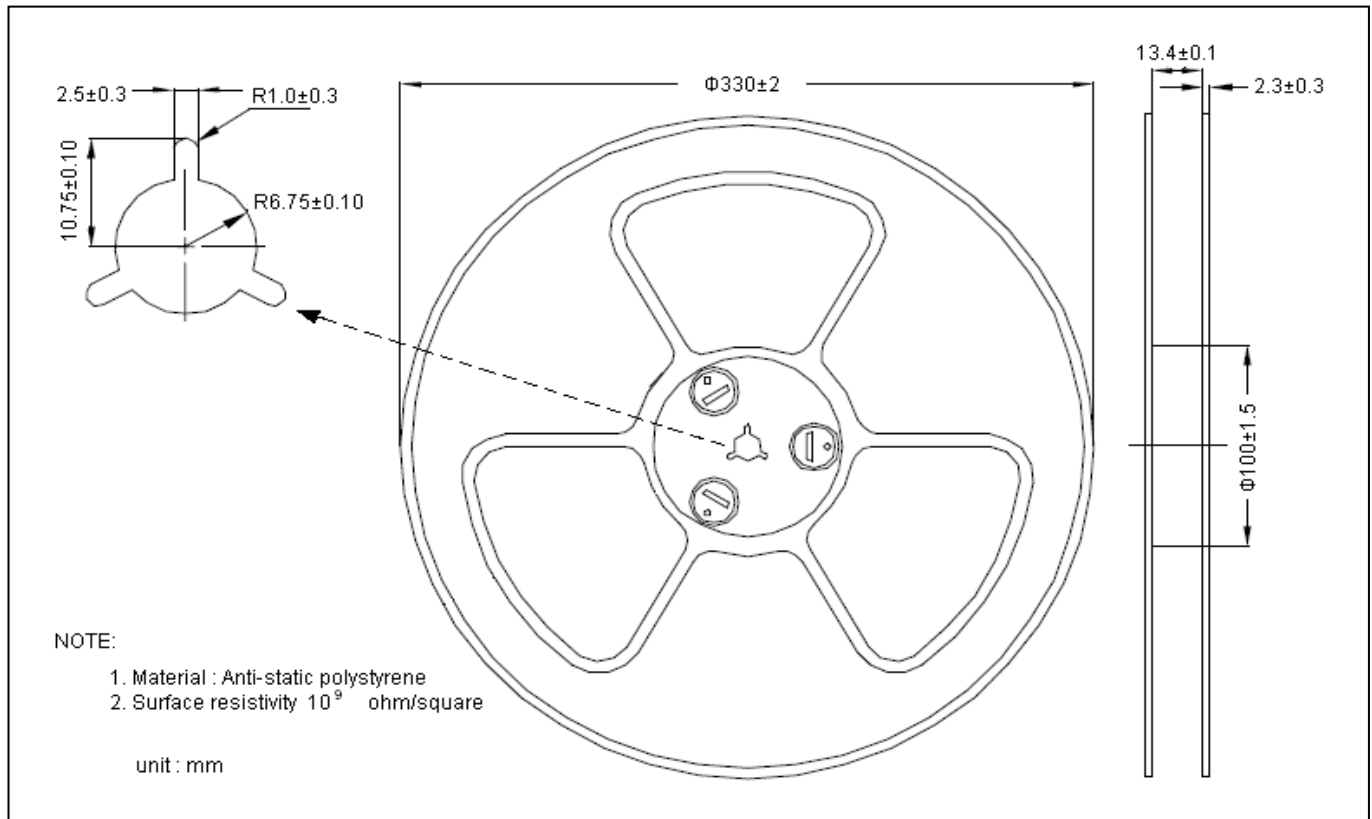
**Typical Characteristics (Cont.)**



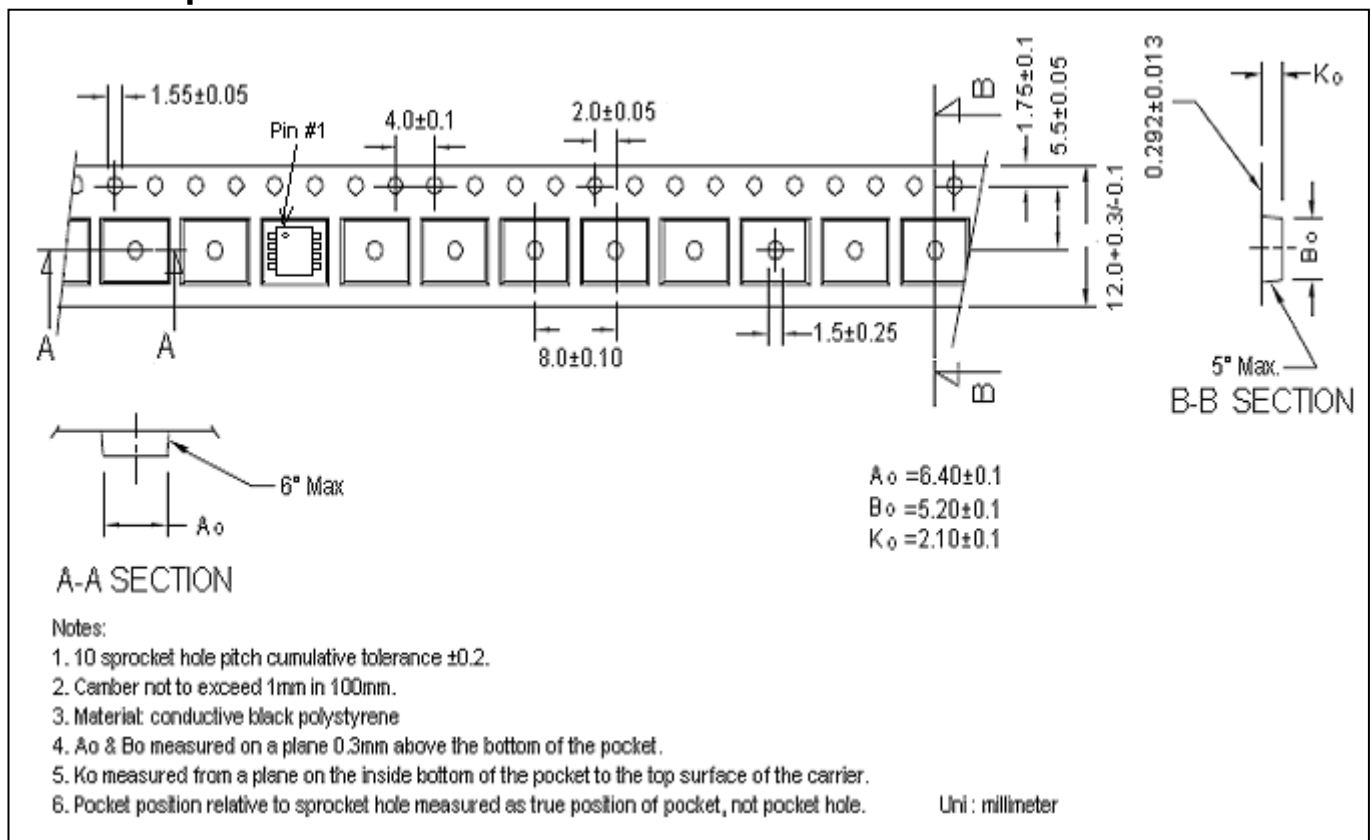
**Recommended Soldering Footprint**



### Reel Dimension



### Carrier Tape Dimension

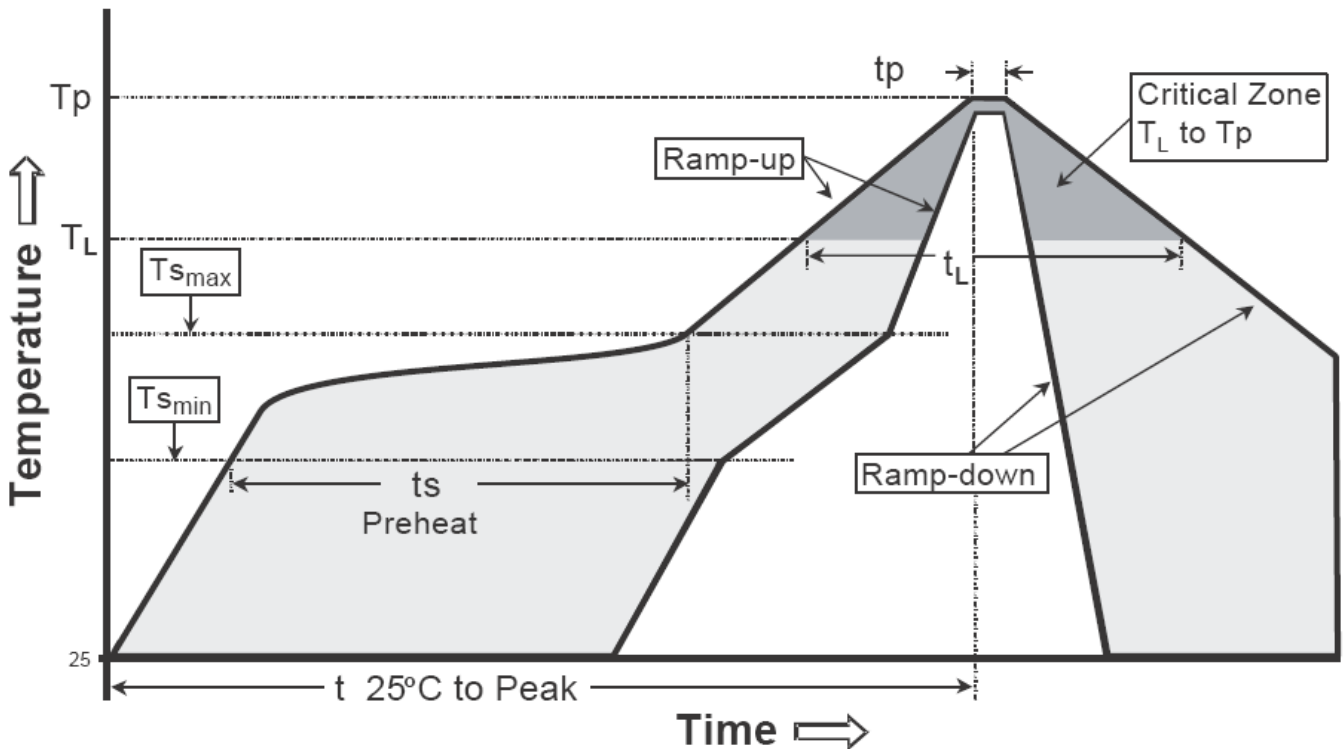




**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

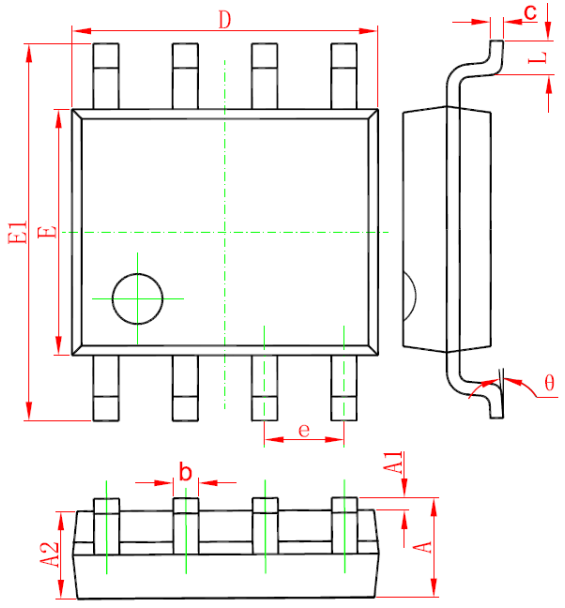
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>P</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

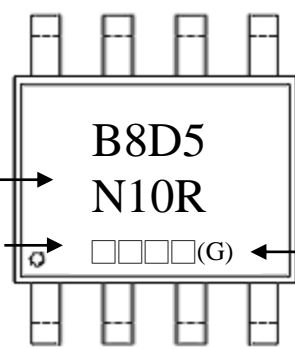
Note : All temperatures refer to topside of the package, measured on the package body surface.

**SOP-8 Dimension**



The diagram shows three views of an 8-lead SOP-8 package: a top view with dimensions D, E, E1, and e; a side view with dimensions c, L, and  $\theta$ ; and a bottom view with dimensions A1, A2, and b.

**Marking:**



The marking diagram shows the top of the package with the text "B8D5" and "N10R". Below "N10R" are four squares representing the date code, followed by "(G)" representing the assembly site code. Arrows point from "Device Code" to "B8D5" and "Date Code" to the squares.

Date Code(counting from left to right) :

1<sup>st</sup> code: year code, the last digit of Christian year  
 2<sup>nd</sup> code : month code, Jan→A, Feb→B, Mar→C, Apr→D  
 May→E, Jun→F, Jul→G, Aug→H, Sep→J,  
 Oct→K, Nov→L, Dec→M  
 3<sup>rd</sup> and 4<sup>th</sup> codes : production serial number, 01~99

Assembly site code : blank→ JCET, G →GEM

**8-Lead SOP-8 Plastic Package**  
 CYStek Package Code: Q8

\*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069	E	3.800	4.000	0.150	0.157
A1	0.100	0.250	0.004	0.010	E1	5.800	6.200	0.228	0.244
A2	1.350	1.550	0.053	0.061	e	*1.270		*0.050	
b	0.330	0.510	0.013	0.020	L	0.400	1.270	0.016	0.050
c	0.170	0.250	0.006	0.010	$\theta$	0°	8°	0°	8°
D	4.700	5.100	0.185	0.200					

Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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