

N-Channel Enhancement Mode MOSFET

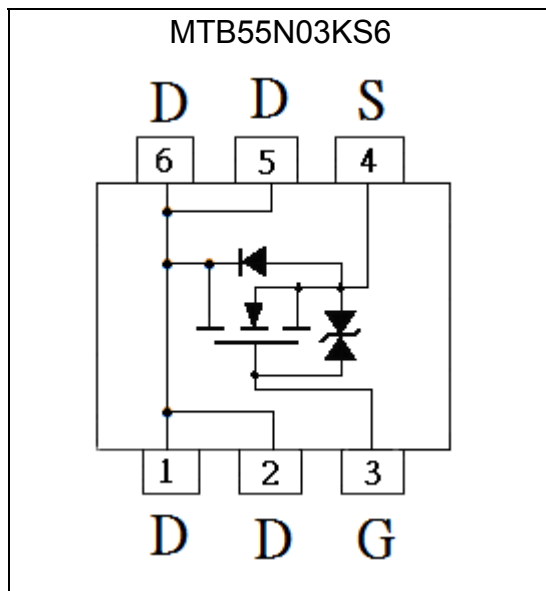
MTB55N03KS6

BV_{DSS}	30V
$I_D@V_{GS}=10V, T_A=25^\circ C$	2.3A
$R_{DSON}@V_{GS}=10V, I_D=2A$	65mΩ (typ)
$R_{DSON}@V_{GS}=4.5V, I_D=1.7A$	80mΩ (typ)

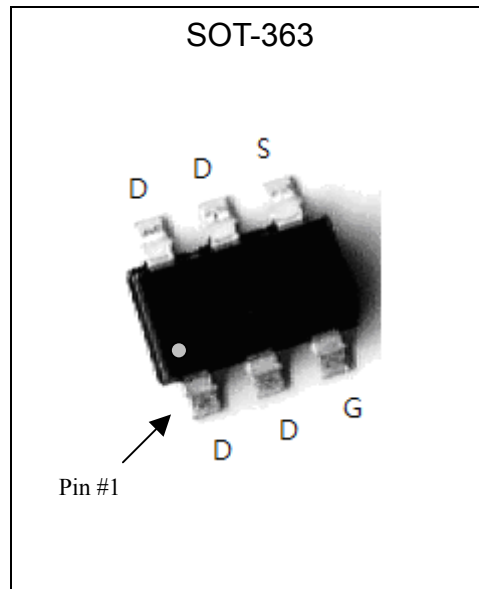
Features

- Low on-resistance
- High speed switching
- ESD protected gate
- Pb-free lead plating and halogen-free package

Equivalent Circuit

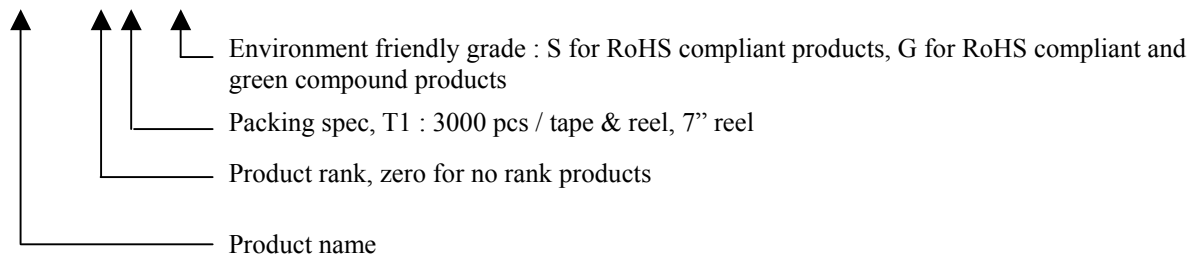


Outline



Ordering Information

Device	Package	Shipping
MTB55N03KS6-0-T1-G	SOT-363 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel





Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @ T _A =25°C, V _{GS} =10V (Note 3)	I _D	2.3	A
Continuous Drain Current @ T _A =70°C, V _{GS} =10V (Note 3)		1.8	
Pulsed Drain Current (Notes 1, 2)	I _{DM}	14	
Maximum Power Dissipation (Note 3)	P _D	750	mW
Maximum Power Dissipation (Note 4)		480	
Operating Junction and Storage Temperature	T _j , T _{stg}	-55~+150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	120	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	R _{θJA}	167	
Thermal Resistance, Junction-to-Ambient (Note 4)		260	

- Note : 1. Pulse width limited by maximum junction temperature.
 2. Pulse width ≤ 300μs, duty cycle ≤ 2%.
 3. Surface mounted on a 1 in² pad of 2 oz. copper.
 4. Surface mounted on a minimum pad.

Electrical Characteristics (T_j=25°C, unless otherwise noted)

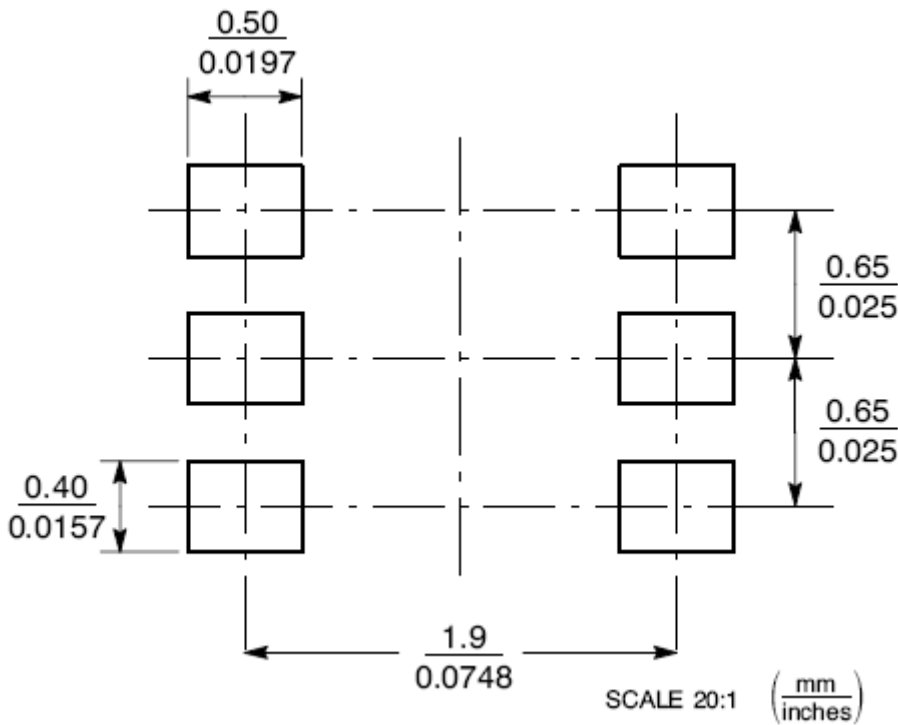
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
B _V D _{SS}	30	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	1	-	2.5		V _{DS} =V _{GS} , I _D =250μA
I _{GS}	-	-	±10	μA	V _{GS} =±16V, V _{DS} =0V
I _{DSS}	-	-	1		V _{DS} =24V, V _{GS} =0V
	-	-	5		V _{DS} =24V, V _{GS} =0V @T _j =55°C
*R _{DS(ON)}	-	65	115	mΩ	V _{GS} =10V, I _D =2A
	-	80	155		V _{GS} =4.5V, I _D =1.7A
*G _{FS}	-	3.3	-	S	V _{DS} =5V, I _D =1A
Dynamic					
C _{iss}	-	166	-	pF	V _{DS} =15V, V _{GS} =0V, f=1MHz
C _{oss}	-	50	-		
C _{rss}	-	32	-		
t _{d(ON)}	-	3.6	-	ns	V _{DS} =15V, I _D =1A, V _{GS} =10V, R _G =6Ω
t _r	-	15	-		
t _{d(OFF)}	-	10.4	-		
t _f	-	4.6	-		



Qg	-	2.8	-	nC	V _{DS} =15V, I _D =2A, V _{GS} =5V
Qgs	-	0.9	-		
Qgd	-	0.9	-		
Source-Drain Diode					
*V _{SD}	-	0.75	1.2	V	V _{GS} =0V, I _S =0.42A
*trr	-	7	-	ns	I _F =1A, dI _F /dt=100A/μs
*Qrr	-	2	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

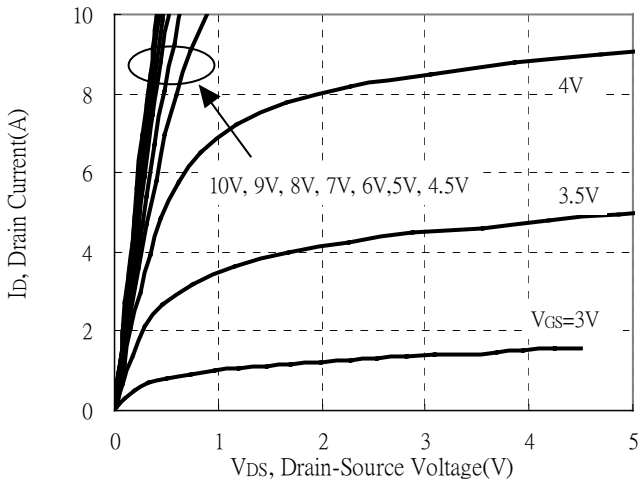
Recommended Soldering Footprint



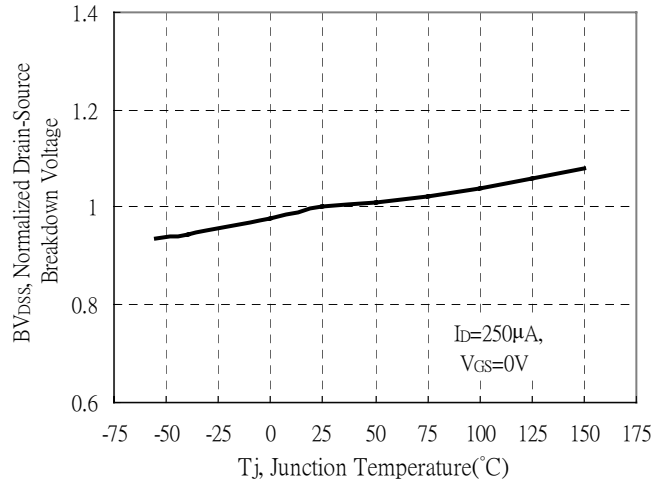


Typical Characteristics

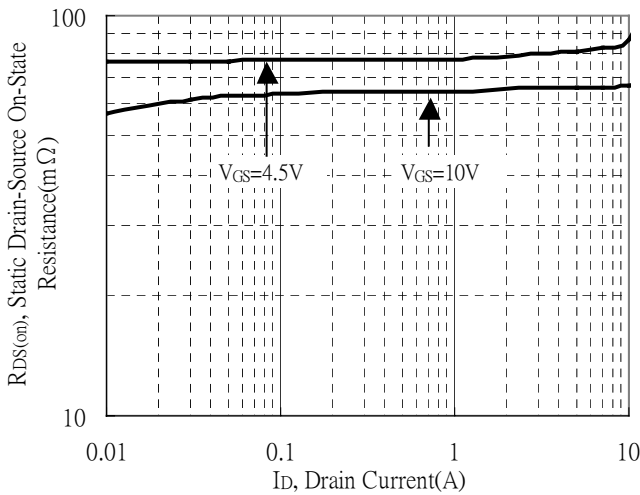
Typical Output Characteristics



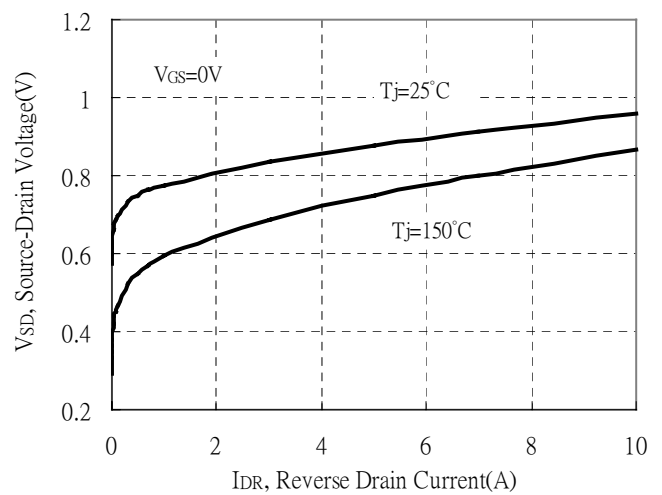
Breakdown Voltage vs Ambient Temperature



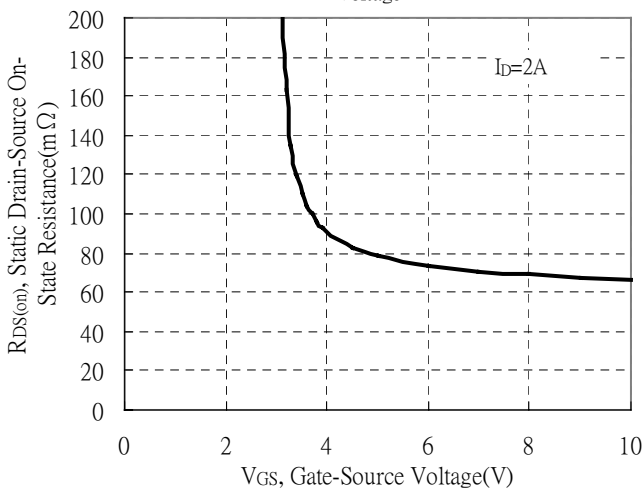
Static Drain-Source On-State resistance vs Drain Current



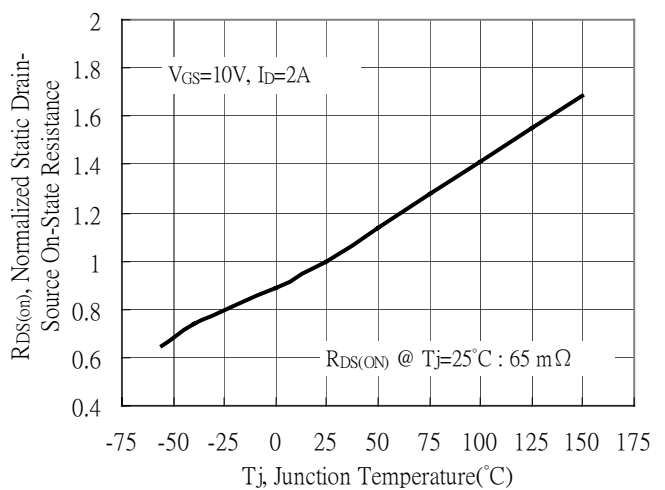
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

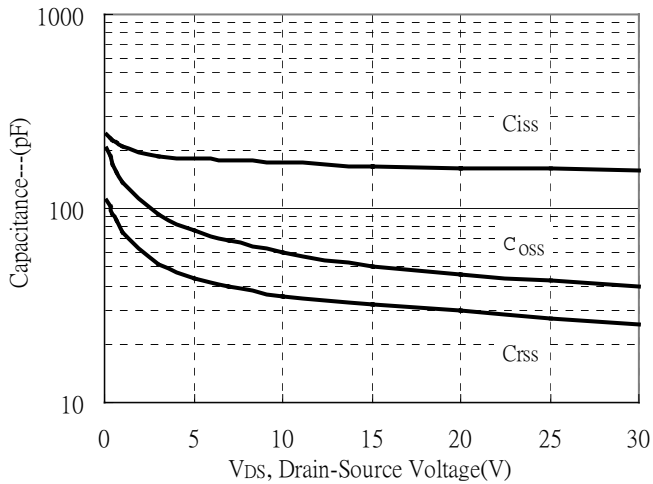


Drain-Source On-State Resistance vs Junction Temperature

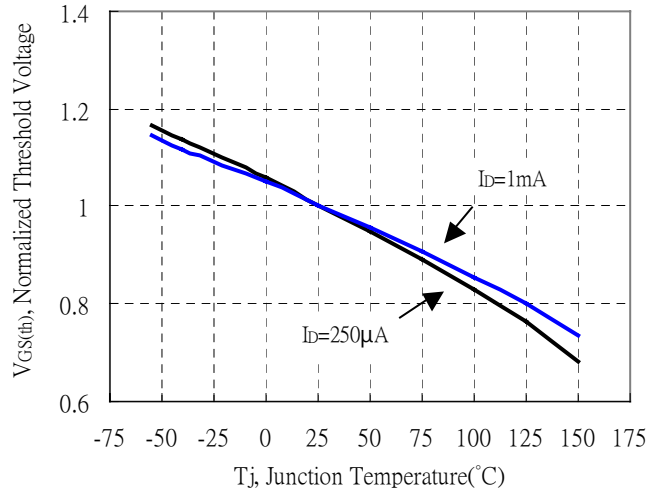


Typical Characteristics(Cont.)

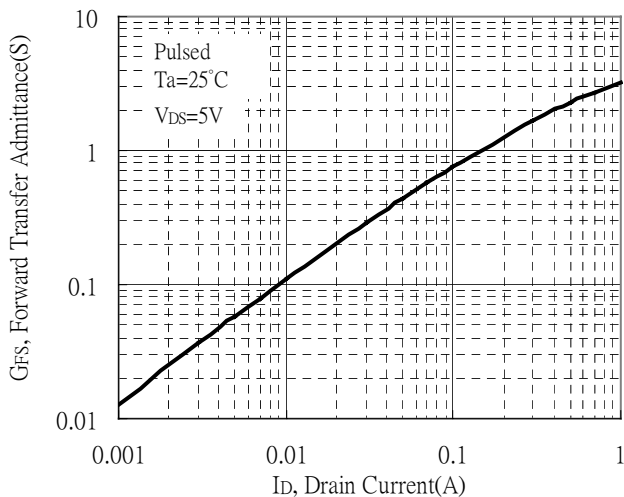
Capacitance vs Drain-to-Source Voltage



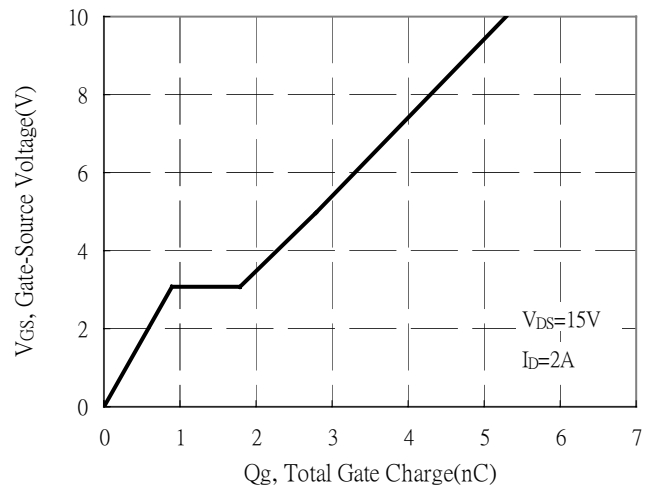
Threshold Voltage vs Junction Temperature



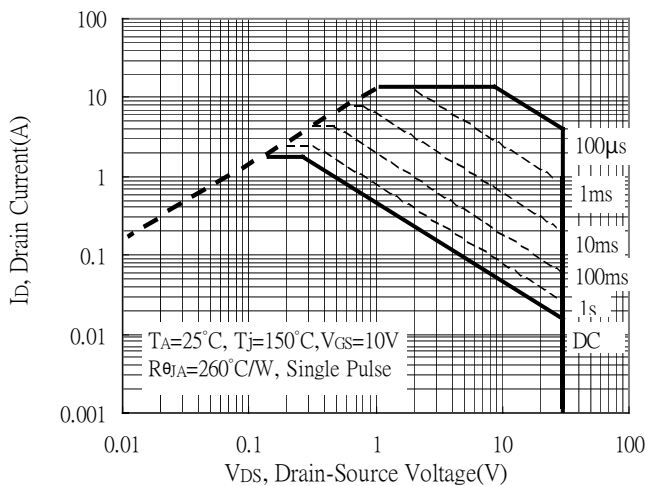
Forward Transfer Admittance vs Drain Current



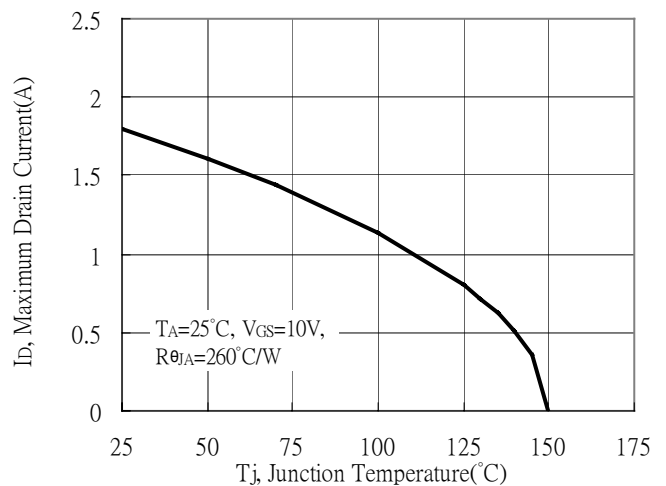
Gate Charge Characteristics



Maximum Safe Operating Area

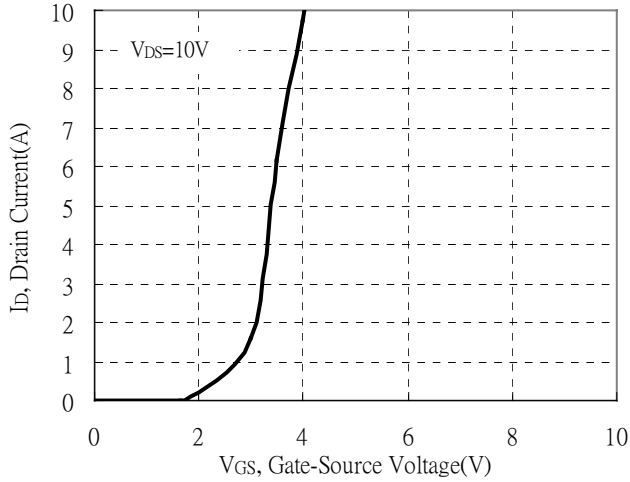


Maximum Drain Current vs Junction Temperature

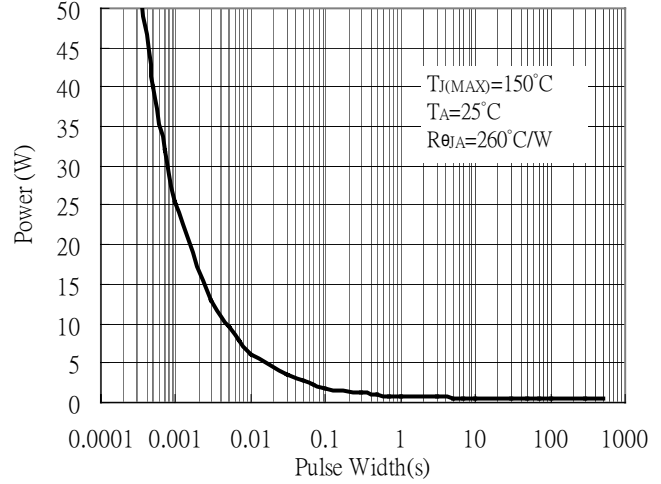


Typical Characteristics(Cont.)

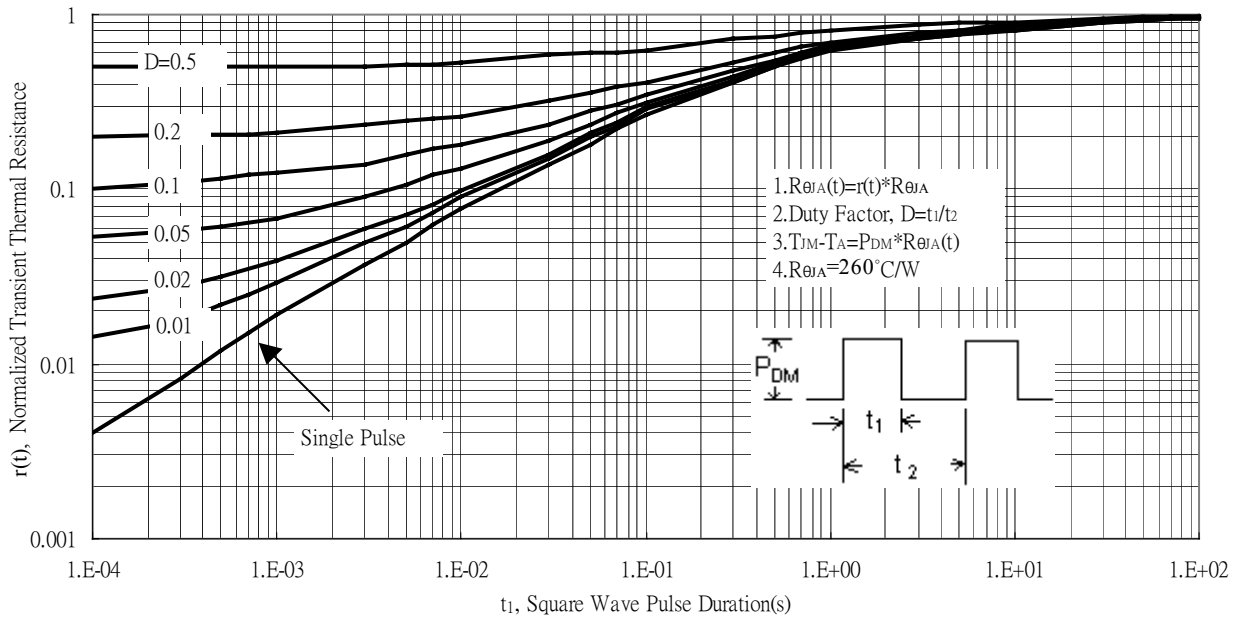
Typical Transfer Characteristics



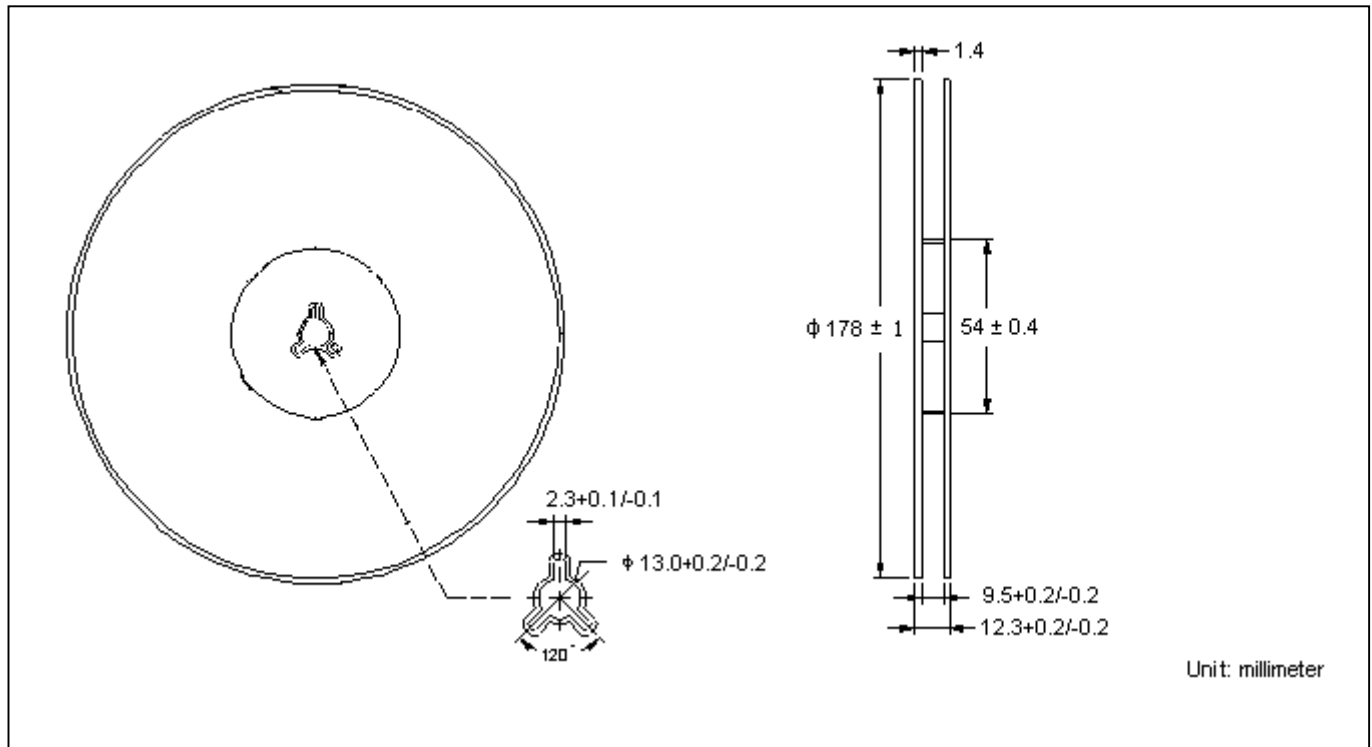
Single Pulse Maximum Power Dissipation, Junction to Ambient



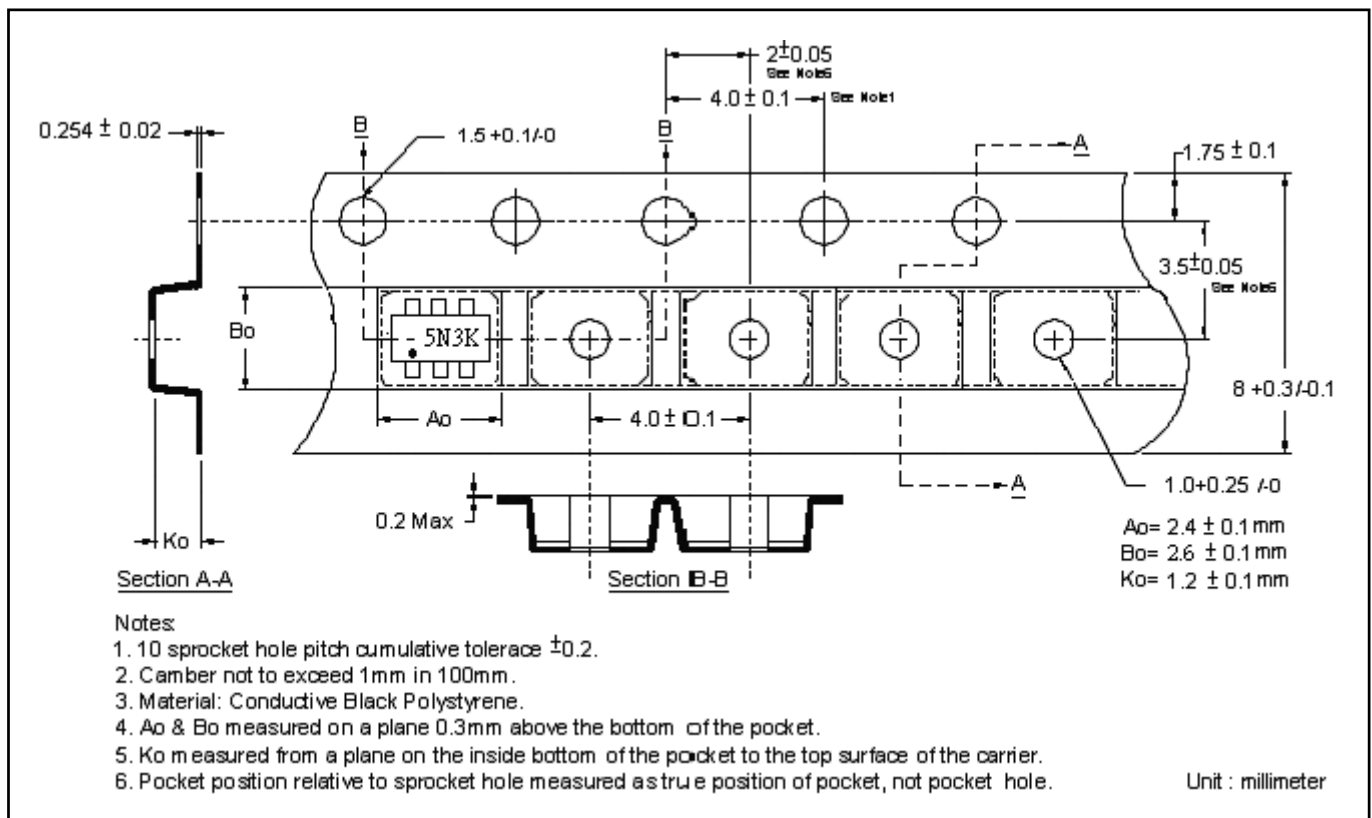
Transient Thermal Response Curves



Reel Dimension



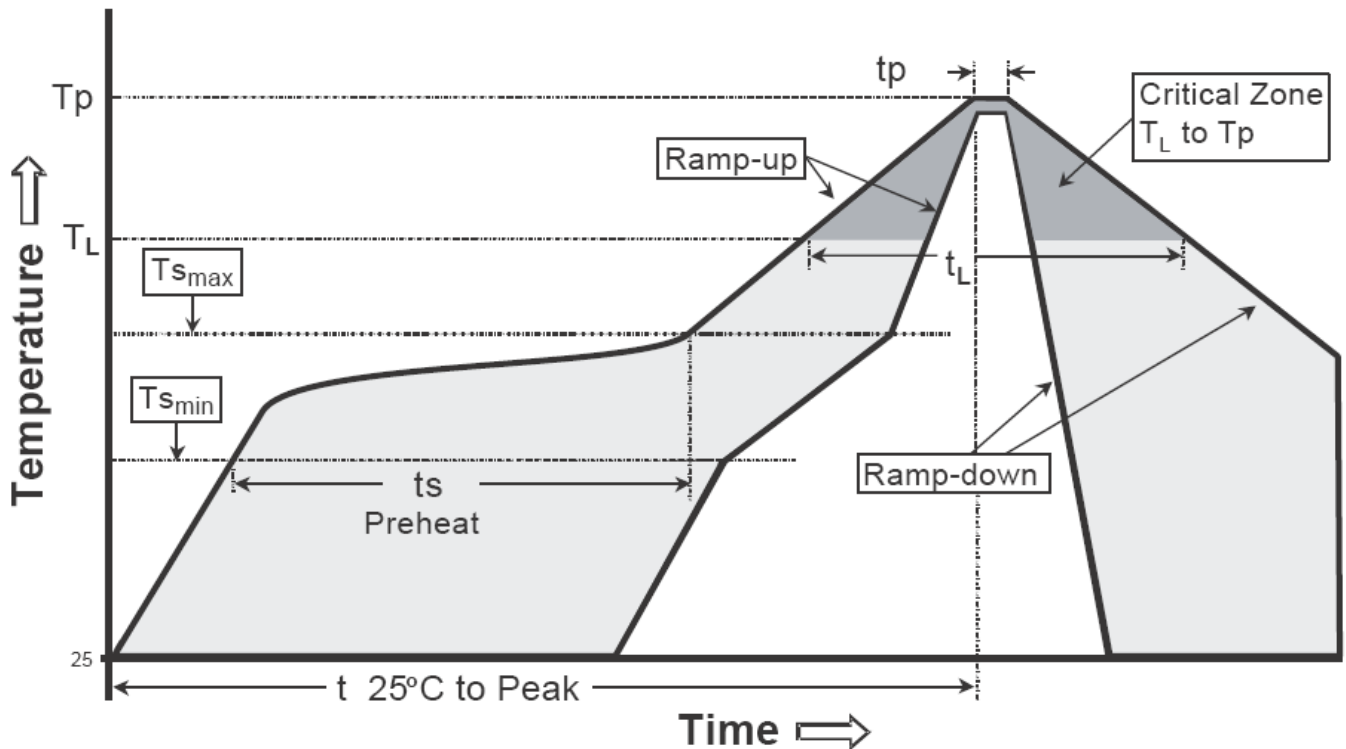
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

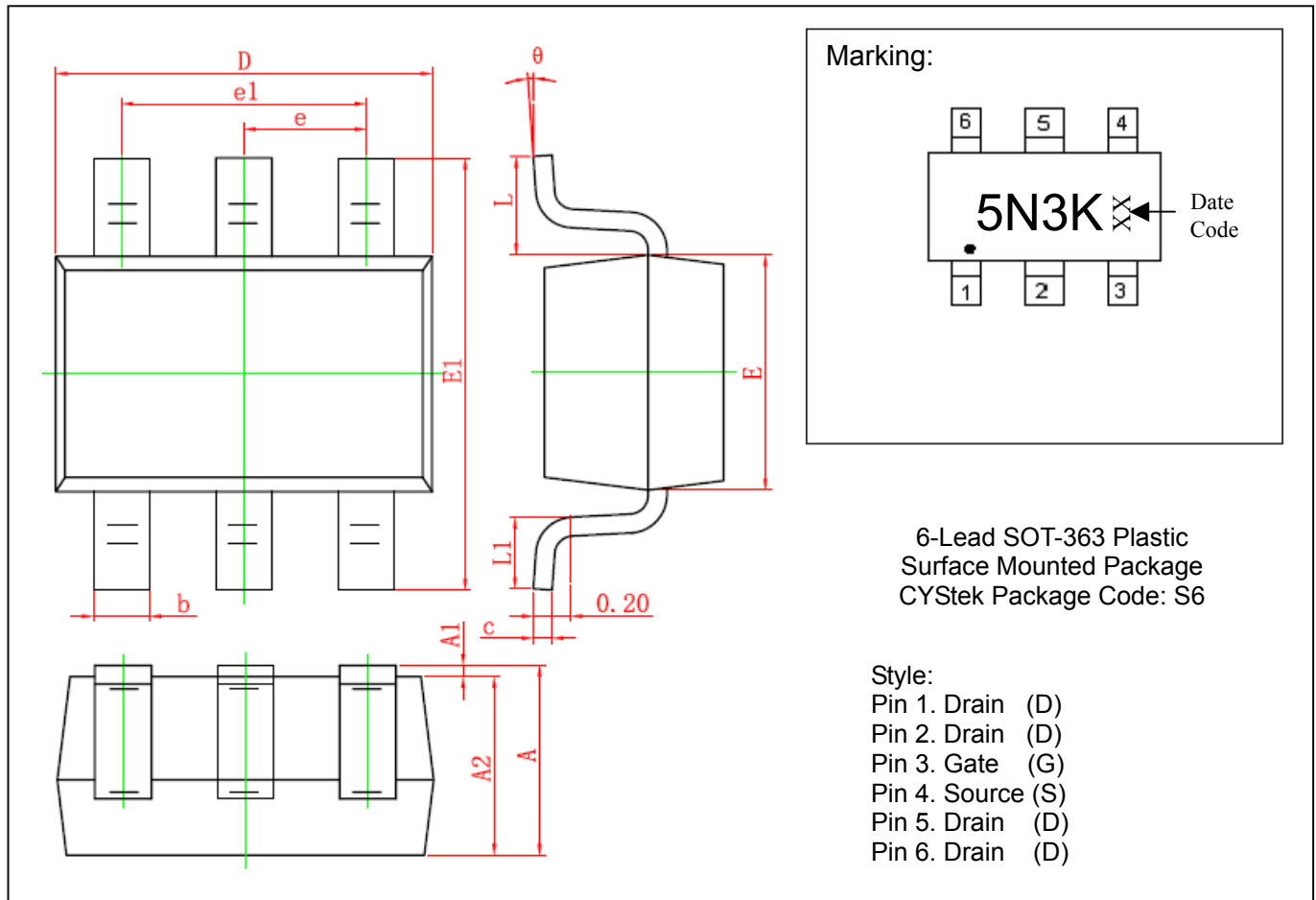
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t _p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note :1. All temperatures refer to topside of the package, measured on the package body surface.
 2.For devices mounted on FR-4 PCB of 1.6mm or equivalent grade PCB. If other grade PCB is used, care should be taken to match the coefficients of thermal expansion between components and PCB. If they are not matched well, the solder joints may crack or the bodies of the parts may crack or shatter as the assembly cools.

SOT-363 Dimension



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043	E1	2.150	2.450	0.085	0.096
A1	0.000	0.100	0.000	0.004	e	0.650	TYP	0.026	TYP
A2	0.900	1.000	0.035	0.039	e1	1.200	1.400	0.047	0.055
b	0.150	0.350	0.006	0.014	L	0.525	REF	0.021	REF
c	0.080	0.150	0.003	0.006	L1	0.260	0.460	0.010	0.018
D	2.000	2.200	0.079	0.087	θ	0°	8°	0°	8°
E	1.150	1.350	0.045	0.053					

Notes : 1.Controlling dimension : millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material :

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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