

P-Channel Logic Level Enhancement Mode Power MOSFET

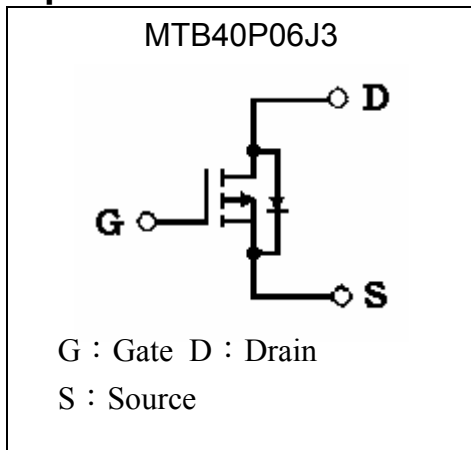
MTB40P06J3

BV_{DSS}	-60V
I_D	-17A
$R_{DSON(MAX)}$	62mΩ

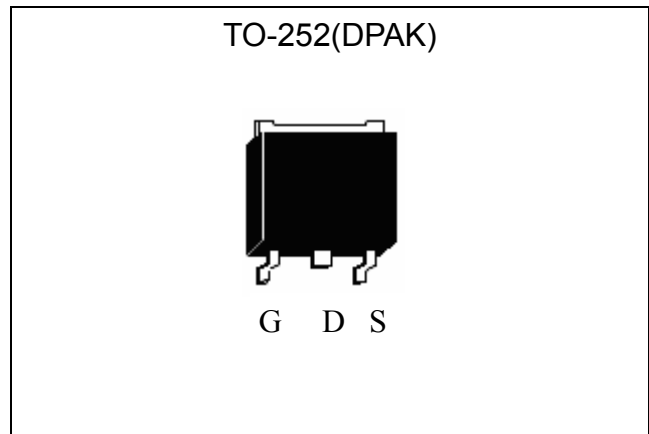
Features

- Low Gate Charge
- Simple Drive Requirement
- Pb-free lead plating & Halogen-free package

Equivalent Circuit

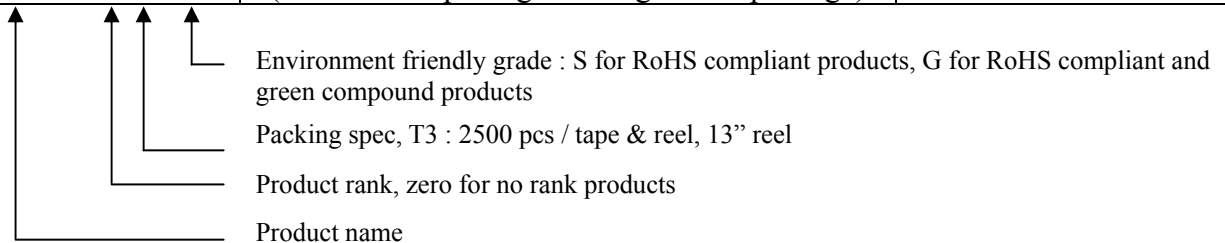


Outline



Ordering Information

Device	Package	Shipping
MTB40P06J3-0-T3-G	TO-252 (Pb-free lead plating & Halogen-free package)	2500 pcs / Tape & Reel





Absolute Maximum Ratings (Tc=25°C, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	
Continuous Drain Current @ Tc=25°C	I _D	-17	A
Continuous Drain Current @ Tc=100°C	I _D	-11	
Pulsed Drain Current *1	I _{DM}	-60	
Avalanche Current	I _{AS}	-12	
Avalanche Energy @ L=0.1mH, I _D =-12A, R _G =25Ω	E _{AS}	7.2	mJ
Repetitive Avalanche Energy @ L=0.05mH *2	E _{AR}	3.6	
Total Power Dissipation @Tc=25°C	P _d	33	W
Total Power Dissipation @Tc=100°C		10	
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+175	°C

Note : *1. Pulse width limited by maximum junction temperature
 *2. Duty cycle ≤ 1%

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	R _{th,j-c}	4.5	°C/W
Thermal Resistance, Junction-to-ambient, max	R _{th,j-a}	110	°C/W

Characteristics (Tc=25°C, unless otherwise specified)

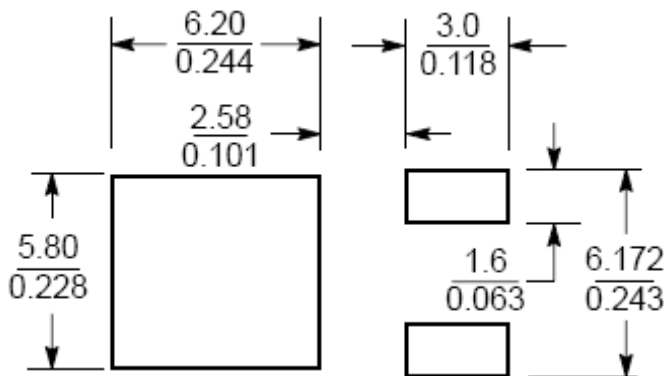
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-60	-	-	V	V _{GS} =0, I _D =-250μA
V _{GS(th)}	-1	-1.8	-3	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±20, V _{DS} =0
I _{DSS}	-	-	-1	μA	V _{DS} =-48V, V _{GS} =0
	-	-	-25	μA	V _{DS} =-40V, V _{GS} =0, T _J =125°C
I _{D(ON)} *1	-17	-	-	A	V _{DS} =-5V, V _{GS} =-4.5V
R _{DS(ON)} *1	-	55	62	mΩ	V _{GS} =-10V, I _D =-15A
	-	75	90	mΩ	V _{GS} =-4.5V, I _D =-7A
G _{FS} *1	-	12	-	S	V _{DS} =-5V, I _D =-15A
Dynamic					
Q _g *1, 2	-	35	-	nC	I _D =-10A, V _{DS} =-30V, V _{GS} =-10V
Q _{gs} *1, 2	-	5	-		
Q _{gd} *1, 2	-	12	-		
t _{d(ON)} *1, 2	-	12	-	ns	V _{DS} =-10V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
t _r *1, 2	-	24	-		
t _{d(OFF)} *1, 2	-	45	-		
t _f *1, 2	-	60	-		



Ciss	-	1450	-	pF	V _{GS} =0V, V _{DS} =-25V, f=1MHz
Coss	-	160	-		
Crss	-	120	-		
Rg	-	7	-	Ω	V _{GS} =15mV, V _{DS} =0, f=1MHz
Source-Drain Diode Ratings and Characteristics					
I _S *1	-	-	-17	A	
I _{SM} *3	-	-	-60		
V _{SD} *1	-	-	-1.3	V	I _F =I _S , V _{GS} =0V
trr	-	12	-	ns	I _F =-5A, dI _F /dt=100A/μs
Qrr	-	9	-	nC	

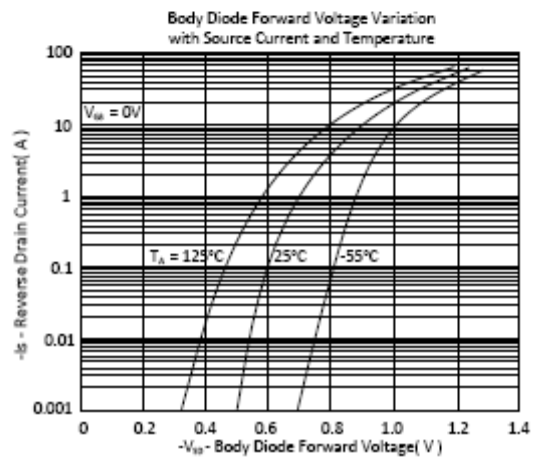
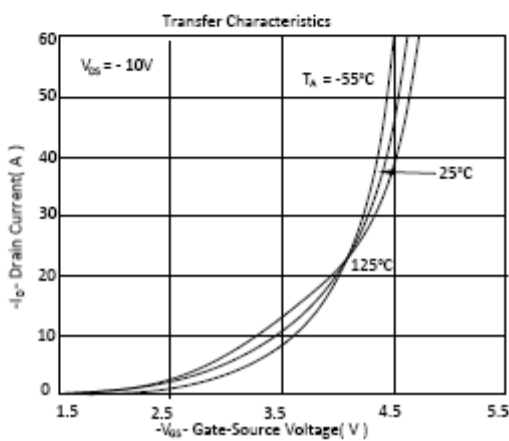
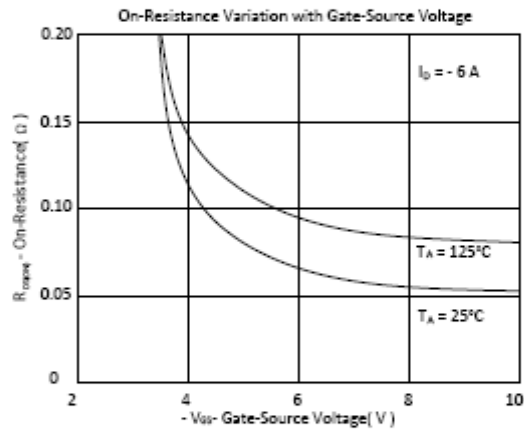
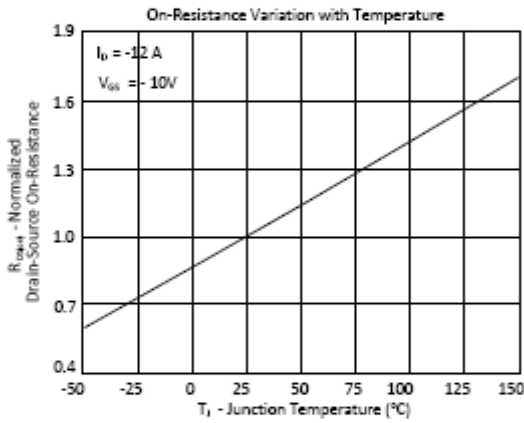
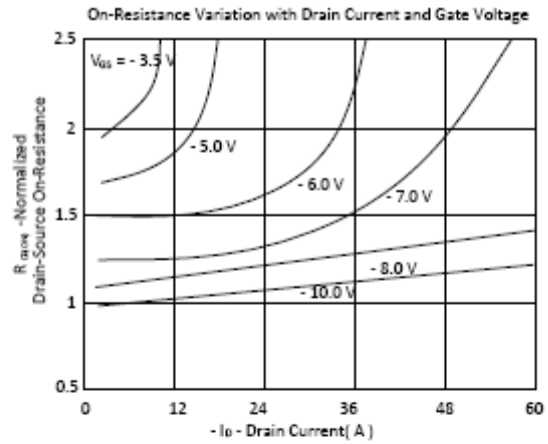
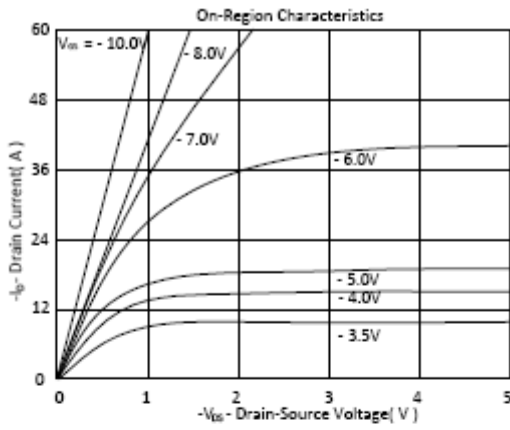
Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
 *2.Independent of operating temperature
 *3.Pulse width limited by maximum junction temperature.

Recommended soldering footprint

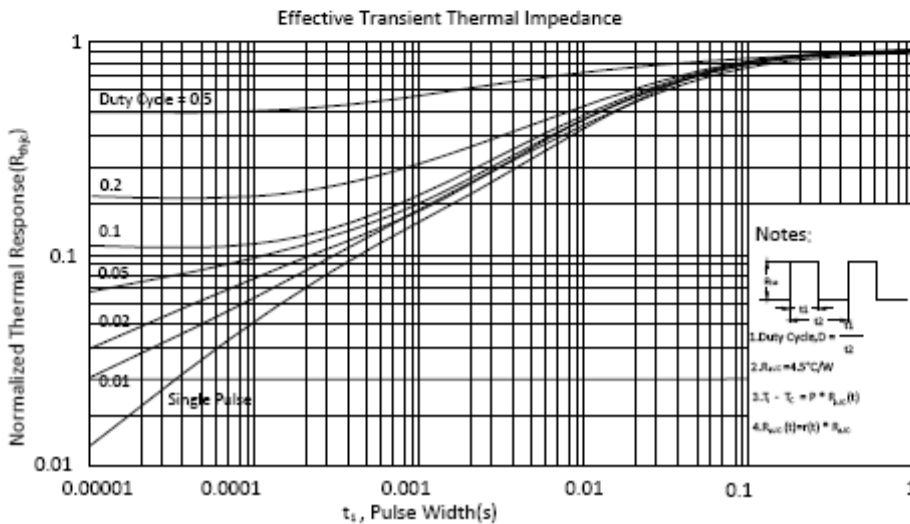
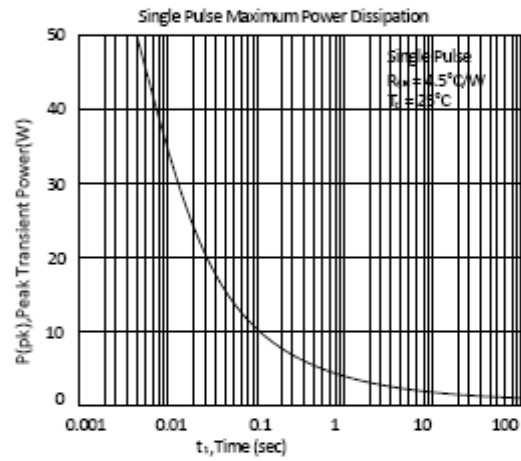
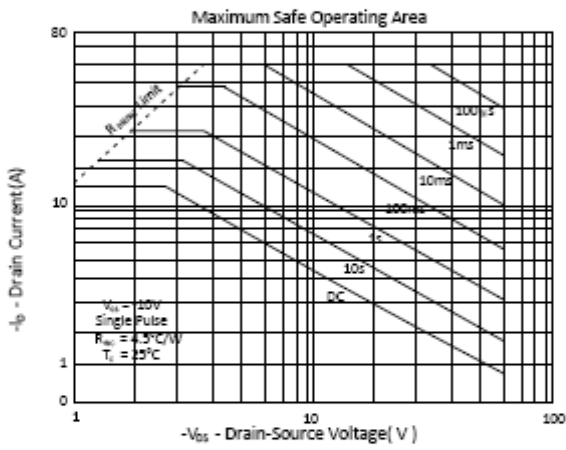
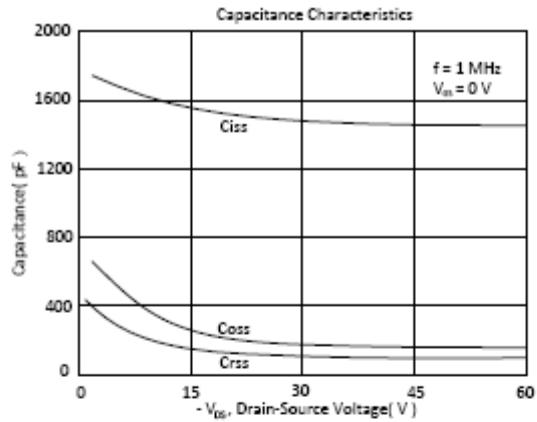
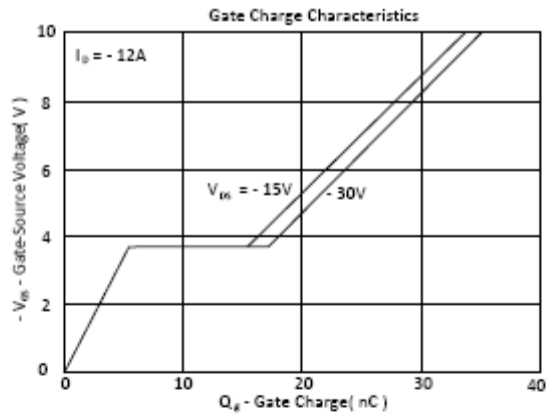


Unit ($\frac{\text{mm}}{\text{inch}}$)

Typical Characteristics



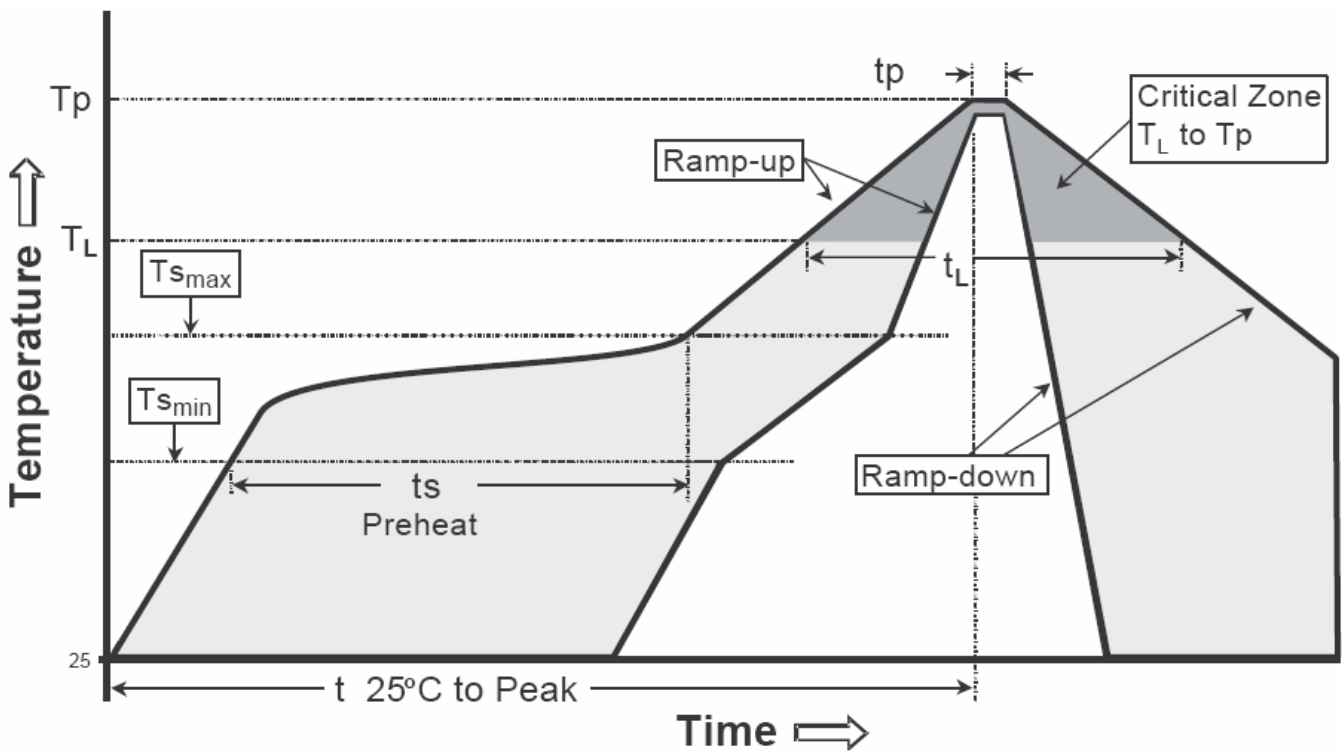
Typical Characteristics (Cont.)



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

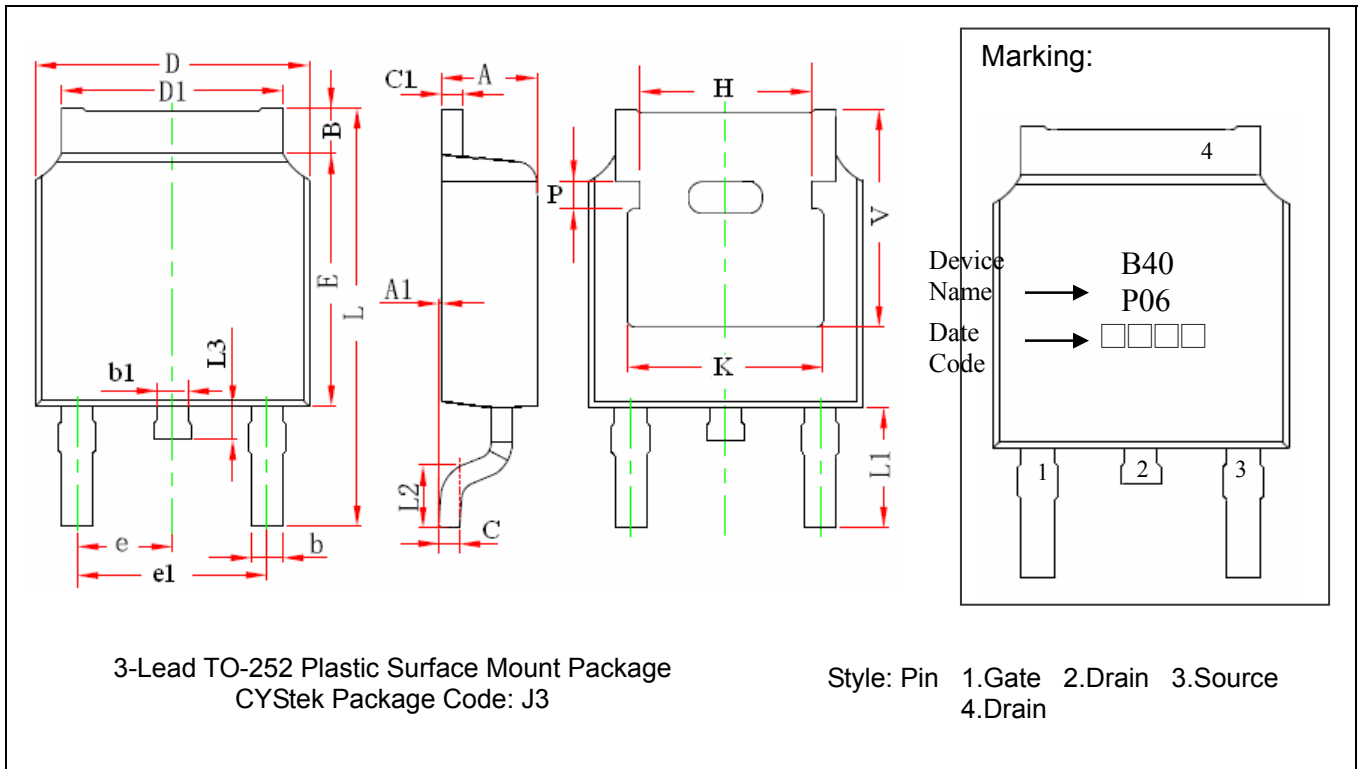
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.087	0.094	2.200	2.400	e	0.086	0.094	2.186	2.386
A1	0.000	0.005	0.000	0.127	e1	0.172	0.188	4.372	4.772
B	0.039	0.048	0.990	1.210	H	0.163	REF	4.140	REF
b	0.026	0.034	0.660	0.860	K	0.190	REF	4.830	REF
b1	0.026	0.034	0.660	0.860	L	0.386	0.409	9.800	10.400
C	0.018	0.023	0.460	0.580	L1	0.114	REF	2.900	REF
C1	0.018	0.023	0.460	0.580	L2	0.055	0.067	1.400	1.700
D	0.256	0.264	6.500	6.700	L3	0.024	0.039	0.600	1.000
D1	0.201	0.215	5.100	5.460	P	0.026	REF	0.650	REF
E	0.236	0.244	6.000	6.200	V	0.211	REF	5.350	REF

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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