

Low Vcesat NPN Epitaxial Planar Transistor

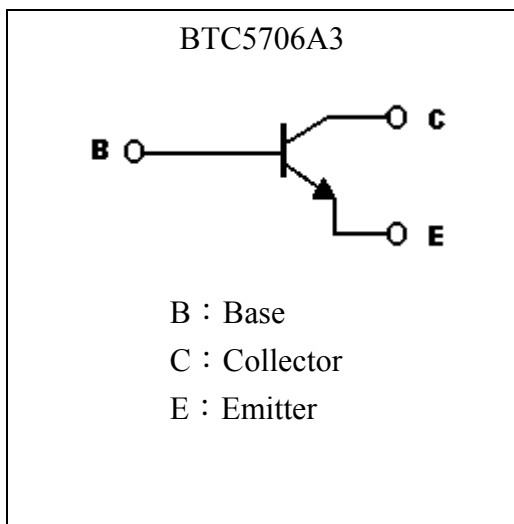
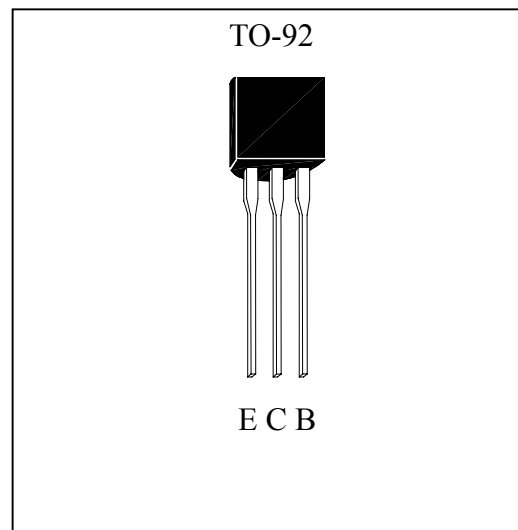
BTC5706A3

Features

- Low collector-to-emitter saturation voltage
- High-speed switching
- High allowable power dissipation
- Large current capability
- Pb-free package

Applications

- DC-DC converter, relay drivers, lamp drivers, motor drivers, strobes.

Symbol**Outline**



Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V _{CBO}	80	V
Collector-Emitter Voltage	V _{CEO}	50	V
Emitter-Base Voltage	V _{EBO}	6	V
Collector Current (DC)	I _C	5	A
Collector Current (Pulse)	I _{CP}	9 (Note 1)	
Base Current	I _B	1.2	A
Power Dissipation @ T _A =25°C	P _D	0.8	W
Thermal Resistance, Junction to Ambient	R _{θJA}	156	°C/W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	-55~+150	°C

Note : 1. Single Pulse , P_w ≤ 380μs, Duty ≤ 2%.

Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	80	-	-	V	I _C =10μA, I _E =0
*BV _{CEO}	50	-	-	V	I _C =1mA, I _B =0
BV _{EBO}	6	-	-	V	I _C =10μA, I _C =0
I _{CBO}	-	-	1	μA	V _{CB} =80V, I _E =0
I _{EBO}	-	-	1	μA	V _{EB} =4V, I _C =0
*V _{CE(sat)} 1	-	110	135	mV	I _C =1A, I _B =50mA
*V _{CE(sat)} 2	-	200	240	mV	I _C =2A, I _B =100mA
*V _{BE(sat)}	-	0.89	1.2	V	I _C =2A, I _B =100mA
*h _{FE} 1	200	-	450	-	V _{CE} =2V, I _C =500mA
*h _{FE} 2	120	-	-	-	V _{CE} =2V, I _C =1A
*h _{FE} 3	80	-	-	-	V _{CE} =2V, I _C =3A
f _T	-	400	-	MHz	V _{CE} =10V, I _C =500mA
C _{ob}	-	15	-	pF	V _{CB} =10V, f=1MHz
t _{on}	-	80	-	ns	V _{CC} =20V, I _C =10I _{B1} =-10I _{B2} =2A, R _L =10Ω
t _{stg}	-	500	-	ns	
t _f	-	60	-	ns	

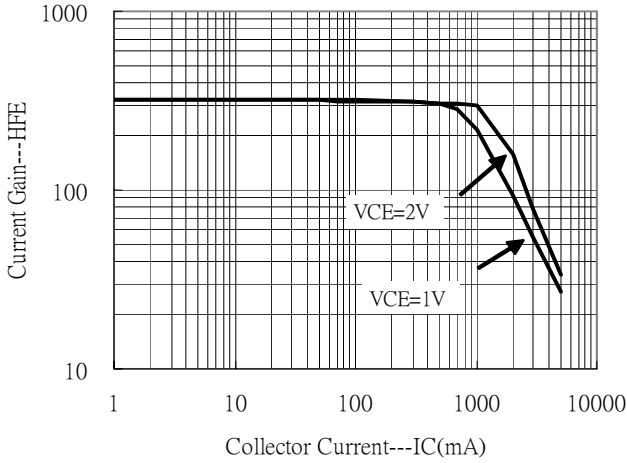
*Pulse Test : Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

Ordering Information

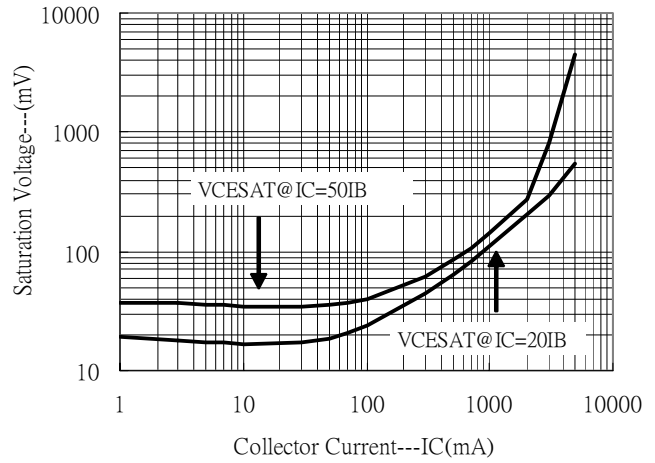
Device	Package	Shipping	Marking
BTC5706A3	TO-92 (Pb-free)	2000 pcs / Tape & Box	C5706

Characteristic Curves

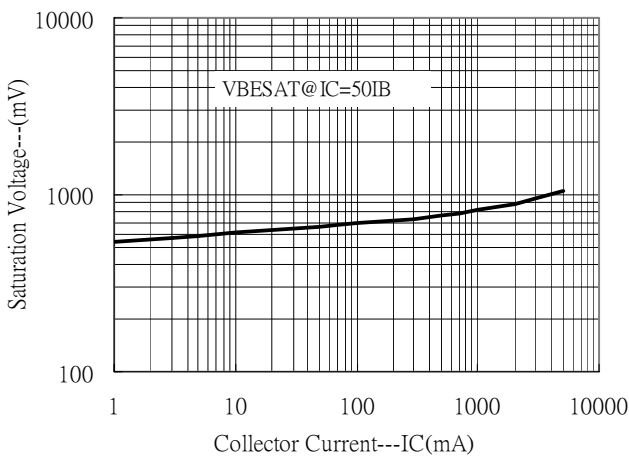
Current Gain vs Collector Current



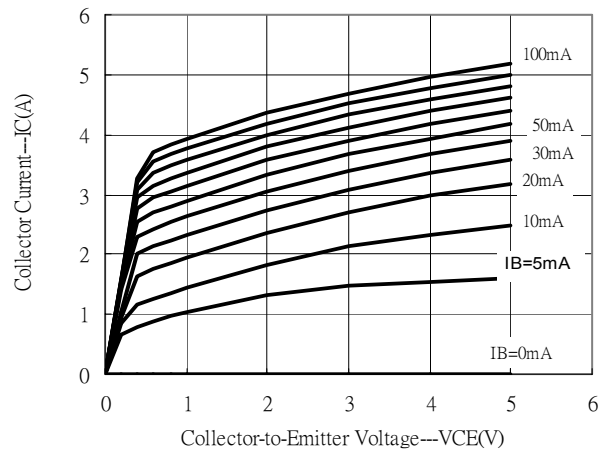
Saturation Voltage vs Collector Current



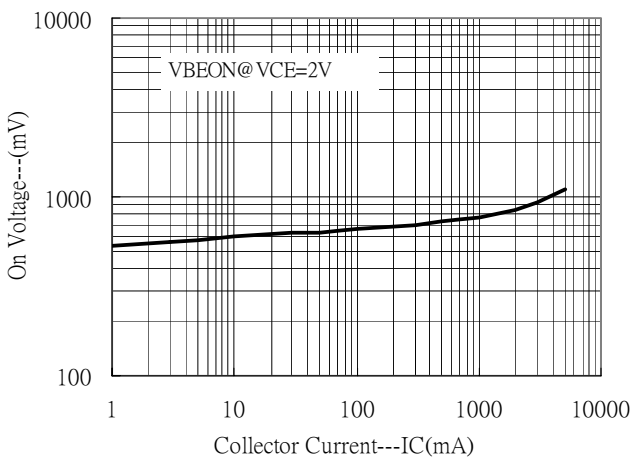
Saturation Voltage vs Collector Current



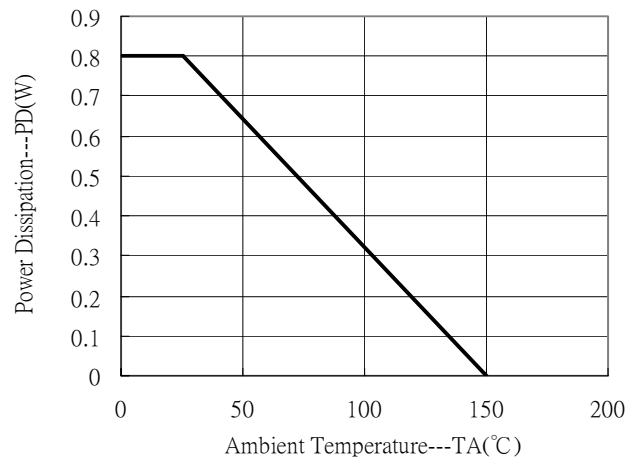
Output Characteristics



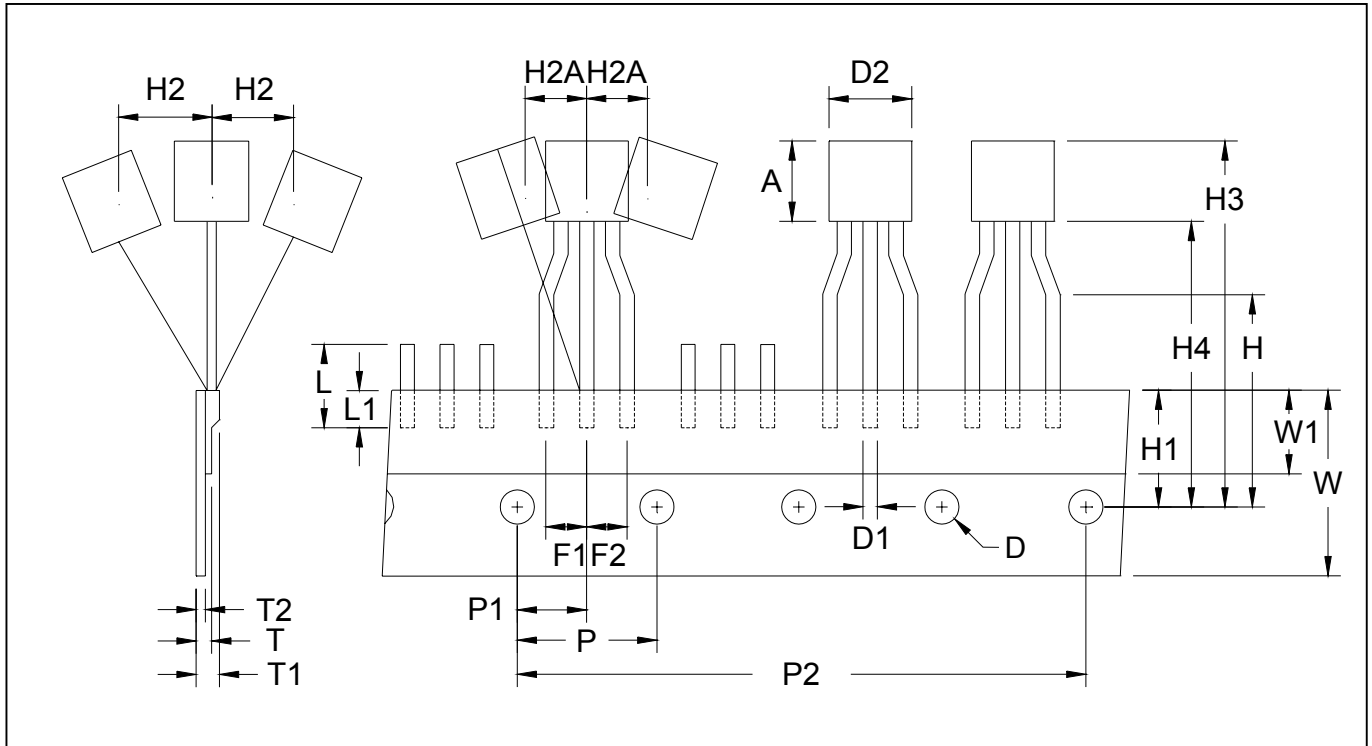
On Voltage vs Collector Current



Power Derating Curve

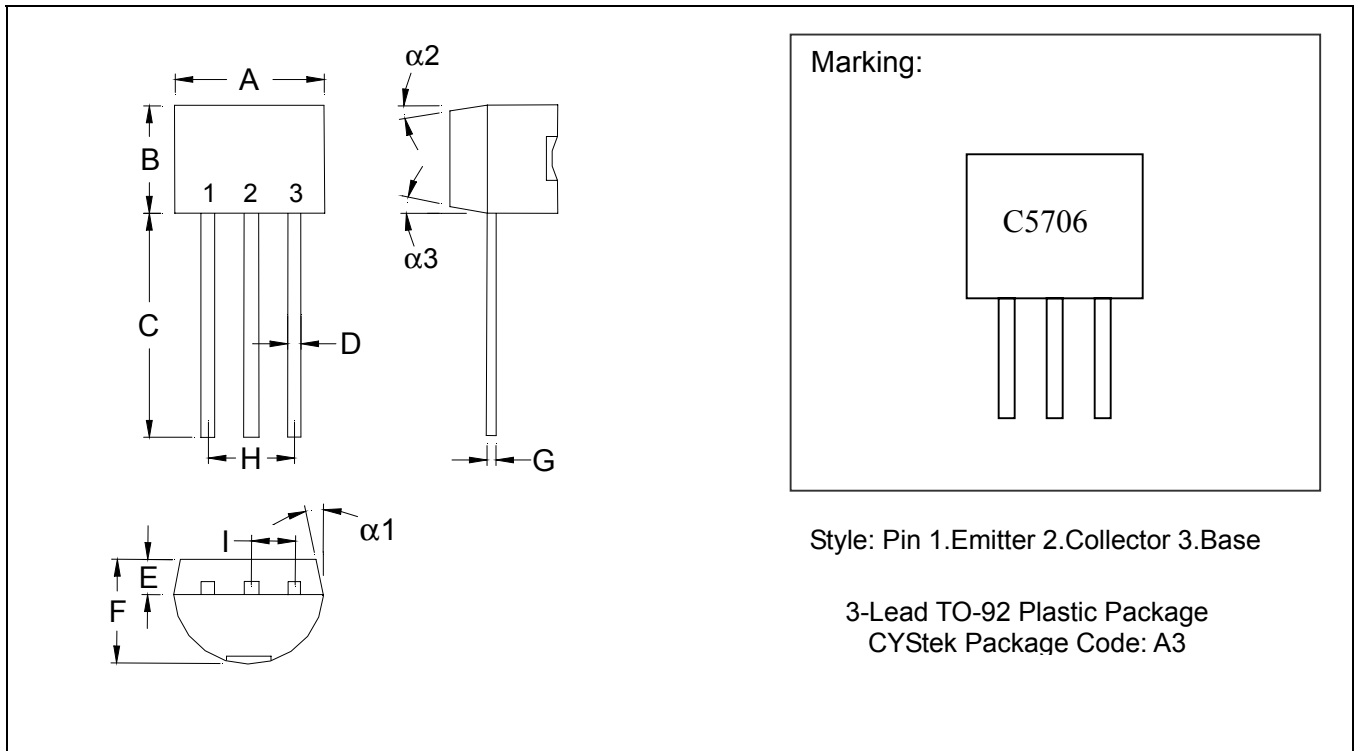


TO-92 Taping Outline



DIM	Item	Millimeters	
		Min.	Max.
A	Component body height	4.33	4.83
D	Tape Feed Diameter	3.80	4.20
D1	Lead Diameter	0.36	0.53
D2	Component Body Diameter	4.33	4.83
F1,F2	Component Lead Pitch	2.40	2.90
F1,F2	F1-F2	-	±0.3
H	Height Of Seating Plane	15.50	16.50
H1	Feed Hole Location	8.50	9.50
H2	Front To Rear Deflection	-	1
H2A	Deflection Left Or Right	-	1
H3	Component Height	-	27
H4	Feed Hole To Bottom Of Component	-	21
L	Lead Length After Component Removal	-	11
L1	Lead Wire Enclosure	2.50	-
P	Feed Hole Pitch	12.50	12.90
P1	Center Of Seating Plane Location	5.95	6.75
P2	4 Feed Hole Pitch	50.30	51.30
T	Over All Tape Thickness	-	0.55
T1	Total Taped Package Thickness	-	1.42
T2	Carrier Tape Thickness	0.36	0.68
W	Tape Width	17.50	19.00
W1	Adhesive Tape Width	5.00	7.00
-	20 pcs Pitch	253	255

TO-92 Dimension



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1704	0.1902	4.33	4.83	G	0.0142	0.0220	0.36	0.56
B	0.1704	0.1902	4.33	4.83	H	-	*0.1000	-	*2.54
C	0.5000	-	12.70	-	I	-	*0.0500	-	*1.27
D	0.0142	0.0220	0.36	0.56	α1	-	*5°	-	*5°
E	-	*0.0500	-	*1.27	α2	-	*2°	-	*2°
F	0.1323	0.1480	3.36	3.76	α3	-	*2°	-	*2°

Notes: 1. Controlling dimension: millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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