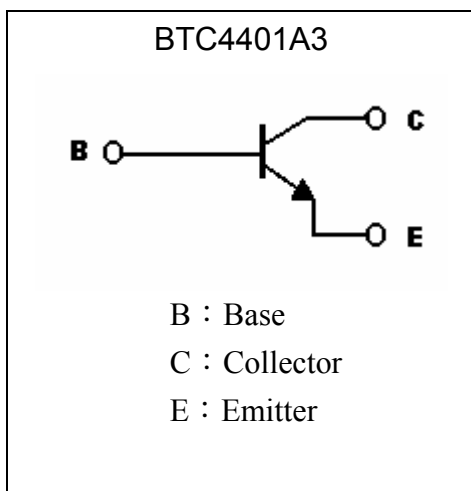
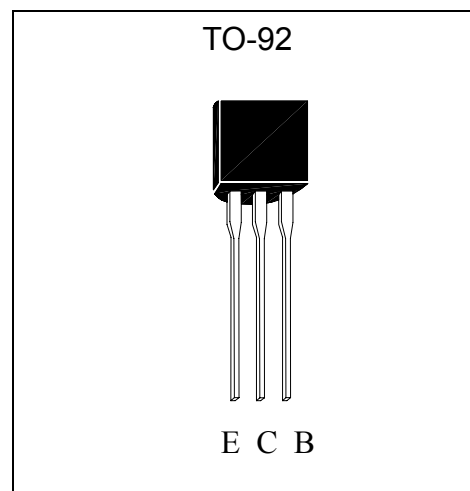


General Purpose NPN Epitaxial Planar Transistor

BTC4401A3

Description

- The BTC4401A3 is designed for using in driver stage of AF amplifier and general purpose switching application.
- High current , $I_C = 0.6A$
- Low $V_{CE(sat)}$, $V_{CE(sat)} = 0.2V$ (typ.) at $I_C/I_B = 500mA/50mA$
Optimal for low Voltage operation
- Complementary to BTA4403A3.
- Pb-free package

Symbol

Outline

Absolute Maximum Ratings ($T_a=25^\circ C$)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V_{CBO}	75	V
Collector-Emitter Voltage	V_{CEO}	50	V
Emitter-Base Voltage	V_{EBO}	6	V
Collector Current	I_C	0.6	A
Power Dissipation	P_d	625	mW
Junction Temperature	T_j	150	$^\circ C$
Storage Temperature	T_{stg}	-55~+150	$^\circ C$

**Characteristics (Ta=25°C)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CBO}	75	-	-	V	I _C =100μA
BV _{CEO}	50	-	-	V	I _C =1mA
BV _{EBO}	6	-	-	V	I _E =10μA
I _{CBO}	-	-	10	nA	V _{CB} =60V
I _{CEX}	-	-	100	nA	V _{CE} =60V, V _{BE} =-0.4V
I _{EBO}	-	-	50	nA	V _{EB} =5V
*V _{CE(sat)1}	-	-	0.3	V	I _C =150mA, I _B =15mA
*V _{CE(sat)2}	-	0.2	0.45	V	I _C =500mA, I _B =50mA
*V _{BE(sat)1}	-	-	0.95	V	I _C =150mA, I _B =15mA
*V _{BE(sat)2}	-	-	1.2	V	I _C =500mA, I _B =50mA
*V _{BE(ON)}	-	-	1	V	V _{CE} =1V, I _C =20mA
*h _{FE1}	100	-	-	-	V _{CE} =2V, I _C =0.1mA
*h _{FE2}	120	-	240	-	V _{CE} =2V, I _C =1mA
*h _{FE3}	120	-	240	-	V _{CE} =2V, I _C =50mA
*h _{FE4}	40	-	-	-	V _{CE} =2V, I _C =500mA
f _T	-	230	-	MHz	V _{CE} =5V, I _C =20mA, f=100MHz
Cob	-	9.3	-	pF	V _{CB} =5V, f=1MHz

*Pulse Test: Pulse Width ≤380μs, Duty Cycle≤2%

Classification Of h_{FE} 2

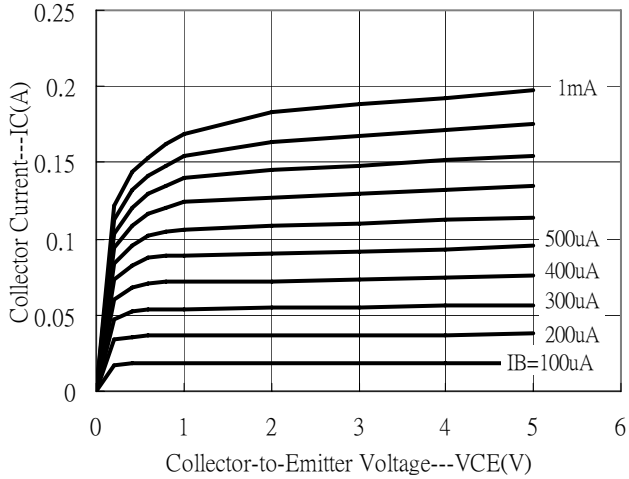
Rank	A	B1	B2	C
Range	120~175	165~195	185~225	215~240

Ordering Information

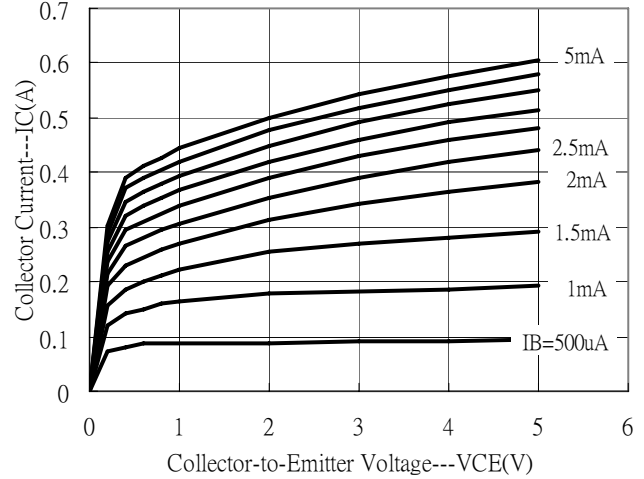
Device	Package	Shipping
BTC4401A3, bulk	TO-92 (Pb-free lead plating package)	1000 pcs/ bag, 10 bags/box, 10boxes/carton
BTC4401A3, T/B	TO-92 (Pb-free lead plating package)	2000 pcs / Tape & Box

Typical Characteristics

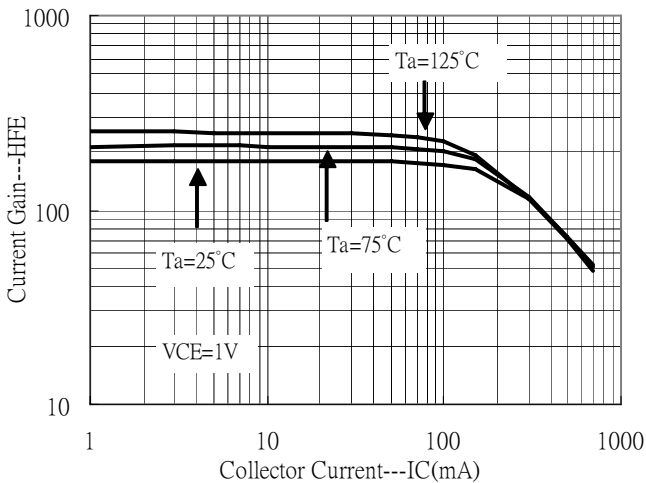
Emitter Grounded Output Characteristics



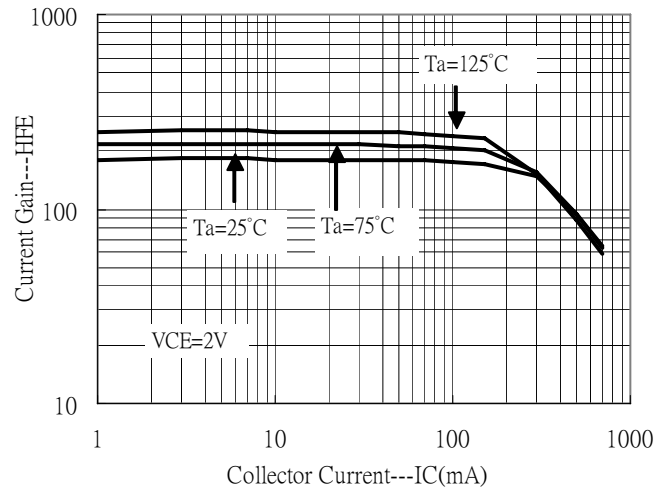
Emitter Grounded Output Characteristics



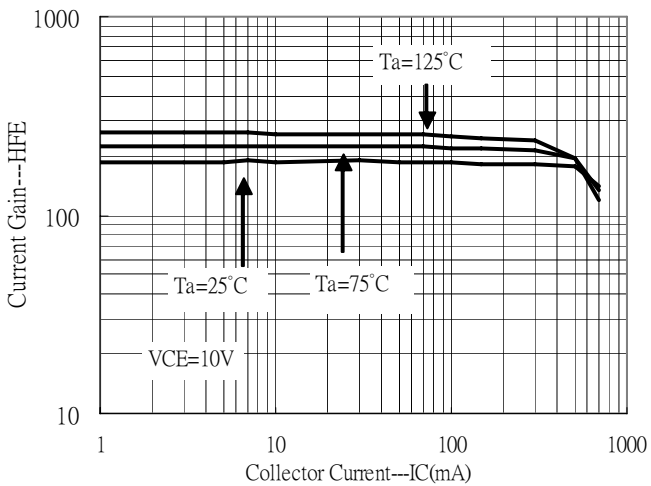
Current Gain vs Collector Current



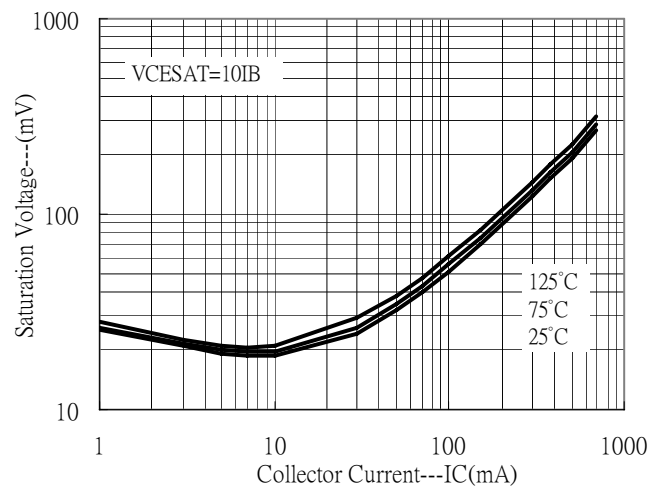
Current Gain vs Collector Current



Current Gain vs Collector Current

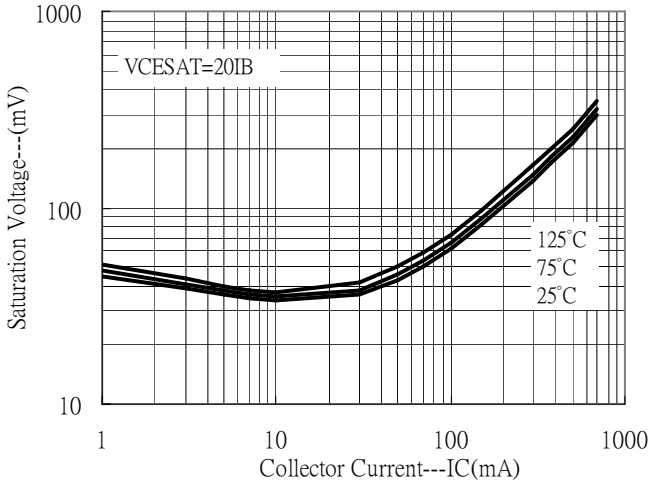


Saturation Voltage vs Collector Current

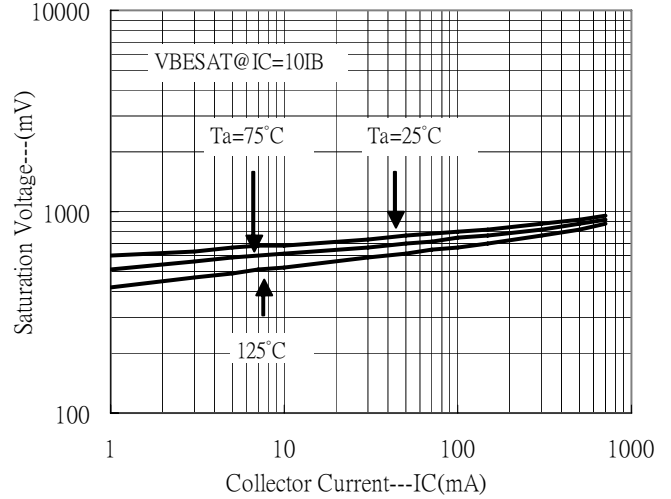


Typical Characteristics(Cont.)

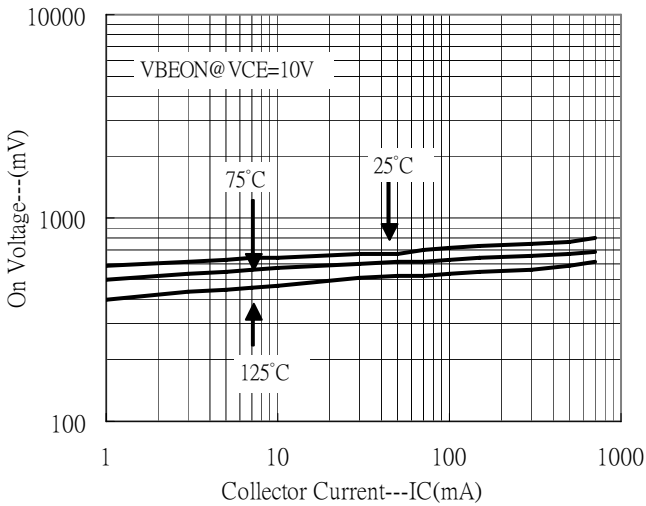
Saturation Voltage vs Collector Current



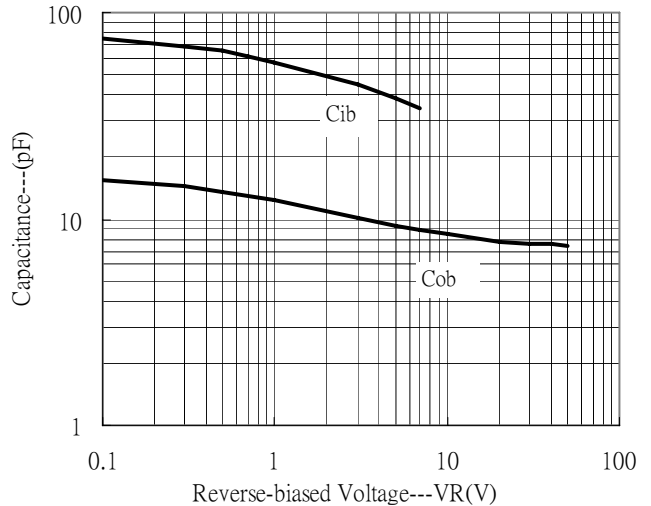
Saturation Voltage vs Collector Current



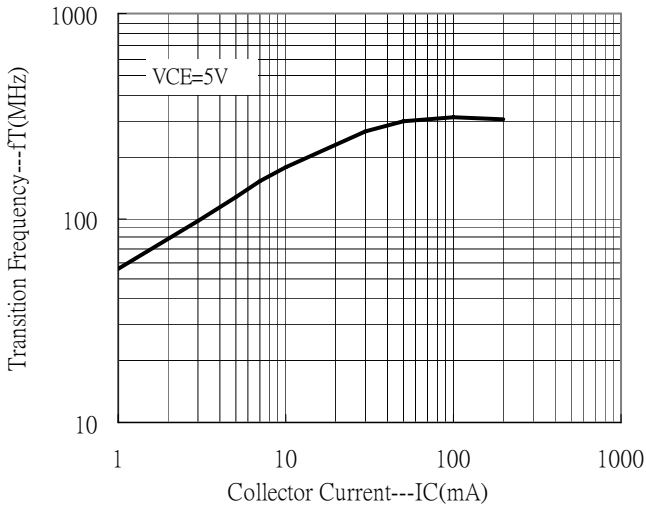
On Voltage vs Collector Current



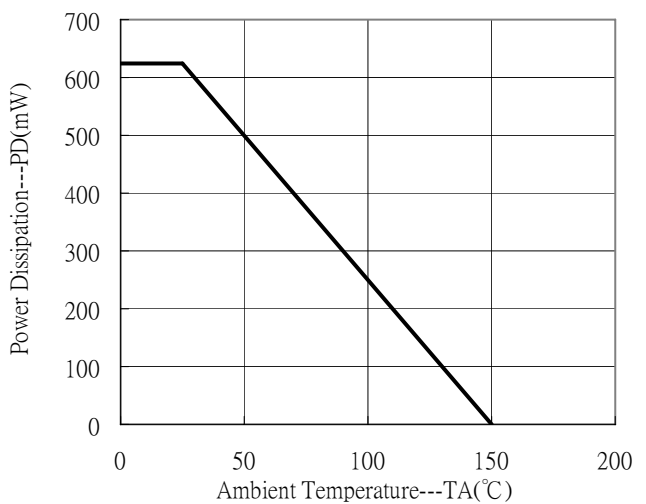
Capacitance vs Reverse-biased Voltage



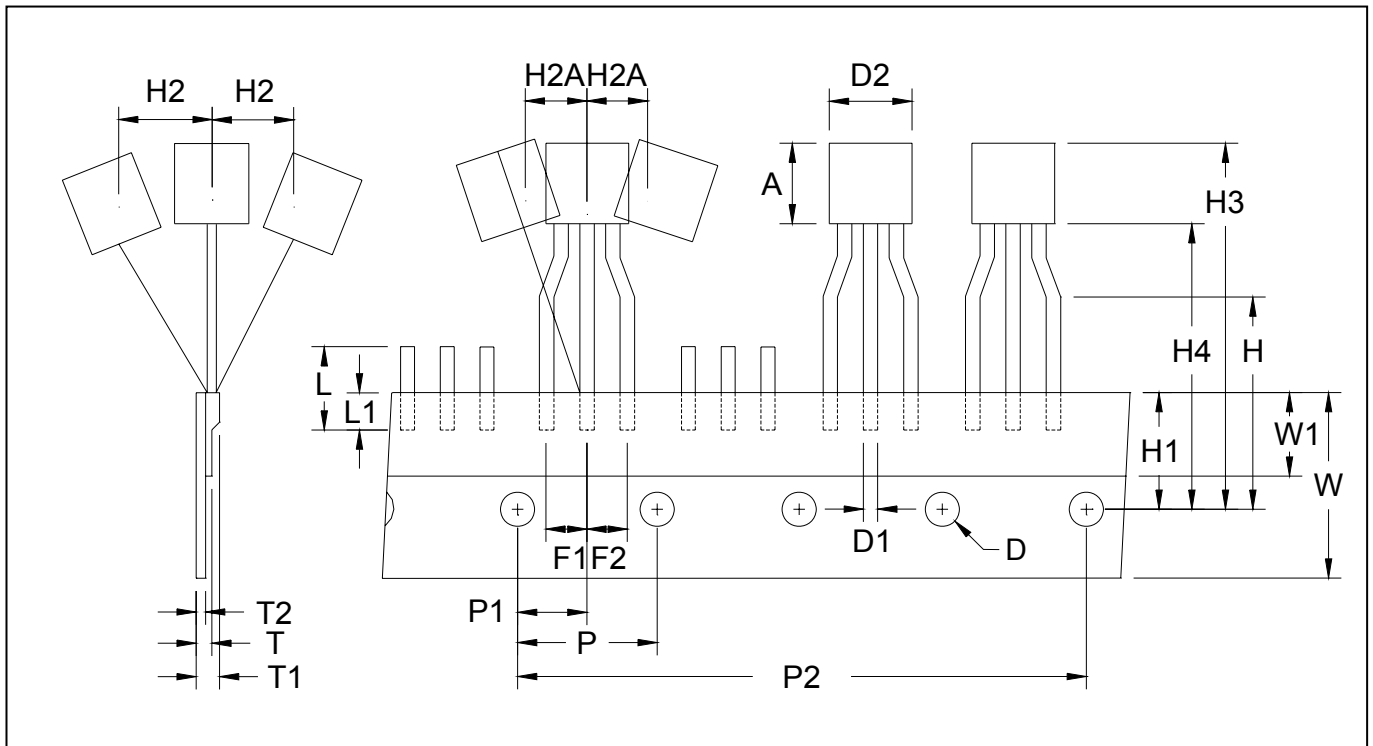
Transition Frequency vs Collector Current



Power Derating Curve



TO-92 Taping Outline

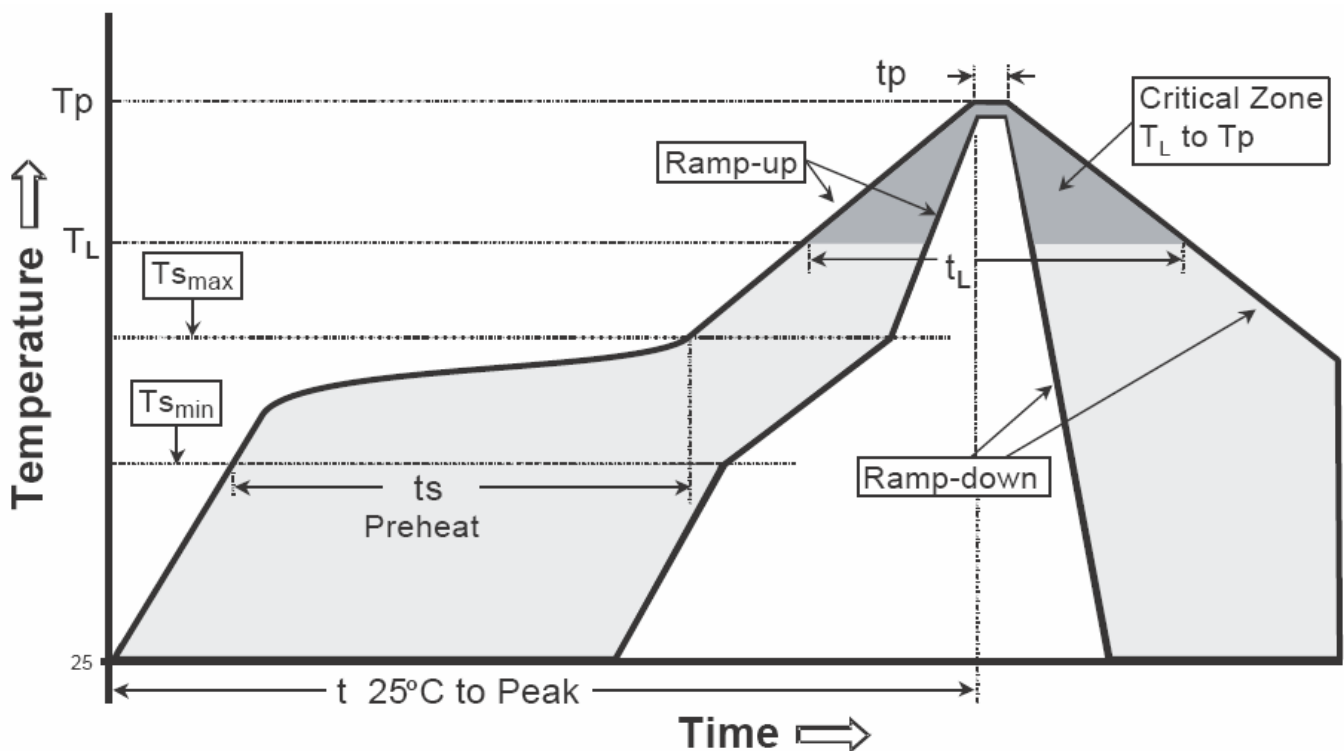


DIM	Item	Millimeters	
		Min.	Max.
A	Component body height	4.33	4.83
D	Tape Feed Diameter	3.80	4.20
D1	Lead Diameter	0.36	0.53
D2	Component Body Diameter	4.33	4.83
F1,F2	Component Lead Pitch	2.40	2.90
F1,F2	F1-F2	-	±0.3
H	Height Of Seating Plane	15.50	16.50
H1	Feed Hole Location	8.50	9.50
H2	Front To Rear Deflection	-	1
H2A	Deflection Left Or Right	-	1
H3	Component Height	-	27
H4	Feed Hole To Bottom Of Component	-	21
L	Lead Length After Component Removal	-	11
L1	Lead Wire Enclosure	2.50	-
P	Feed Hole Pitch	12.50	12.90
P1	Center Of Seating Plane Location	5.95	6.75
P2	4 Feed Hole Pitch	50.30	51.30
T	Over All Tape Thickness	-	0.55
T1	Total Taped Package Thickness	-	1.42
T2	Carrier Tape Thickness	0.36	0.68
W	Tape Width	17.50	19.00
W1	Adhesive Tape Width	5.00	7.00
-	20 pcs Pitch	253	255

Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

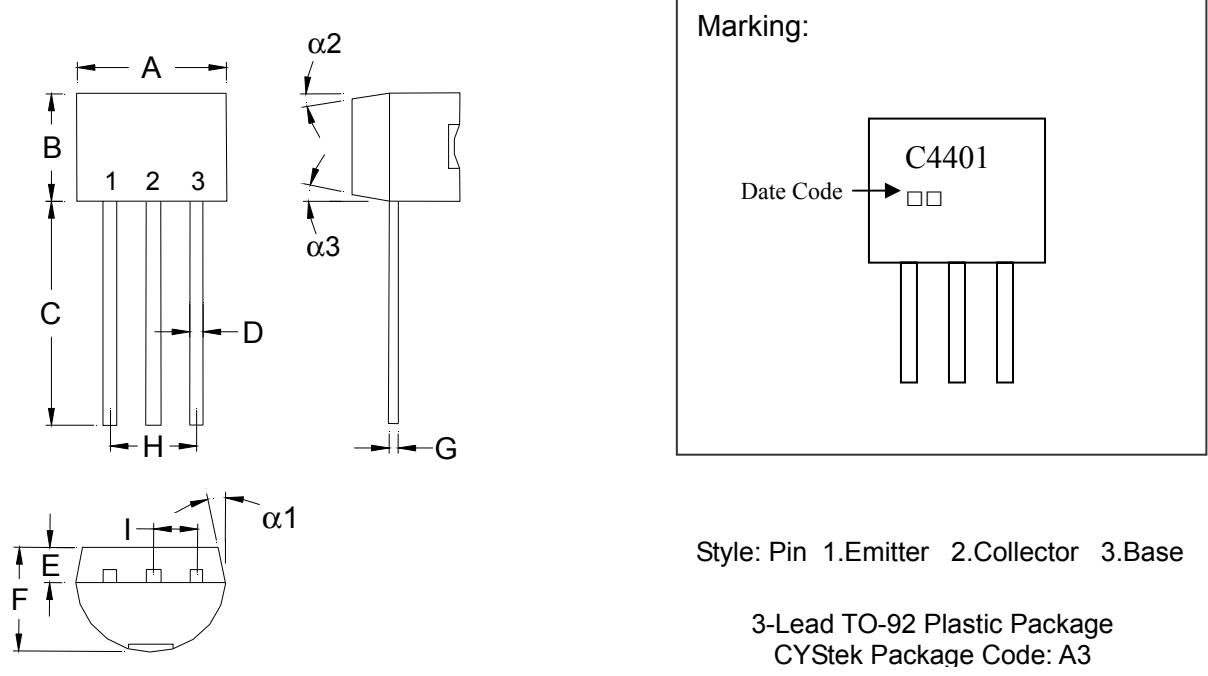
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-92 Dimension



Marking:

C4401
Date Code → □□

Style: Pin 1. Emitter 2. Collector 3. Base

3-Lead TO-92 Plastic Package
 CYStek Package Code: A3

*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1704	0.1902	4.33	4.83	G	0.0142	0.0220	0.36	0.56
B	0.1704	0.1902	4.33	4.83	H	-	*0.1000	-	*2.54
C	0.5000	-	12.70	-	I	-	*0.0500	-	*1.27
D	0.0142	0.0220	0.36	0.56	$\alpha 1$	-	*5°	-	*5°
E	-	*0.0500	-	*1.27	$\alpha 2$	-	*2°	-	*2°
F	0.1323	0.1480	3.36	3.76	$\alpha 3$	-	*2°	-	*2°

Notes: 1. Controlling dimension: millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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