

# Dual P-Channel MOSFET

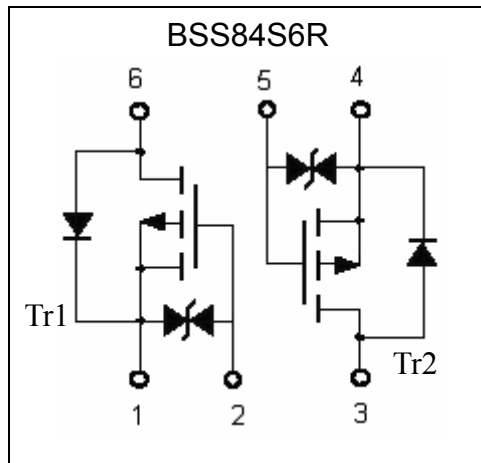
## BSS84S6R

$BV_{DSS}$	-50V
$I_D$	-170mA
$R_{DSON}@V_{GS}=-10V, I_D=-100mA$	5 $\Omega$ (typ)
$R_{DSON}@V_{GS}=-5V, I_D=-100mA$	6 $\Omega$ (typ)
$R_{DSON}@V_{GS}=-3V, I_D=-30mA$	8 $\Omega$ (typ)

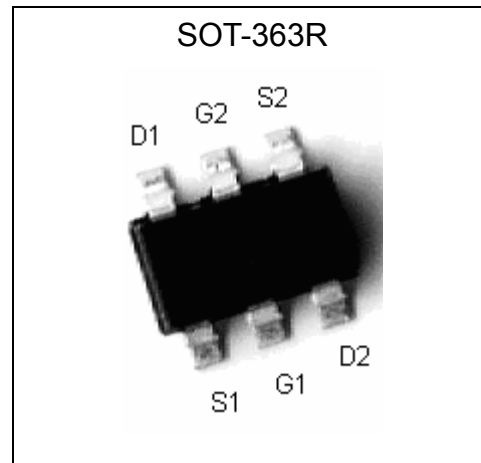
### Features

- Low on-resistance
- High ESD capability
- High speed switching
- Low-voltage drive(-2.5V)
- Pb-free package

### Equivalent Circuit



### Outline



The following characteristics apply to both Tr1 and Tr2

### Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-Source Voltage	$V_{DS}$	-50	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$		
Continuous Drain Current @ $T_A=25^\circ C, V_{GS}=-5V$ (Note 3)	$I_D$	-170	mA	
Continuous Drain Current @ $T_A=85^\circ C, V_{GS}=-5V$ (Note 3)		-120		
Pulsed Drain Current (Notes 1, 2)	$I_{DM}$	-800	mA	
Maximum Power Dissipation (Note 3)	$P_D$	$T_A=25^\circ C$	300	mW
		$T_A=85^\circ C$	160	
Operating Junction and Storage Temperature	$T_j, T_{stg}$	-55~+150	$^\circ C$	

- Note : 1. Pulse width limited by maximum junction temperature.  
 2. Pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .  
 3. Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board,  $t \leq 5s$ .



**Thermal Performance**

Parameter	Symbol	Limit	Unit
Thermal Resistance, Junction-to-Ambient(PCB mounted) (Note)	Rth,ja	415	°C/W

Note : Surface mounted on 1 in<sup>2</sup> copper pad of FR-4 board, t≤5s.

**Electrical Characteristics (Tj=25°C, unless otherwise noted)**

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
<b>Static</b>					
BV <sub>DSS</sub>	-50	-	-	V	V <sub>GS</sub> =0, I <sub>D</sub> =-250μA
V <sub>GS(th)</sub>	-1	-1.4	-2	V	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA
I <sub>GSS</sub>	-	-	±8	μA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0
I <sub>DSS</sub>	-	-	1		V <sub>DS</sub> =50V, V <sub>GS</sub> =0
	-	-	10	V <sub>DS</sub> =40V, V <sub>GS</sub> =0 (Tj=70°C)	
*R <sub>DS(ON)</sub>	-	5	7	Ω	V <sub>GS</sub> =-10V, I <sub>D</sub> =-100mA
	-	6	8.5		V <sub>GS</sub> =-5V, I <sub>D</sub> =-100mA
	-	8	12		V <sub>GS</sub> =-3V, I <sub>D</sub> =-30mA
*G <sub>FS</sub>	80	-	-	mS	V <sub>DS</sub> =-10V, I <sub>D</sub> =-100mA
<b>Dynamic</b>					
C <sub>iss</sub>	-	24	-	pF	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0, f=1MHz
C <sub>oss</sub>	-	4.6	-		
C <sub>rss</sub>	-	1.5	-		
t <sub>d(ON)</sub>	-	2.7	-	ns	V <sub>DS</sub> =-25V, I <sub>D</sub> =-100mA, V <sub>GS</sub> =-5V, R <sub>G</sub> =3.3Ω
t <sub>r</sub>	-	3.3	-		
t <sub>d(OFF)</sub>	-	7.4	-		
t <sub>f</sub>	-	5	-		
Q <sub>g</sub>	-	1.4	-	nC	V <sub>DS</sub> =-40V, I <sub>D</sub> =-170mA, V <sub>GS</sub> =-5V
Q <sub>gs</sub>	-	0.36	-		
Q <sub>gd</sub>	-	0.29	-		
<b>Source-Drain Diode</b>					
*V <sub>SD</sub>	-	-0.85	-1.2	V	V <sub>GS</sub> =0V, I <sub>S</sub> =-130mA

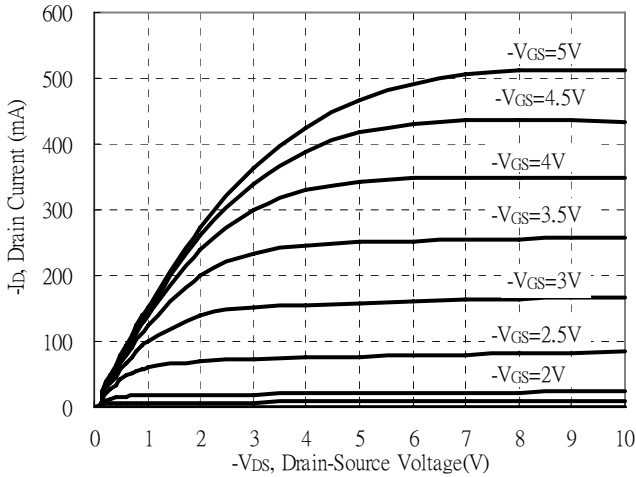
\*Pulse Test : Pulse Width ≤300μs, Duty Cycle ≤2%

**Ordering Information**

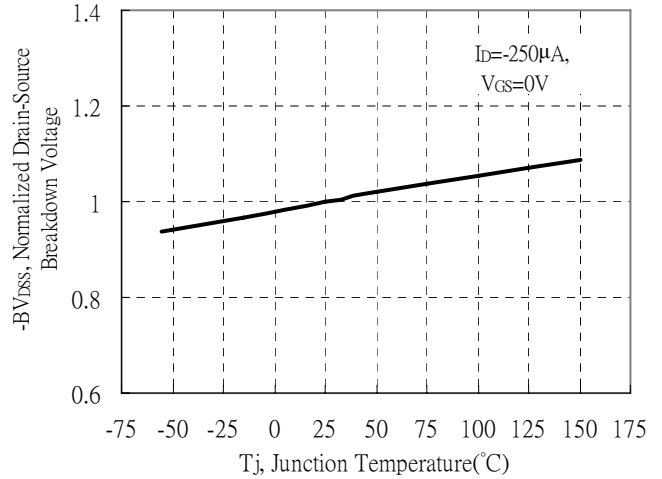
Device	Package	Shipping
BSS84S6R-0-T1-G	SOT-363 (Pb-free lead plating and halogen-free package)	3000 pcs / Tape & Reel

## Typical Characteristics

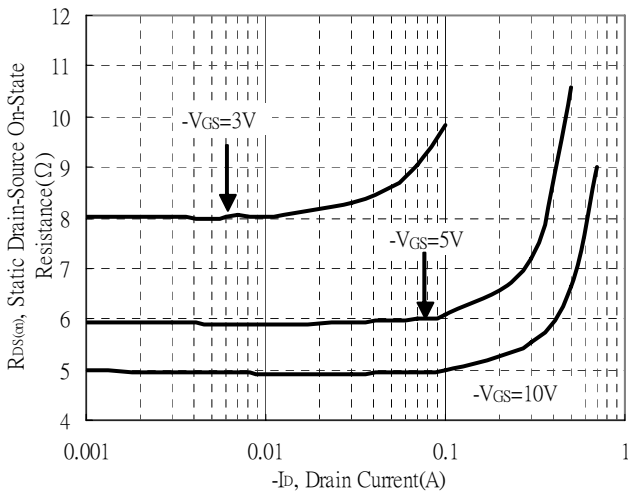
Typical Output Characteristics



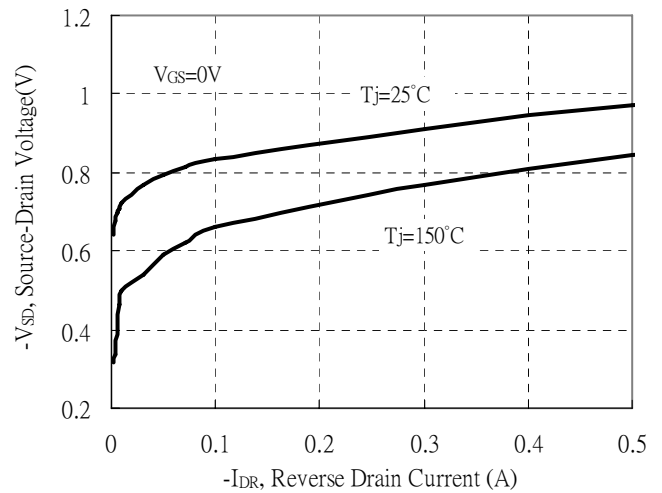
Breakdown Voltage vs Ambient Temperature



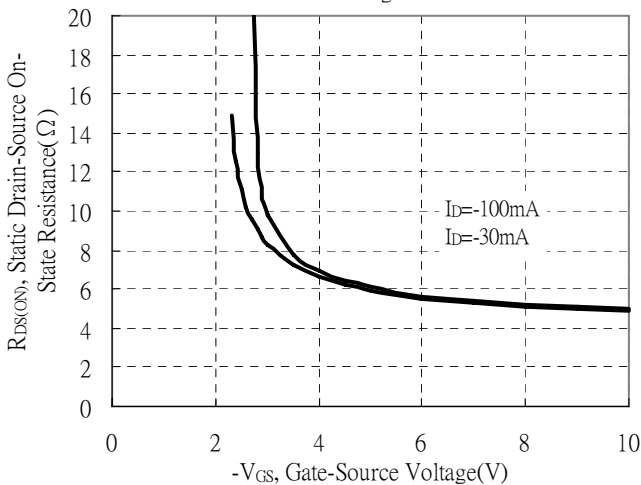
Static Drain-Source On-State resistance vs Drain Current



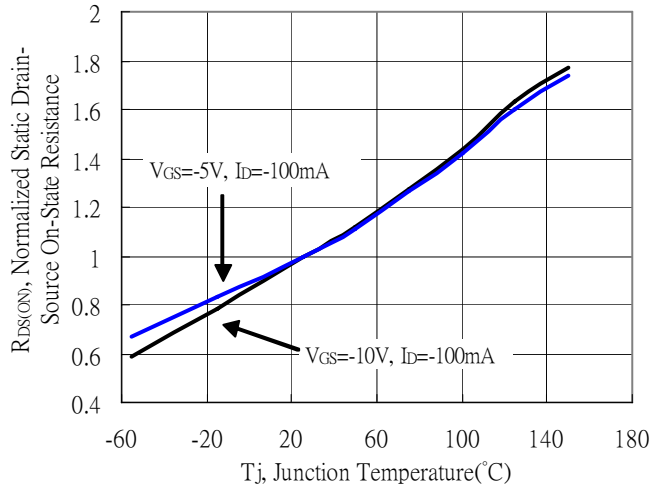
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

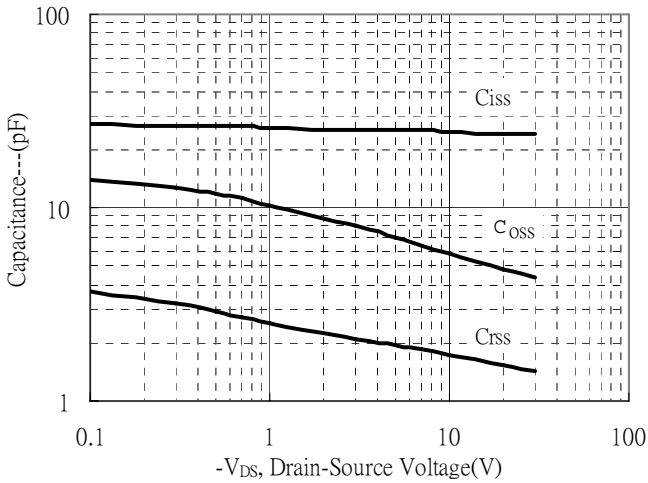


Drain-Source On-State Resistance vs Junction Temperature

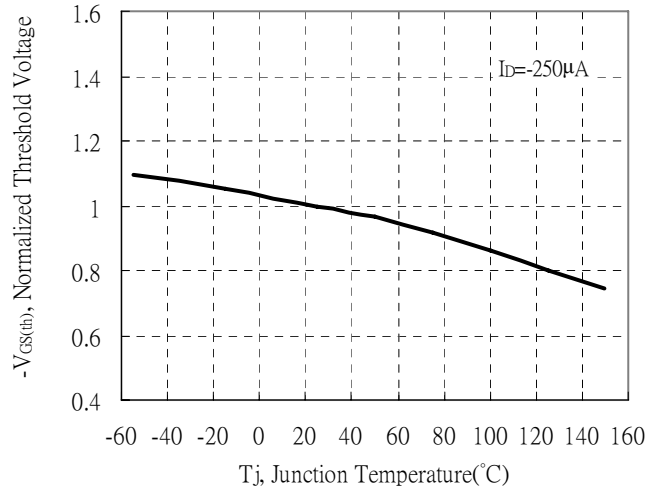


**Typical Characteristics(Cont.)**

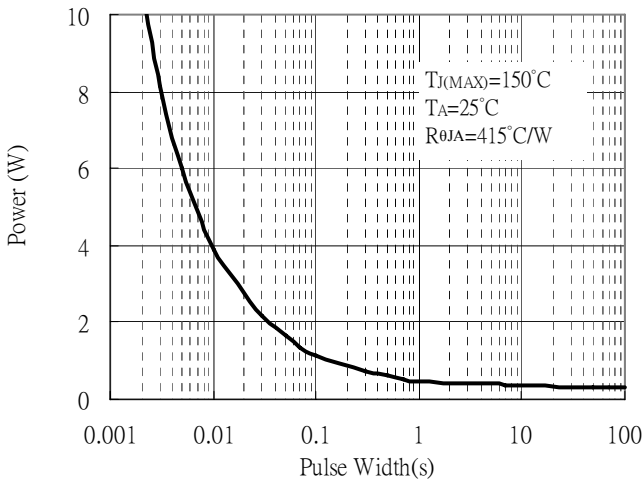
Capacitance vs Drain-to-Source Voltage



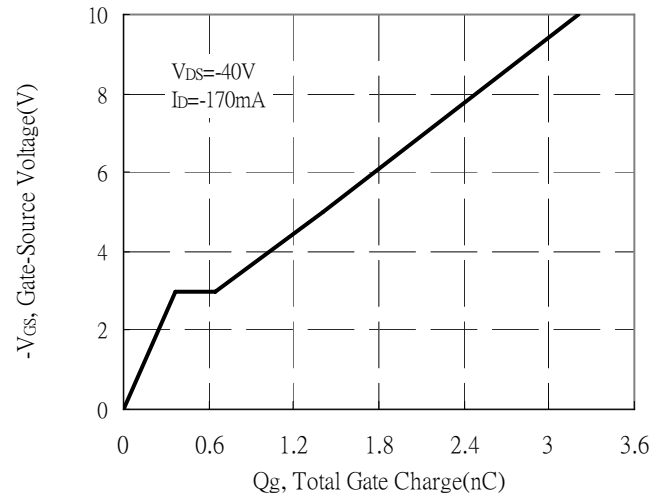
Threshold Voltage vs Junction Temperature



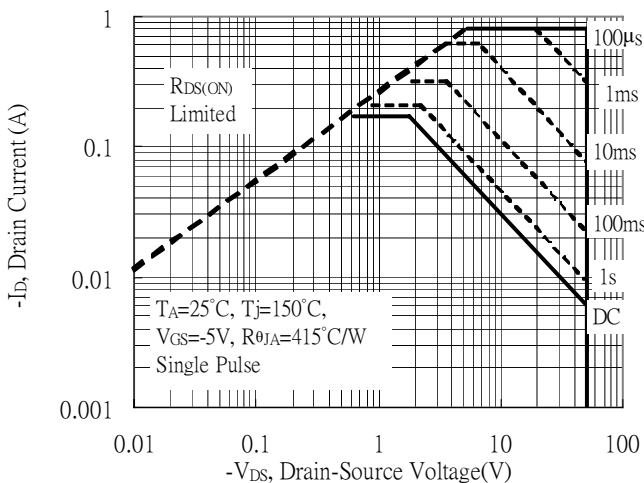
Single Pulse Power Rating, Junction to Ambient  
 (Note on page 1)



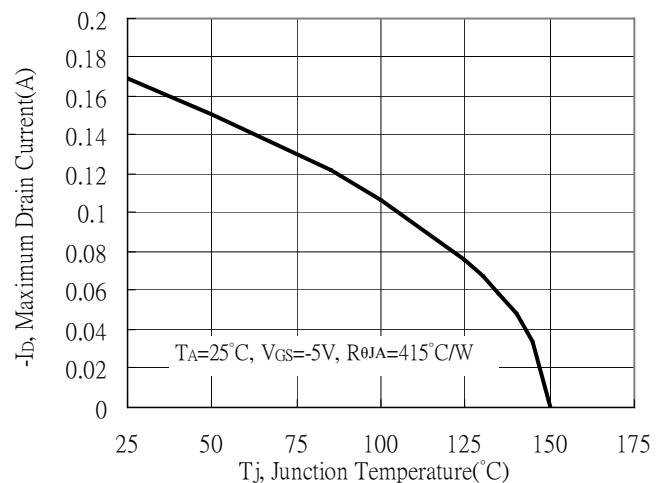
Gate Charge Characteristics



Maximum Safe Operating Area



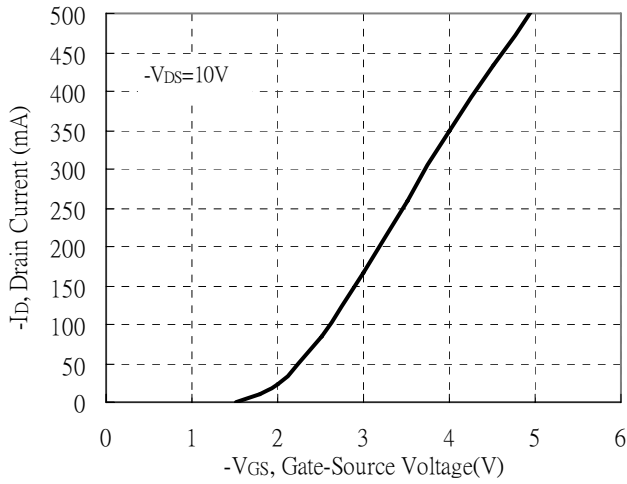
Maximum Drain Current vs Junction Temperature



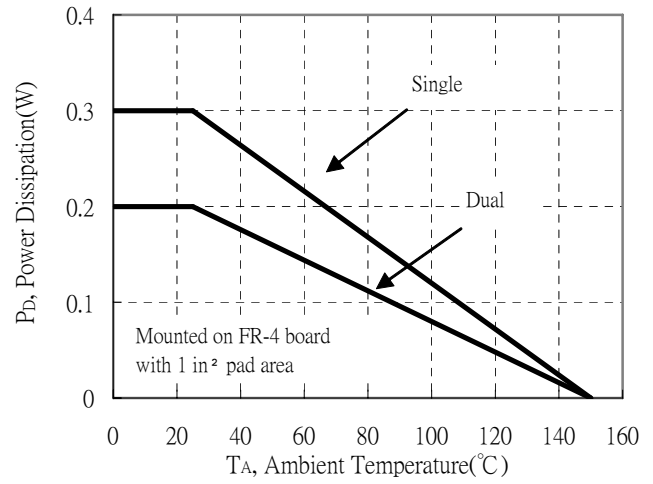


**Typical Characteristics(Cont.)**

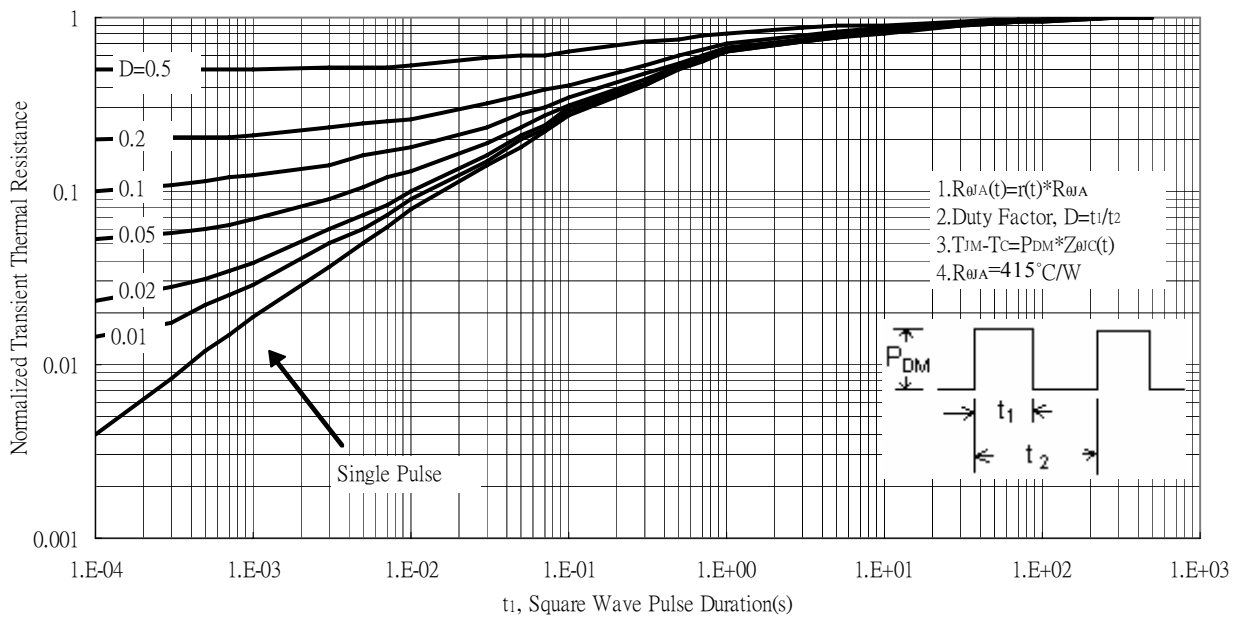
Typical Transfer Characteristics



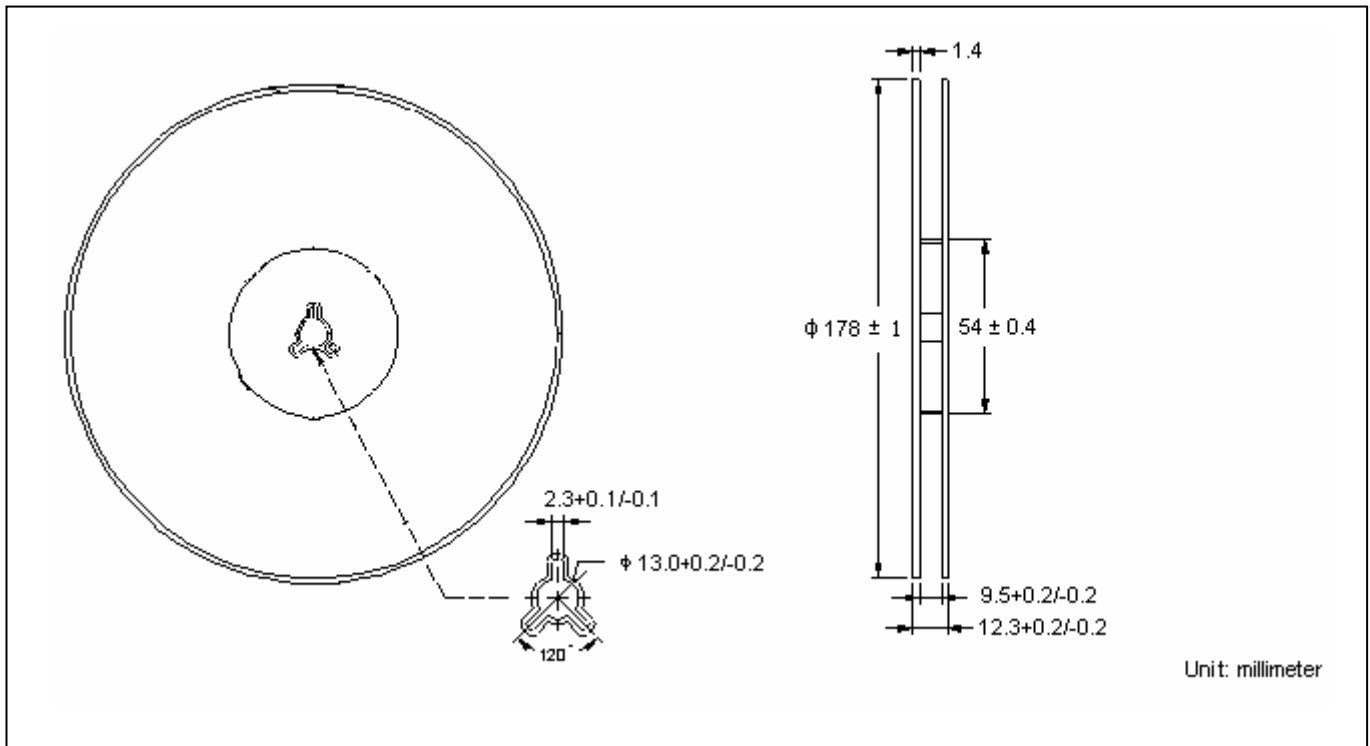
Power Derating Curves



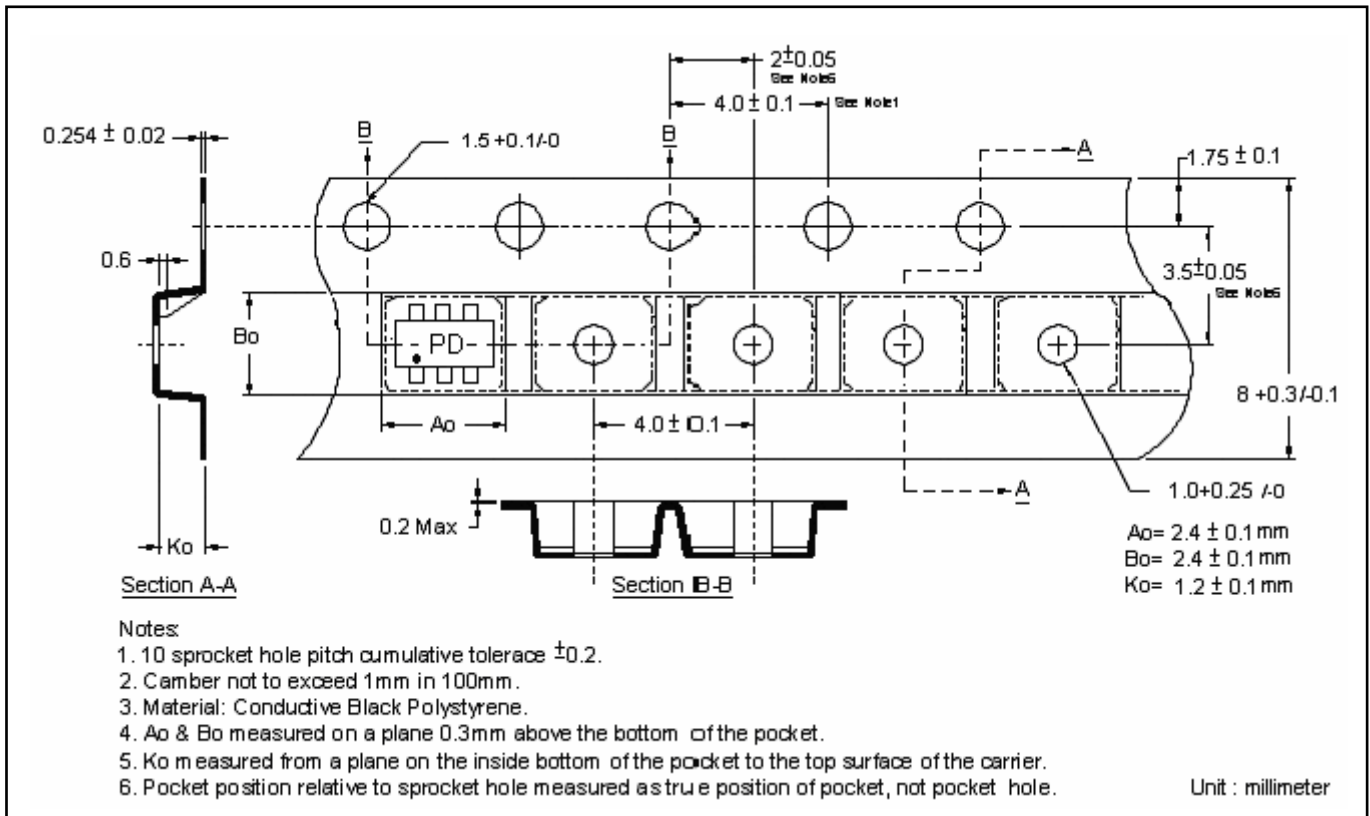
Transient Thermal Response Curves



**Reel Dimension**



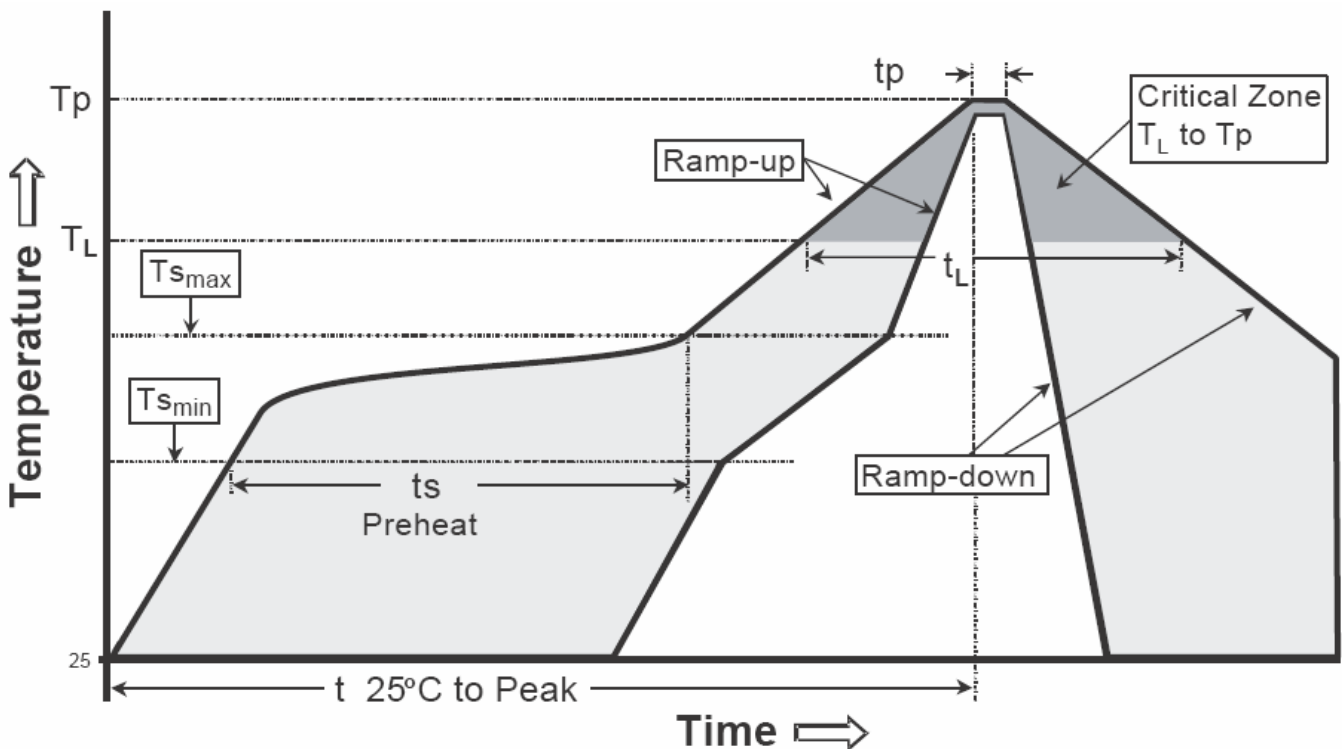
**Carrier Tape Dimension**



**Recommended wave soldering condition**

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

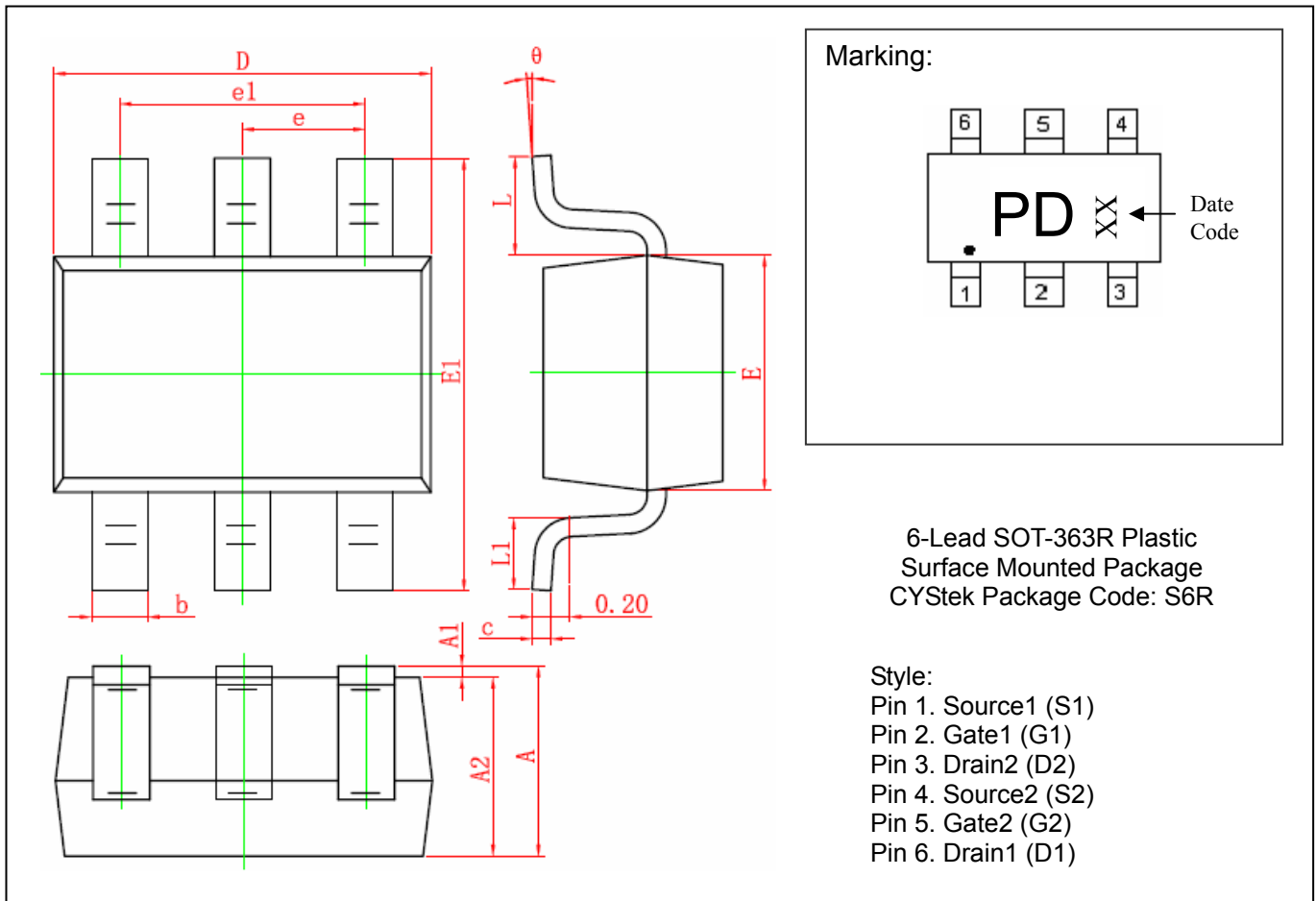
**Recommended temperature profile for IR reflow**



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T <sub>smax</sub> to T <sub>p</sub> )	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T <sub>s min</sub> )	100°C	150°C
-Temperature Max(T <sub>s max</sub> )	150°C	200°C
-Time(t <sub>s min</sub> to t <sub>s max</sub> )	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60-150 seconds	60-150 seconds
Peak Temperature(T <sub>p</sub> )	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

**SOT-363 Dimension**



DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043	E1	2.150	2.450	0.085	0.096
A1	0.000	0.100	0.000	0.004	e	0.650	TYP	0.026	TYP
A2	0.900	1.000	0.035	0.039	e1	1.200	1.400	0.047	0.055
b	0.150	0.350	0.006	0.014	L	0.525	REF	0.021	REF
c	0.080	0.150	0.003	0.006	L1	0.260	0.460	0.010	0.018
D	2.000	2.200	0.079	0.087	theta	0°	8°	0°	8°
E	1.150	1.350	0.045	0.053					

**Notes :** 1.Controlling dimension : millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material :**

- Lead : Pure tin plated.
- Mold Compound : Epoxy resin family, flammability solid burning class:UL94V-0.

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